## Worksheet-2 Solution

(From Lecture 2 given on 01/09/2019)

#### Q<sub>1</sub>

 Construct a Control Flow Graph for the IR (Intermediate Representation) segment shown below, and draw it on the right of the IR. Each vertex can be a single IR instruction, or a basic block containing of a straight-line sequence of multiple IR instructions.

```
assign s, 0
     assign a, 4
      assign i, 0
// branch if (arg1 != arg2)
     brneq k, 0, label0
     assign, b, 1
     goto, label1
7 label0:
     assign, b, 2
   label1:
   label2:
// branch if (arg1 >= arg2)
11
     brgeq, i, n, label3
12
     mult, _t1 a, b
     add, _t2, s, _t1
     assign s, _t2
14
15
     add _t3, i, 1
16
     assign i, _t3
     goto, label2
18 label3:
19
     call, printi, s
20
      call, printi, a
```

## Two answers possible for Q1

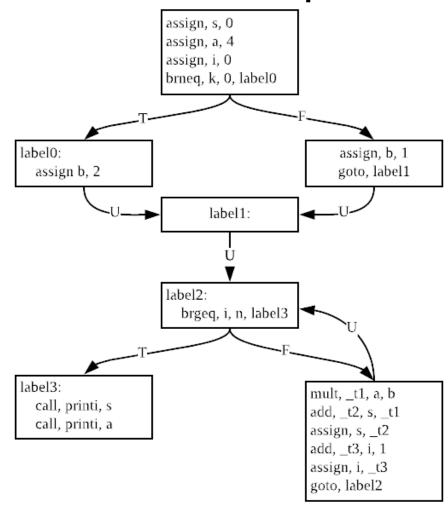
- It is a <u>design choice</u> whether **label**s should be considered as "no-op IR instructions" in CFG vertices, or should be excluded from CFG vertices
  - Just like basic block granularity (minimal vs. maximal) is a design choice when implementing CFGs
- Resulting Control Flow Graphs can be slightly different, depending on the assumption made for labels.
- Most students answered correctly in either case.

#### Q1 Sample Solution1: labels are not instructions

```
assign, s, 0
      assign s, 0
                                                                  assign, a, 4
      assign a, 4
                                                                  assign, i, 0
      assign i, 0
                                                                  brneg, k, 0, label0
 // branch if (arg1 != arg2)
      brneq k, 0, label0
      assign, b, 1
      goto, label1
   label0:
                                                 assign b, 2
                                                                                             assign, b, 1
      assign, b, 2
    label1:
    labe12:
// branch if (arg1 >= arg2)
                                                                    brgeq, i, n, label3
      brgeq, i, n, label3
11
      mult, _t1 a, b
12
      add, _t2, s, _t1
      assign s, _t2
      add _t3, i, 1
15
                                                                                        mult, _t1, a, b
                                                call, printi, s
      assign i, _t3
16
                                                                                        add, _t2, s, _t1
                                                call, printi, a
      goto, label2
17
                                                                                        assign, s, _t2
   label3:
18
                                                                                        add, _t3, i, 1
19
      call, printi, s
                                                                                        assign, i, _t3
      call, printi, a
20
                                                                                        goto, label2
```

### Q1 Sample Solution2: labels are no-op instructions

```
assign s, 0
1
     assign a, 4
     assign i, 0
// branch if (arg1 != arg2)
     brneq k, 0, label0
     assign, b, 1
     goto, label1
  label0:
     assign, b, 2
   label1:
   labe12:
  branch if (arg1 >= arg2)
     brgeq, i, n, label3
11
     mult, _t1 a, b
12
     add, _t2, s, _t1
13
     assign s, _t2
14
     add _t3, i, 1
15
     assign i, _t3
16
     goto, label2
17
   label3:
18
19
     call, printi, s
     call, printi, a
20
```



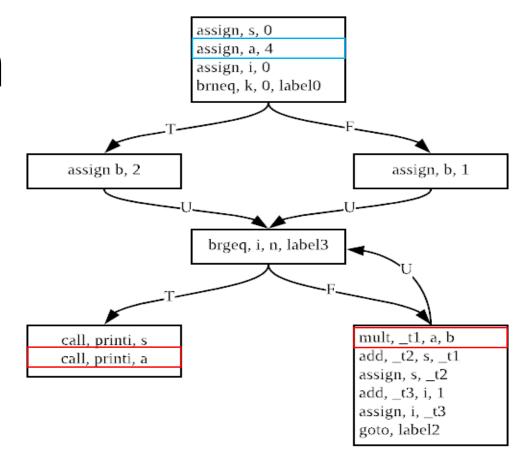
## Q2, Q3

- Q2
   Which uses of a are reached by the def of a in line 2?
- Q3
   Which defs of **b** reach the use of **b** in line 12?

```
assign s, 0
    assign a, 4
     assign i, 0
// branch if (arg1 != arg2)
     brneq k, 0, label0
    assign, b, 1
     goto, label1
7 label0:
     assign, b, 2
   label1:
10 label2:
// branch if (arg1 >= arg2)
11
     brgeq, i, n, label3
12
     mult, _t1 a, b
    add, _t2, s, _t1
13
    assign s, _t2
14
15
   add _t3, i, 1
    assign i, _t3
16
17
     goto, label2
18 label3:
19
     call, printi, s
     call, printi, a
20
```

## Q2 Solution

```
assign s, 0
      assign a, 4
      assign i, 0
   branch if (arg1 != arg2)
      brneq k, 0, label0
      assign, b, 1
      goto, label1
    label0:
      assign, b, 2
    label1:
   label2:
  branch if (arg1 >= arg2)
      brgeq, i, n, label3
11
12
      mult, _t1 a, b
      add, _t2, s, _t1
13
     assign s, _t2
14
      add _t3, i, 1
15
      assign i, _t3
16
      goto, label2
17
    label3:
18
      call, printi, s
19
      call, printi, a
20
```



In line 2, variable 'a' is defined as 4. The definition of 'a' in line 2 reaches line 12 & 20 without any intervening 'def of a'.

# Q3 Solution assign s, 0 assign a, 4

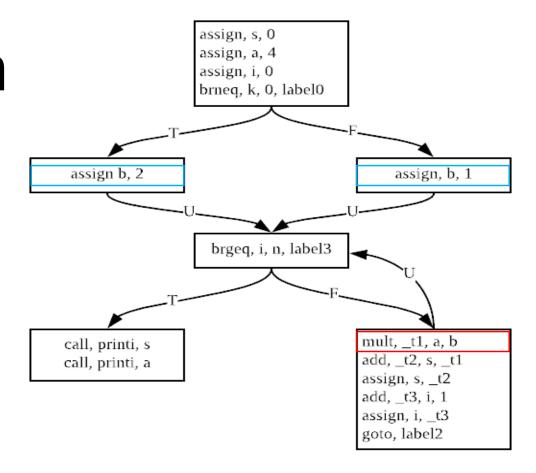
```
assign a, 4
      assign i, 0
    branch if (arg1 != arg2)
      brneq k, 0, label0
      assign, b, 1
      goto, label1
    label0:
      assign, b, 2
    label1:
    label2:
  branch if (arg1 >= arg2)
      brgeq, i, n, label3
      mult, _t1 a, b
      add, _t2, s, _t1
13
      assign s, _t2
14
      add _t3, i, 1
15
16
      assign i, _t3
      goto, label2
17
    label3:
18
```

call, printi, s

call, printi, a

19

20



There is no intervening 'def of b' in the control flow between line 5 and line 12. Same for line 8 and line 12. The defs of b in lines **5 & 8** both reach the use of b in line 12.

## Comments about Q2, Q3

- Most students answered these questions correctly.
- Some students were not sure about the meaning of the terms 'def' and 'use'.

#### <u>'def'</u> :

a write operation on a variable (short for "definition")

#### <u>'use'</u> :

a read operation on a variable

CS 4240 : Compilers & Interpreters, Spring 2019