PixelStream:PixStream

memory

1'h0 — CLR1
24'h0 — DATAIN[23..0]
1'h1 — ENA1   DATAOUT[23..0] — pixel[23..0]
address[15..0] — RADDR[15..0]
16'h0 — WADDR[15..0]
1'h0 — WE

SYNC_RAM