

All

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| S | W | Job ↓ | Last Success | Last Failure | Last Duration | Console | |
|--|---|-------------|-------------------------------------|-------------------------------------|---------------|---|---|
|  |  | VHDL-AES | 1 mo 7 days (#29) | 3 mo 4 days (#16) | 32 sec |  |  |
|  |  | VHDL-Bitvis | 21 days (#34) | 1 mo 6 days (#23) | 20 sec |  |  |
|  |  | VHDL-CRC | 3 mo 1 day (#12) | 3 mo 1 day (#11) | 5,5 sec |  |  |
|  |  | VHDL-SHA | N/A | 1 mo 6 days (#4) | 5,4 sec |  |  |