

EE 230-2025 Analog Lab			
Experiment No.	Batch-A (Thursday) Btech Batch	Batch-B (Friday) DD Batch	Experiment
Pre-Requisite Material 2-3 Jan			LTSpice, AFG, DSO manuals & Simulation Exercise
Exp 1	09-Jan	10-Jan	RC time and frequency response Basics of probing the circuit Half wave rectifier Non Inverting (Buffer + amplifier)
Exp 2	16-Jan	17-Jan	Inverting Differentiator circuit Mathematical Equation Solver (LDE) Schmitt Trigger
Exp 3	23-Jan	24-Jan	Center tapped full wave rectifier Precision Rectifier using Opamps Multivibrator (Monostable, Astable)
Exp 4	30-Jan	31-Jan	Simulation based: Log Amplifier, Antilog Amplifier and Square root amplifier
Exp 5	06-Feb	07-Feb	Analog Computation using op-Amps: Square Root Calculation (Implementation)
Exp 6	13-Feb	14-Feb	Non-idealities in OPAMP using TL084 and LM324
	15-Feb		Mid-sem practical exam
Exp 7	20-Feb	21-Feb	Wien Bridge Oscillator and Active Filters
22 Feb -2 March, 2025			Mid-sem week
Exp 8	06-Mar	07-Mar	MOSFET Characterization, CS amplifier and Current mirror (simulation)
Exp 9	20-Mar	21-Mar	CS Amplifier Current mirror design
Exp 10	27-Mar	28-Mar	Differential Amplifier with active load
	03-Apr	04-Apr	OTA designing + negative feedback application
	05-Apr		End-sem practical exam
Exp 11	From April 6 to April 17		Application project (2-weeks)