1). Prove that the average current in a capacitor for a periodic voltage across it is zero. (at stoody etate).

The Period

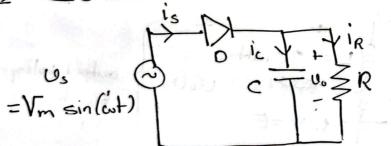
Time Period

2) Prove that the average voltage across an inductor for a periodic current through it is zero. (at stoody li(t) restore)

i(t) restore

i(t) the li(t) in the line of the lin

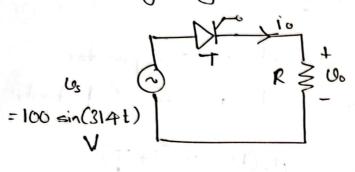
3) Consider the circuit below.



The time constant of the RC circuit is such that the output voltage is becomes y zero just before the diode is forward bicard.

- a) Draw us & find the expression of us(t) for OKKT
- W Drow the woveforms of ir, ic dis
- e) Comment on the nature of is

The firing angle of the Thyristor is  $\alpha = 30^{\circ}$  (after wt=0°)



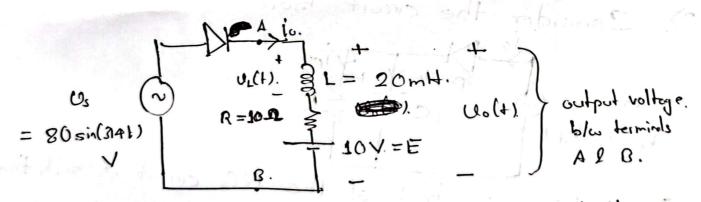
a) Drow the woveforms of Uo(t) L io(t)

RMS value of the output voltage.

c) Find the overage output

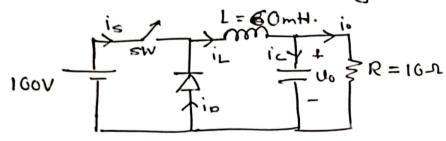
d> Find the time after t=0s in the first cycle at which the SCR should be triggered to get on average output voltage of 23.87 V.

5) Consider the circuit below.



If the overage output voltage is 50V and the dc source at the output is of IOV. R=10\_1. Find the overage output current.

and  $f_s = 2 \text{ kHz}$ , assume that the copaciter is large such that the autput voltage is ripple free.



At steady state.

- 1) Find Wo and overage inductor current (IL)
- 2) Find the overage diode (unent (Io) and Average switch current (Isw). [Hint: Use Power balance]
- 3) Find the ripple conductor current (DJL = Ilmor Ilmin) and also Ilmor and Ilmin.