





Target 1

Project

Project: saregamapadhanisa

- Target 1
 - Source Group 1
 - STARTUP.A51
 - saregamapadhanisa.c
 - at89c5131.h
 - lcd.h
 - serial.h

saregamapadhanisa.c

```
68     TR0 = 1;
69     break;
70     case 'd':      // Dha
71     current_th0 = 0xF6;
72     current_tl0 = 0x3C;
73     lcd_write_string("Dha");
74     TR0 = 1;
75     break;
76     case 'n':      // Ni
77     current_th0 = 0xF7;
78     current_tl0 = 0x52;
79     lcd_write_string("Ni");
80     TR0 = 1;
81     break;
82     case 'a':      // Sa (High)
83     current_th0 = 0xF7;
84     current_tl0 = 0xDD;
85     lcd_write_string("Sa (High)");
86     TR0 = 1;
87     break;
88     case 'q':      // Silence
89     TR0 = 0;
90     speaker = 0;
91     lcd_write_string("Silence");
92     break;
93     default:
94     lcd_write_string("Invalid Key");
95     break;
96     }
97 }
98 }
99 }
```

Build Output

Rebuild started: Project: saregamapadhanisa
Rebuild target 'Target 1'
assembling STARTUP.A51...
compiling saregamapadhanisa.c...
linking...
*** WARNING L16: UNCALLED SEGMENT, IGNORED FOR OVERLAY PROCESS

Simulation

L:27 C:57

CAP. NUM. SCRL OVR. R/W

