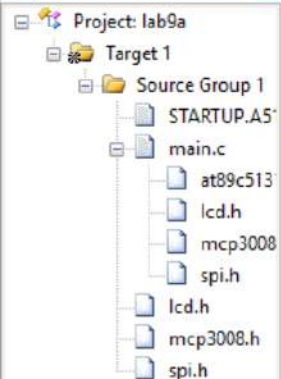




## Project



```

41      (2) Configure SSDIS to disable SSBAR thus
42          P1.1(SSBAR) will be available as standard I/O pin
43
44      (3) Configure clock polarity s.t. sck from uC set to 1 in idle state
45          Refer to SPCON register table no 19-3 from AT89C5131 datasheet
46
47      (4) Configure clock phase i.e. CPHA: Refer SPI timing
48          diagram of ADC IC (figure no. 6-2 in MCP3008 datasheet)
49          first and then table no 19-3, figure no. 19-4 to 19-6 in AT89C5131 datasheet
50          to choose appropriate CPHA value
51      (5) Fclk/16 & Fclk Periph=12MHz ,thus BAUD RATE=750KHz
52
53      (6) Enable SPI interrupt (i.e. configure IEN1 and EA)
54      (7) Enable SPI module (i.e. configure SPCON )
55      */
56
57
58      // SPCON = 0X1F;
59      // IEN1|= 0X04 ;           //enable spi interrupt IEN1 |= 0x__
60      // EA= 1 ;                //enable interrupts EA=_;
61      // SPCON|= 0X00;          //Enable spi module
62
63      SPCON = 0X3F;
64      IEN1|= 0X04 ;           //enable spi interrupt IEN1 |= 0x__
65      EA= 1 ;                //enable interrupts EA=_;
66      SPCON|= 0x40;
67  }
68
69  /*****
70      spi_trx():
71
72      Transmits 3 bytes to SPI slave and receive 3 bytes.
73      These 3 bytes are packed into 4 bytes as long datatype.
74  *****/
75  unsigned long int spi_trx(unsigned long int spi_data_tx)

```

## Build Output

```

Program Size: data=46.1 xdata=0 code=1782
creating hex file from ".\Objects\lab9a"...
".\Objects\lab9a" - 0 Error(s), 0 Warning(s).
Build Time Elapsed: 00:00:01

```

