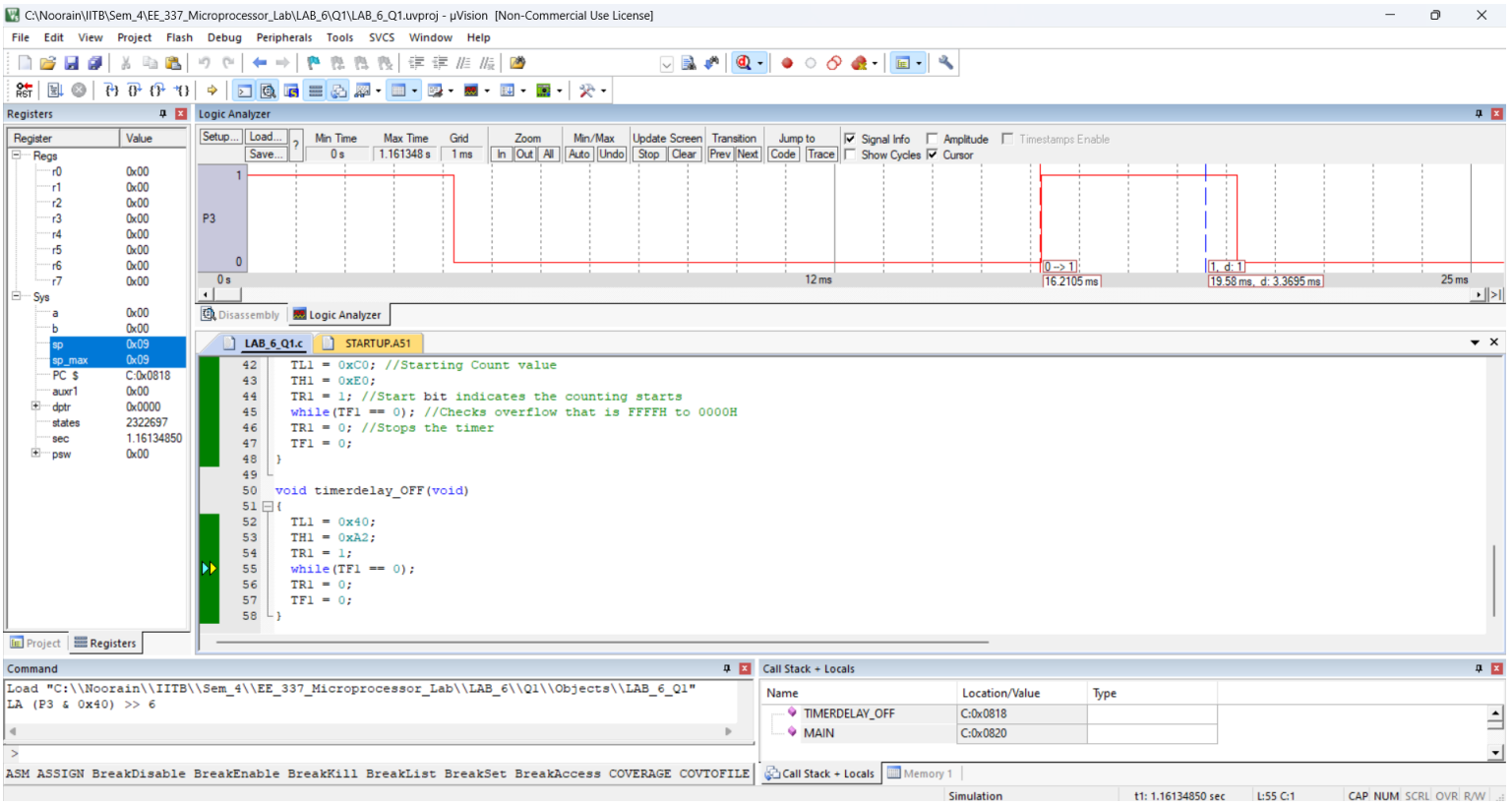
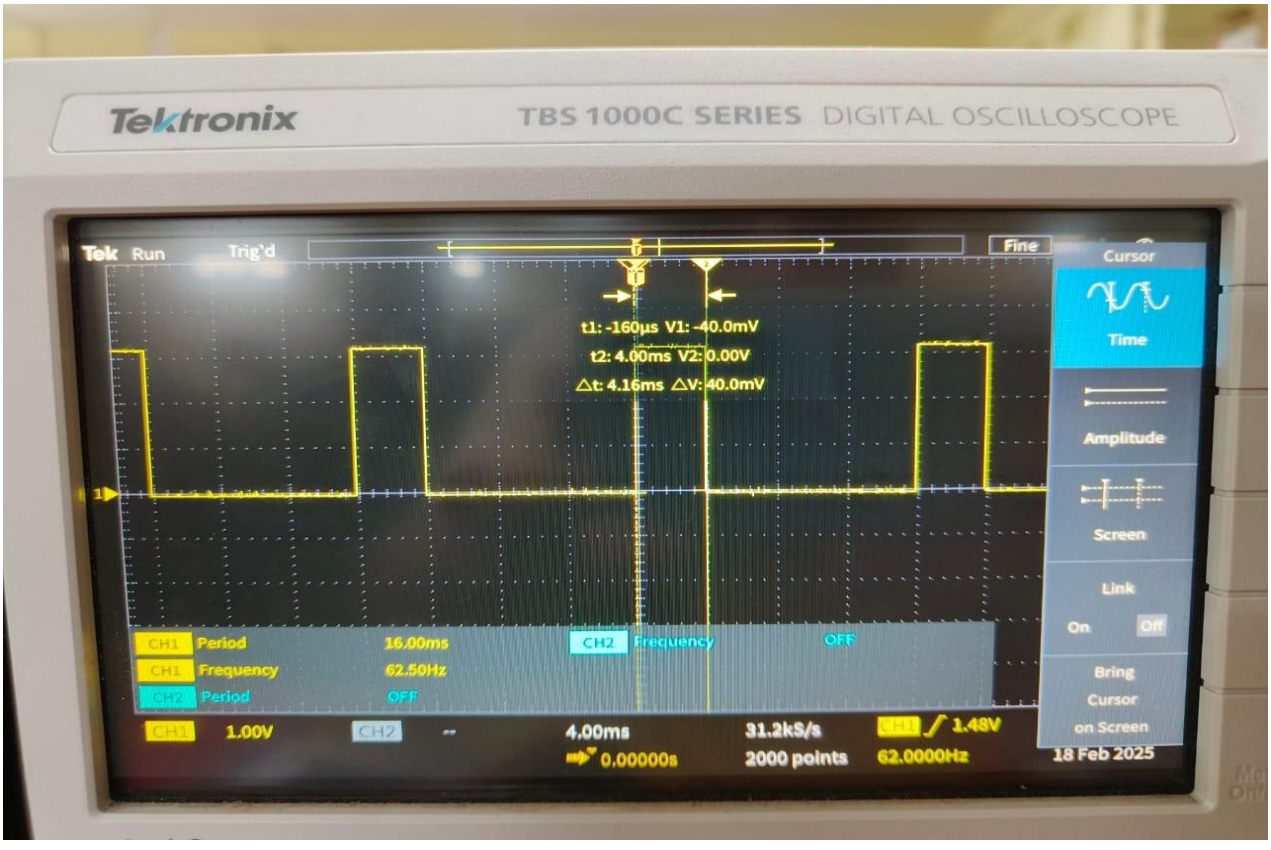
Report Lab 6

Q1:





Q2:

C:\Noorain\IITB\Sem_4\EE_337_Microprocessor_Lab\LAB_6\Q2\LAB_6_Q2.uvproj - µVision [Non-Commercial Use License]

File Edit View Project Flash Debug Peripherals Tools SVCS Window Help

Registers

Register	Value
Regs	
r0	0x00
r1	0x00
r2	0x00
r3	0x00
r4	0x00
r5	0x00
r6	0x00
r7	0x00
Sys	
a	0x00
b	0x00
sp	0x07
sp_max	0x09
PC $	C:0x0819
auxr1	0x00
dptr	0x0000
states	16639782
sec	8.319891...
psw	0x00

Project | Registers

Logic Analyzer

Setup... Load... Save... | Min Time 0.1945 ms | Max Time 8.319891 s | Grid 1 ms | Zoom In Out All | Min/Max Auto Undo | Update Screen Stop Clear | Transition Prev Next | Jump to Code Trace | Signal Info | Amplitude | Timestamps Enable | Show Cycles | Cursor

P3

P3

0.1945 ms | 7.7185 ms | 12.1945 ms | 25.8545 ms, d: 18.136 ms

Disassembly | Logic Analyzer

LAB_6_Q2.c | STARTUP.A51

```
1  #include <at89c5131.h>
2
3  /*
4
5  Two frequency simultaneously with timers in interrupts mode.
6
7  First LED1 time period is 2ms.
8  Second LED2 time period is 3ms.
9
10 */
11
12 sbit LED1=P3^6;
13 sbit LED2=P3^7;
14
15
16 void timer0_isr () interrupt 1
```

Command

```
Running with Code Size Limit: 2K
Load "C:\\Noorain\\IITB\\Sem_4\\EE_337_Microprocessor_Lab\\LAB_6\\Q2\\Objects\\LAB_6_Q2"
LA (P3 & 0x00000040) >> 6
LA (P3 & 0x00000080) >> 7
```

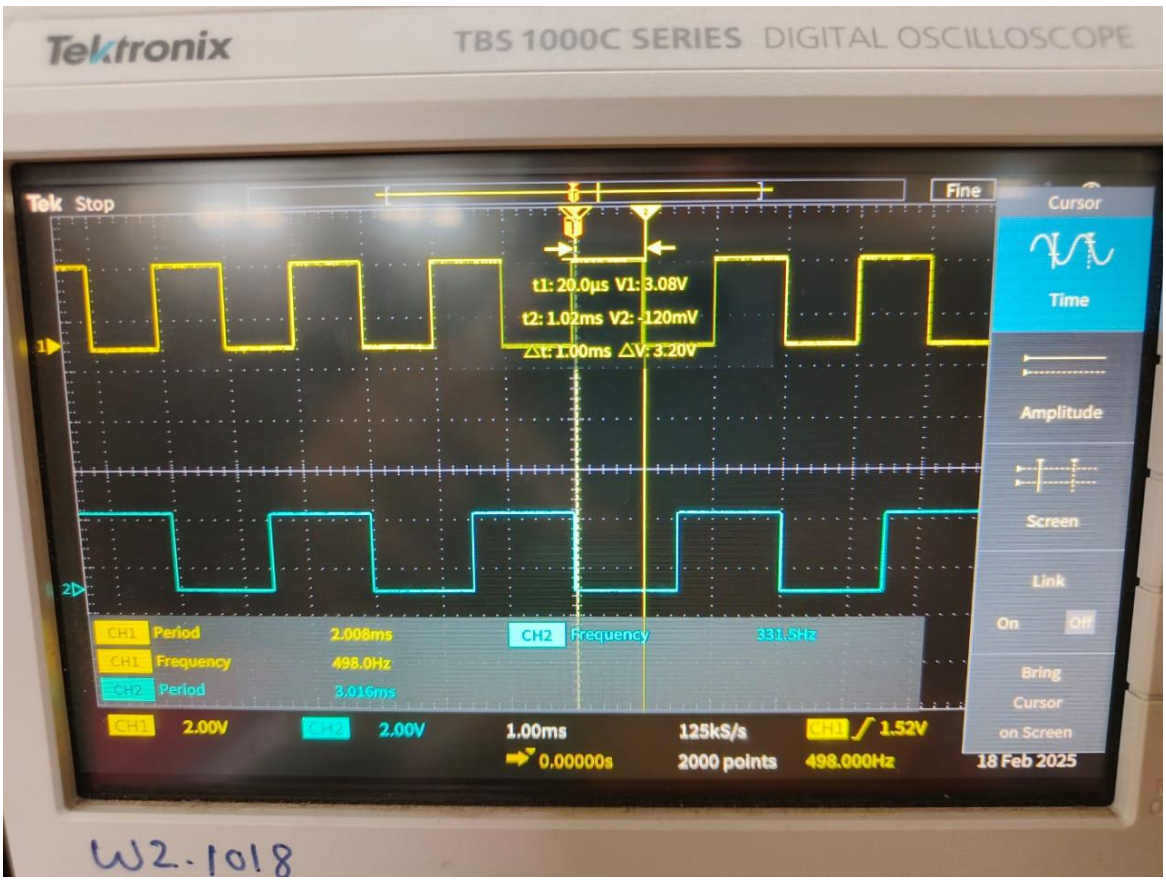
ASM ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE COVTOFILE

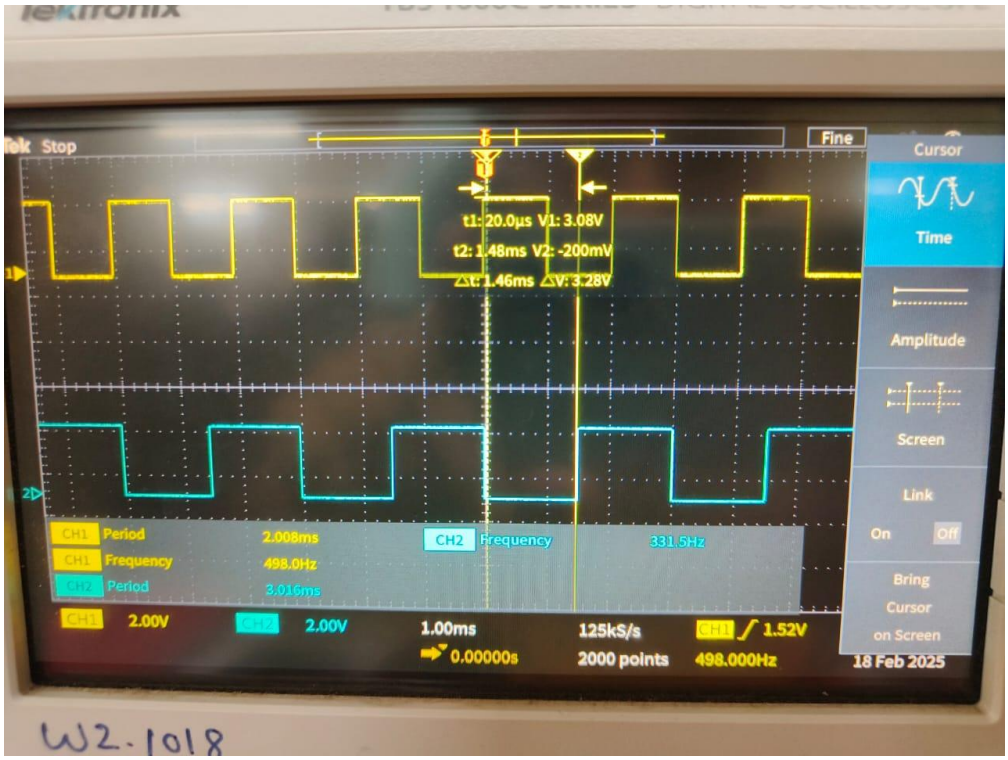
Call Stack + Locals

Name	Location/Value	Type
MAIN	C:0x0800	

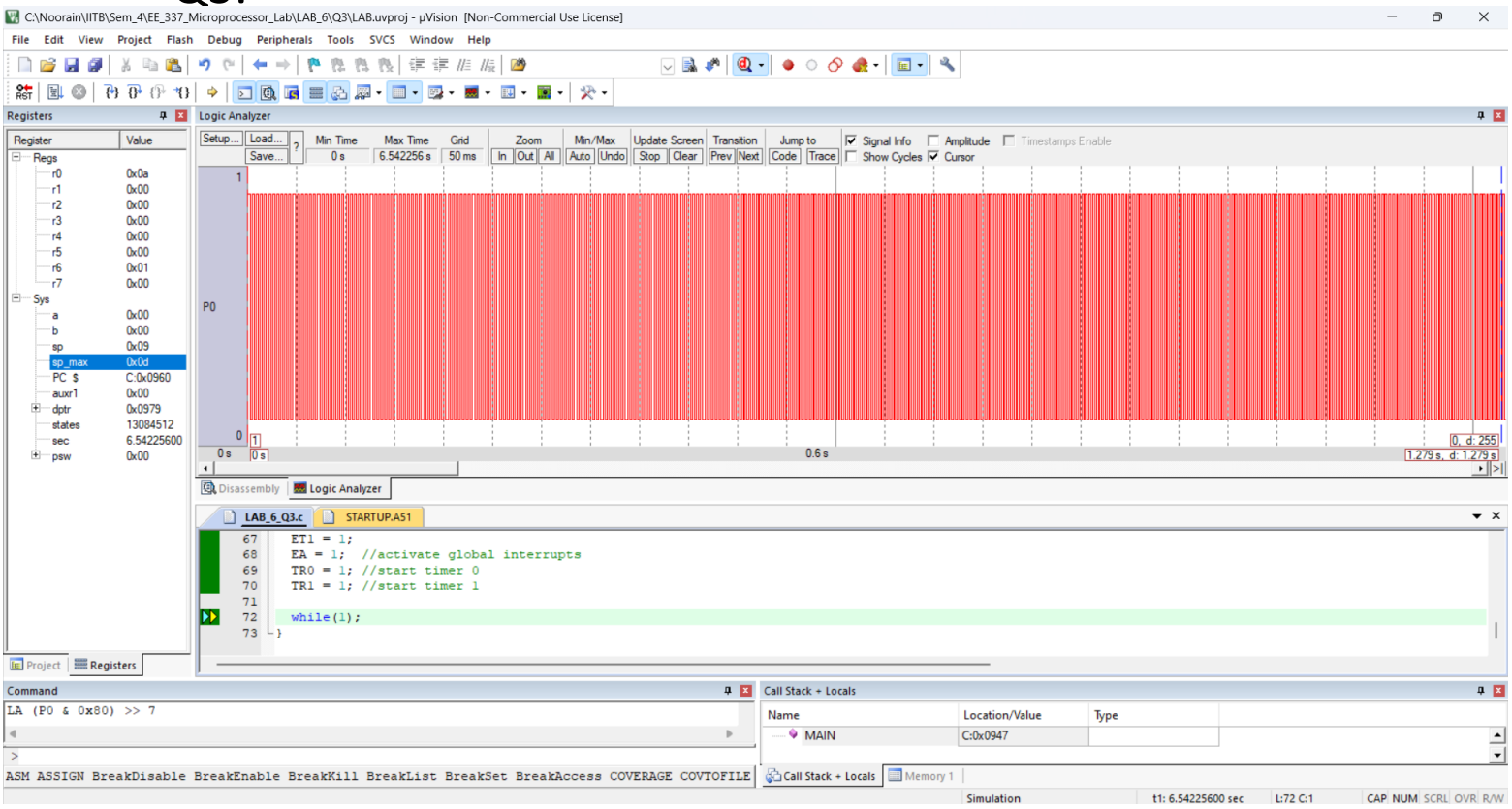
Call Stack + Locals | Memory 1

Simulation | t1: 8.31989100 sec | L:43 C:19 | CAP NUM SCRL OVR R/W





Q3:



For some reason my LAB6 zip was not uploaded on moodle even though I uploaded it so I am including it with the Report