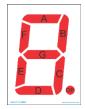
Lab Experiment: Project 1

Circuit Design: Display a name

Using Multisim, design a decoder (like the 7447) that will display each letter of either your first or last name (use 5 letters). A template and worksheet are provided to you as a starting point. There is an example solution on page 3. Your report should describe the design process and must include the Multisim file for your design along with the completed Truth Table and Karnaugh maps.



7 Segment Font



Segment Truth Table Worksheet (the X stands for a Don't Care)

Minterm	Letter	C	В	A	a	b	С	d	e	f	g
0		0	0	0							
1		0	0	1							
2		0	1	0							
3		0	1	1							
4		1	0	0							
5		1	0	1	X	X	X	X	X	X	X
6		1	1	0	X	X	X	X	X	X	X
7		1	1	1	X	X	X	X	X	X	X

Karnaugh Map Worksheets

	\overline{A}		A			\overline{A}
<u></u> <u> </u>	$\overline{C} \overline{B} \overline{A}$ $= 0$		$\overline{C}\overline{B}A=1$	$\overline{C}BA = 3$		$\overline{C} B \overline{A} = 2$
	$C \overline{B} \overline{A} = 4$		$C \overline{B} A = 5$	CBA = 7		$CB\overline{A}=6$
С			X	X		X
	-	В			В	

	\overline{A}		A			Ā
_	$\overline{C}\overline{B}\overline{A}=0$	\overline{C}	$\overline{B}A=1$	$\overline{C}BA = 3$		$\overline{C} B \overline{A} = 2$
<u></u> <u> </u>						
	$C \overline{B} \overline{A} = 4$	С	$\overline{B}A = 5$	CBA = 7		$CB\overline{A}=6$
С		2	X	X		X
	Ī	3			В	

	\overline{A}		A	l			\overline{A}
	$\overline{C}\overline{B}\overline{A}=0$		$\overline{C} \overline{B} A = 1$		$\overline{C}BA = 3$		$\overline{C}B\overline{A}=2$
<u>c</u>							
	$C \overline{B} \overline{A} = 4$		$C\overline{B}A=5$		CBA = 7		$CB\overline{A}=6$
С			X		X		X
	Ī	3				В	

	\overline{A}	A		\overline{A}
	$\overline{C}\overline{B}\overline{A}=0$	$\overline{C}\overline{B}A=1$	$\overline{C}BA = 3$	$\overline{C} B \overline{A} = 2$
<u></u> <u> </u>				
	$C\overline{B}\overline{A}=4$	$C\overline{B}A=5$	CBA = 7	$CB\overline{A}=6$
С		X	X	X
	Ī	3		В

	\overline{A}		A	l			Ā
	$\overline{C}\overline{B}\overline{A}=0$		$\overline{C}\overline{B}A=1$		$\overline{C}BA = 3$		$\overline{C}B\overline{A}=2$
<u></u> <u> </u>							
	$C\overline{B}\overline{A}=4$		$C\overline{B}A=5$		CBA = 7		$CB\overline{A}=6$
С			X		X		X
	Ī	3				В	

	\overline{A}		A			\overline{A}
	$\overline{C}\overline{B}\overline{A}=0$		$\overline{C}\overline{B}A=1$	$\overline{C}BA = 3$		$\overline{C} B \overline{A} = 2$
C						
	$C\overline{B}\overline{A}=4$		$C\overline{B}A=5$	CBA = 7		$CB\overline{A}=6$
С			X	X		X
	Ī	3			В	

	\overline{A}	A		Ā
_	$\overline{C}\overline{B}\overline{A}=0$	$\overline{C}\overline{B}A=1$	$\overline{C}BA = 3$	$\overline{C}B\overline{A}=2$
C				
	$C \overline{B} \overline{A} = 4$	$C\overline{B}A=5$	CBA = 7	$CB\overline{A}=6$
С		X	X	X
	Ī	B		В

Enter your name. If your name is less than 5 letters add any letters or numbers you want to create 5 characters for the display. Using the seven-segment display font determine the display segments for each character. Determine the segment minterms and create the Karnaugh Map for each segment. Determine the segment equations and design and simulate the circuit using Multisym.

Example/Solution:

Minterm	Letter	C	В	A	a	b	c	d	e	f	g
0	A	0	0	0	1	1	1	0	1	1	1
1	S	0	0	1	1	0	1	1	0	1	1
2	S	0	1	0	1	0	1	1	0	1	1
3	Е	0	1	1	1	0	0	1	1	1	1
4	R	1	0	0	0	0	0	0	1	0	1
5	Not used	1	0	1	X	X	X	X	X	X	X
6	Not used	1	1	0	X	X	X	X	X	X	X
7	Not used	1	1	1	X	X	X	X	X	X	X

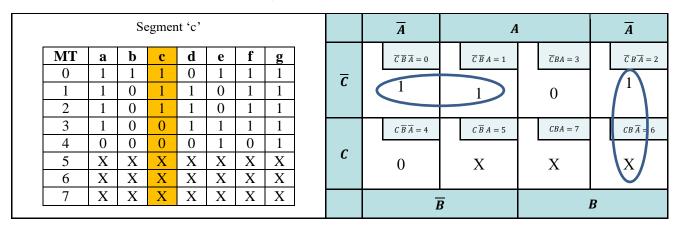
The Karnaugh Maps:

		Se	egmer	nt 'a'							\overline{A}	A	1	Ā
MT	a	b	c	d	e	f	g				$\overline{C}\overline{B}\overline{A}=0$	$\overline{C}\overline{B}A=1$	$\overline{C}BA = 3$	$\overline{C}B\overline{A}=2$
0	1	1	1	0	1	1	1		<u></u> <u> </u>					
1	1	0	1	1	0	1	1		C		1	1	1	1
2	1	0	1	1	0	1	1							
3	1	0	0	1	1	1	1				$C\overline{B}\overline{A}=4$	$C\overline{B}A=5$	CBA = 7	$CB\overline{A}=6$
4	0	0	0	0	1	0	1			L L				
5	X	X	X	X	X	X	X		С		0	X	X	X
6	X	X	X	X	X	X	X							
7 X X X X X X X X											-	<u> </u>		В
											I	3		D

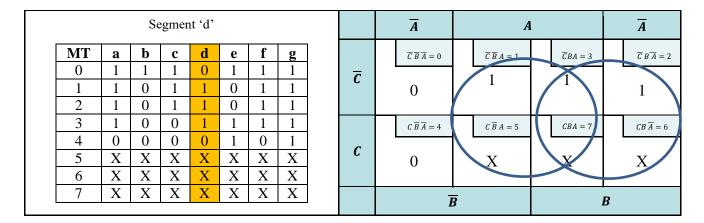
Segment
$$a = \overline{C}$$

			Se	egmer	nt 'b'						\overline{A}	A	1	\overline{A}
	MT	a	b	С	d	e	f	g			$\overline{C}\overline{B}\overline{A}=0$	$\overline{C}\overline{B}A=1$	$\overline{C}BA = 3$	$\overline{C} B \overline{A} = 2$
	0	1	1	1	0	1	1	1		<u>\u03c6</u>				
	1	1	0	1	1	0	1	1		C	(1)	0	0	0
	2	1	0	1	1	0	1	1						
	3	1	0	0	1	1	1	1			$C \overline{B} \overline{A} = 4$	$C\overline{B}A=5$	CBA = 7	$CB\overline{A}=6$
	4	0	0	0	0	1	0	1						
	5	X	X	X	X	X	X	X		С	0	X	X	X
	6	X	X	X	X	X	X	X						
	7	X	X	X	X	X	X	X			-	R		R
Ì												В		В

Segment $b = \overline{A} \overline{B} \overline{C}$



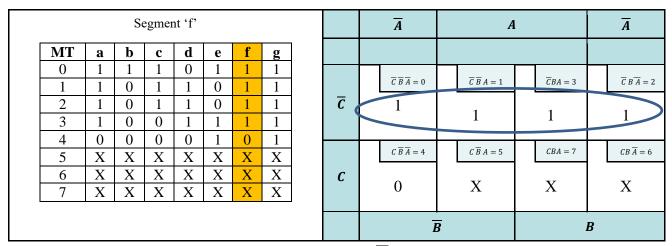
Segment $c = \overline{A} B + \overline{B} \overline{C}$



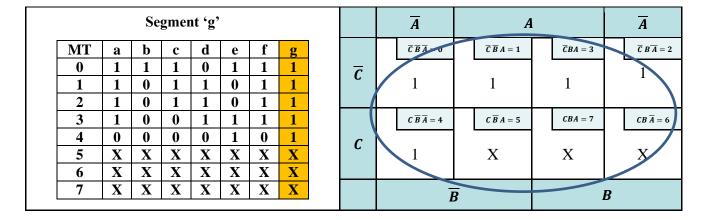
Segment d = A + B

		Se	egmer	nt 'e'						\overline{A} A			Ā
MT	a	b	c	d	e	f	g			$\overline{C}\overline{B}\overline{A}=0$	$\overline{C}\overline{B}A=1$	$\overline{C}BA = 3$	$\overline{C}B\overline{A}=2$
0	1	1	1	0	1	1	1		<u></u> <u></u>	1			
1	1	0	1	1	0	1	1		C	/ 1	0		0
2	1	0	1	1	0	1	1			1 1		1 / 1	
3	1	0	0	1	1	1	1			$C \overline{B} \overline{A} = 4$	$C\overline{B}A=5$	CBA = 7	$CB\overline{A}=6$
4	0	0	0	0	1	0	1		_			\ <u> </u>	
5	X	X	X	X	X	X	X		С	1	X	\ x/	X
6	X	X	X	X	X	X	X						
7	X	X	X	X	X	X	X			7	3		В
											9		D

Segment $e = AB + \overline{A}\overline{B}$

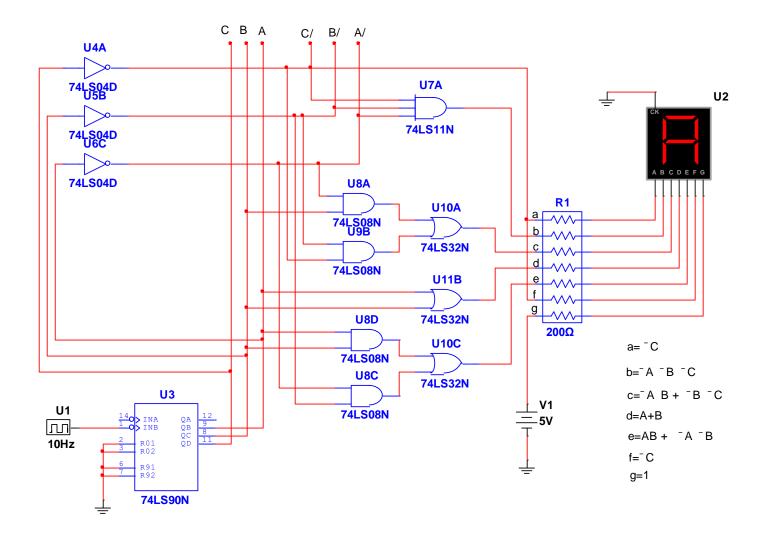


Segment $f = \overline{C}$



Segment g = 1

Multisim Circuit:



------ END PROJECT 1 ------