

GRF5511

High Frequency Power-LNA™ Tuning Range: 0.7 - 8.0 GHz



Features

Reference: 5V/130mA/2.5GHz

Gain: 20.1 dB

OP1dB: 26.1 dBm OIP3: 39.6 dBm

Eval Board NF: 1.5 dB

Reference: 5V/100mA/5.5 GHz

Gain: 15.4 dB Gain - for more accurate gain, you

IIP3 OIP3: 44.0 dBm

Eval Board NF: 1.7 dB NF

Flexible Bias Voltage and Current

Process: GaAs pHEMT

Applications

PA/Driver for 802.11ac

Linear Driver Amplifier for High PAR Waveforms

Multi-stage LNA

Microwave Backhaul

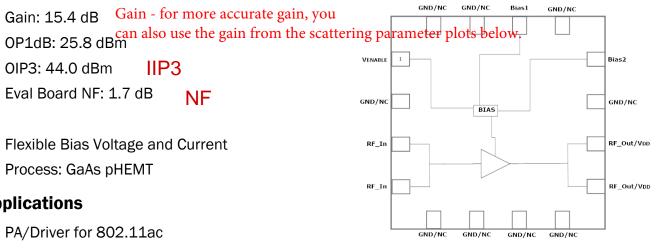
Revision Date: 05/21/20

Product Description

GRF5511 is a high linearity PA/Linear Driver with low noise figure (NF). It delivers excellent P1dB, IP3 and NF over a wide range of frequencies with fractional bandwidths >15% and is well suited to demanding 802.11ac 5 GHz PA/driver applications.

The device offers flexible biasing to achieve optimal linearity and efficiency and it can be tuned over a wide range of frequencies from 700 MHz up to 8 GHz.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device sparameters.



3.0 x 3.0 mm QFN-16



GRF5511

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Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Drain Voltage	V _{DD}		9.0	V
Transient Average RF Input Power CW: (Load VSWR < 2:1; Duration: <1 hour)	P _{IN MAX}		22.0	dBm
Operating Temperature (Package Heat Sink)	Т _{АМВ}	-40	105	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power	P _{DISS MAX}		1.8	W
Electrostatic Discharge:				
Charged Device Model: (TBD)	CDM	1500		V
Human Body Model:	HBM	250		V
Storage:				
Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	



Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF5511 landing page: Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

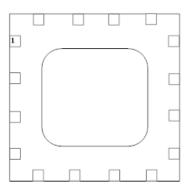
Link to manufacturing note



GRF5511

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Pin Out (Top View)



Pin Assignments:

Pin	Name	Description	Note
1	V _{ENABLE}	Enable Voltage Input	Venable and series resistor set IDDQ. Venable <= 0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.
2	NC	No Connect or Ground	No internal connection to die
3	RF_In	RF Input	Pins 3-4 tied together on system board
4	RF_In	RF Input	Pins 3-4 tied together on system board
5	NC	No Connect or Ground	No internal connection to die
6	NC	No Connect or Ground	No internal connection to die
7	NC	No Connect or Ground	No internal connection to die
8	NC	No Connect or Ground	No internal connection to die
9	RF_Out/VDD	PA Output/Bias	Pins 9-10 tied together on system board. Supply Vdd here.
10	RF_Out/VDD	PA Output/Bias	Pins 9-10 tied together on system board. Supply Vdd here.
11	NC	No Connect or Ground	No internal connection to die
12	Bias2	Bias Circuit Supply	Connect to VDD through external resistor
13	NC	No Connect or Ground	No internal connection to die
14	Bias1	Bias Circuit Ground	Consult application schematic
15	NC	No Connect or Ground	No internal connection to die
16	NC	No Connect or Ground	No internal connection to die
	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Rec-
DV 0 D40=			ommend multiple 8 mil vias beneath the package for optimal RF and
PKG BASE			thermal performance. Refer to evaluation board top layer graphic on
			schematic page.



GRF5511

High Frequency Power-LNA™ **Tuning Range: 0.7 - 8.0 GHz**

Nominal Operating Parameters:

Parameter	Symbol	Specification			Unit	Condition	
Faranieter	Symbol	Min.	Тур.	Max.	Ullit	Condition	
Tune: 2.3 to 2.7 GHz						Bias: 5.0 V and 130 mA unless otherwise noted. (+25C)	
Test Frequency	FTEST		2.5		GHz		
Gain	S(2,1)	19.0	20.1		dB		
Noise Figure (Evaluation Board)	NF		1.5		dB		
Output 1dB Compression Point	OP1dB	24.5	26.0		dBm		
Output Third Order Intercept Point	OIP3		39.6		dBm		
Switching Rise Time	Trise		200		ns		
Switching Fall Time	TFALL		200		ns		
Quiescent Supply Current	IDDQ		130		mA		
Enable Current	I ENABLE		4.0		mA		
Disabled Mode						VDD: 5.0 V; VENABLE: 0.0 V	
Supply Current (Leakage)	I _{DD}		600		uA		
Thermal Data							
Thermal Resistance: (IR Scan Method)	Θјс		44		°C/W		
Channel Temperature @ +85C Reference (package heat sink)	TCHANNEL		114 (See note)		°C	V _{DD} : 5.0 volts; I _{DDQ} : 130 mA P _{DISS} : 0.65 W; No RF	

Note: MTTF >10^6 hours for TCHANNEL < =170 degrees C.

Downwater	Cyroob ol	S	Specification			Condition	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition	
Tune: 5.1 to 5.9 GHz						Bias: 5.0 V and 100 mA unless otherwise noted. (+25C)	
Reference Frequency	Free		5.5		GHz		
Gain	S(2,1)		15.4		dB		
Noise Figure (Evaluation Board)	NF		1.7		dB		
Output 1dB Compression Point	OP1dB		25.8		dBm		
Output Third Order Intercept Point	OIP3		44.0		dBm		
Switching Rise Time	Trise		200		ns		
Switching Fall Time	TFALL		200		ns		
Quiescent Supply Current	IDDQ		100		mA		
Enable Current	I ENABLE		3.0		mA		

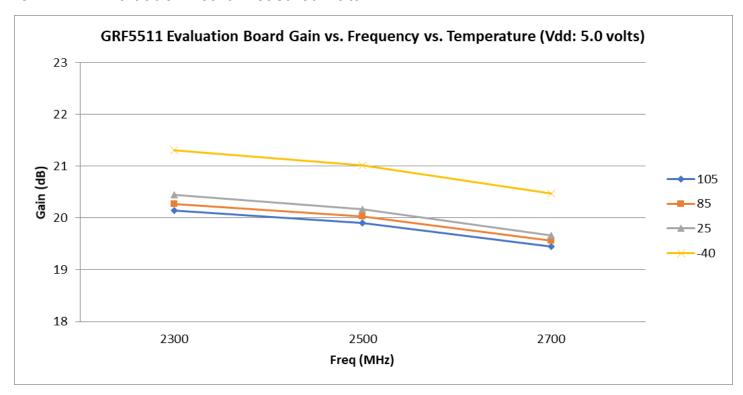


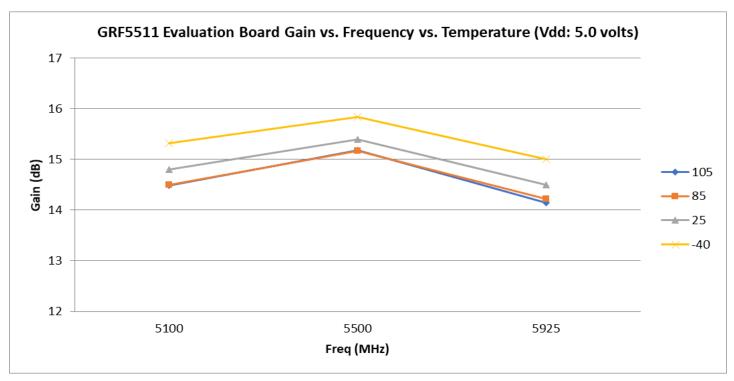
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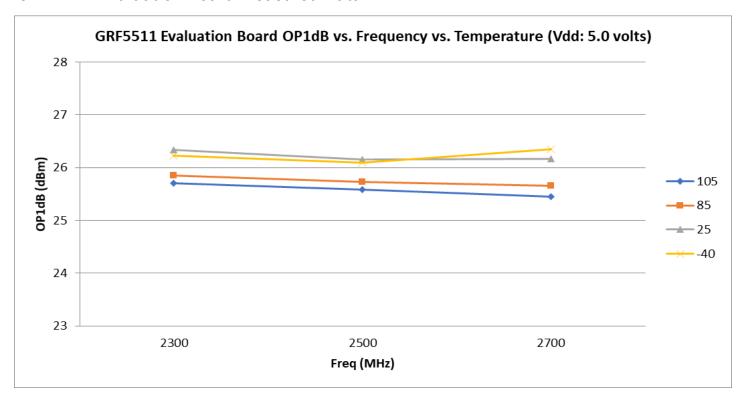


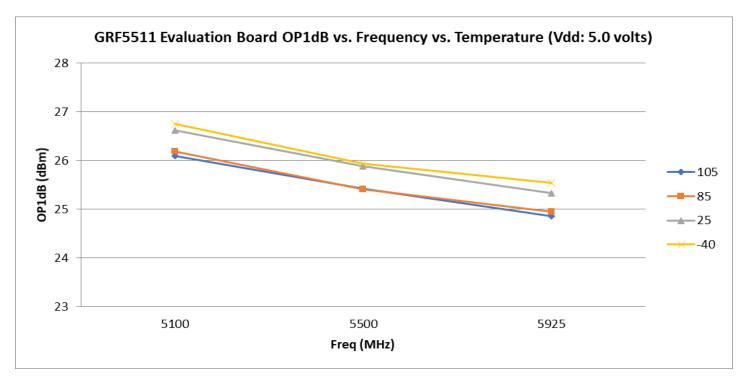




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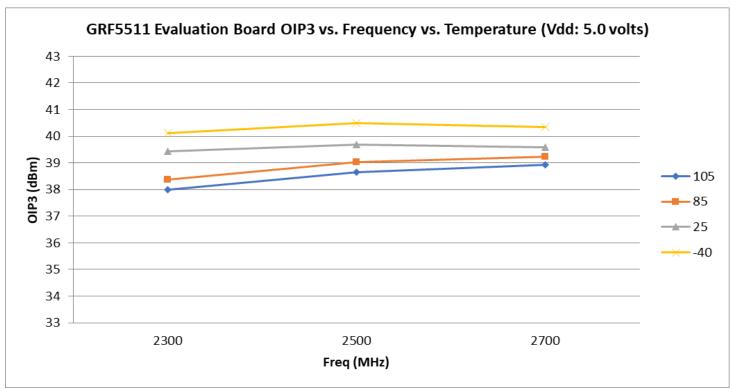


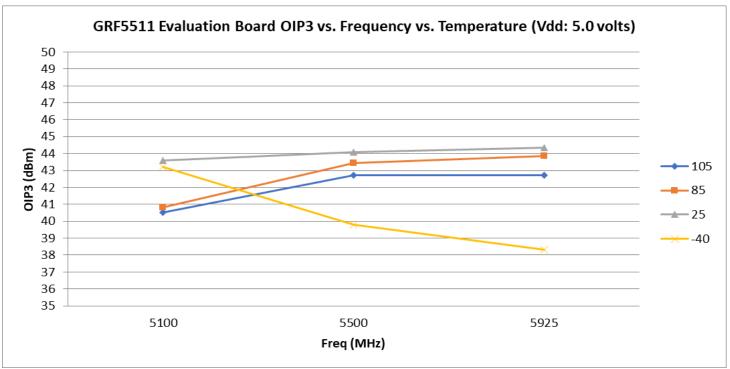
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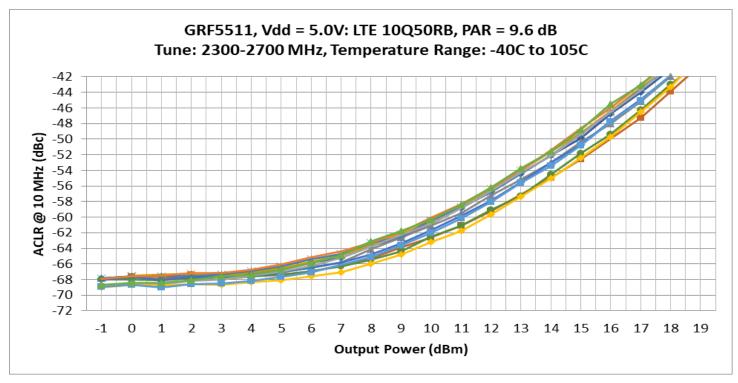


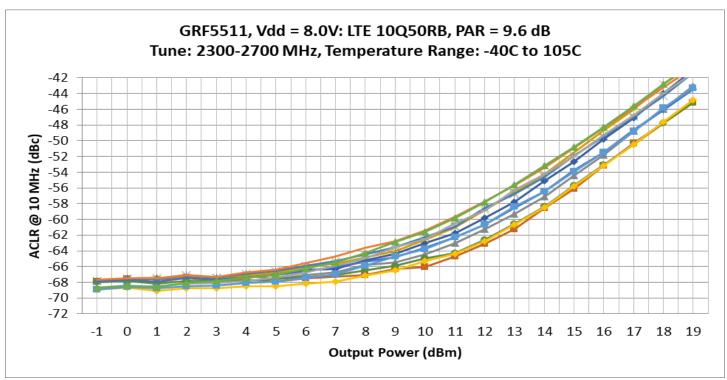




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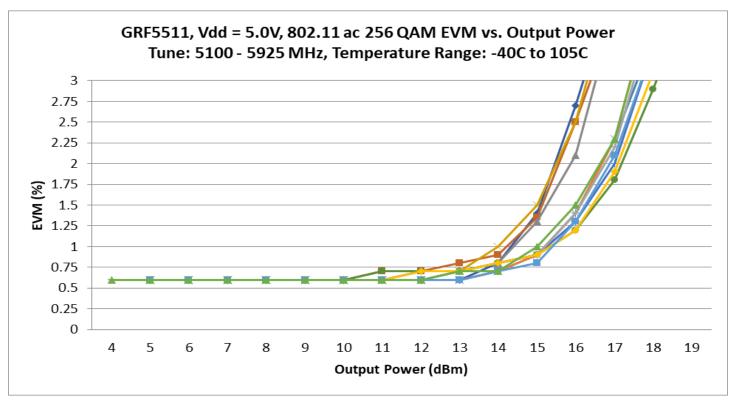


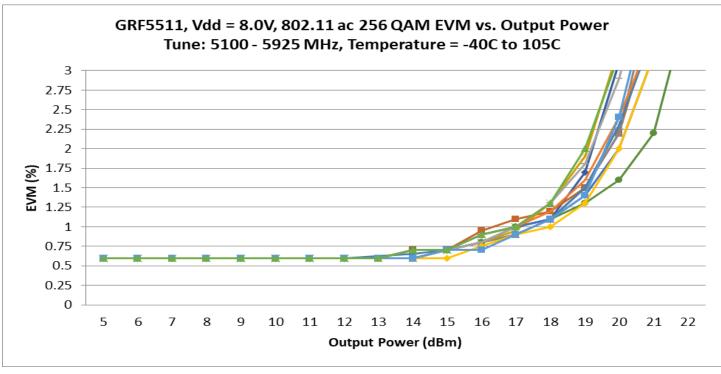




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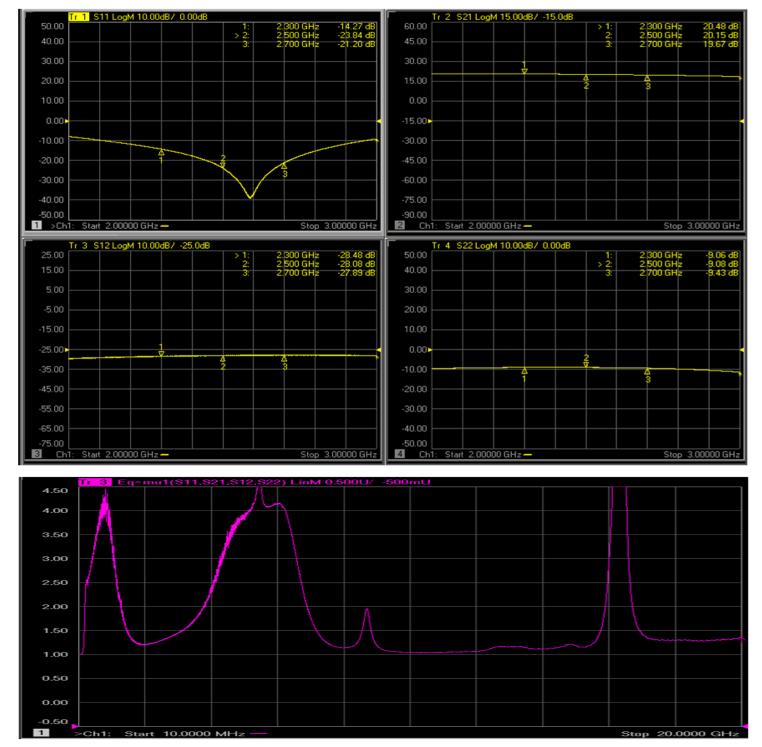




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GRF5511 Evaluation Board S-Pars: (2.3 to 2.7 GHz Tune)



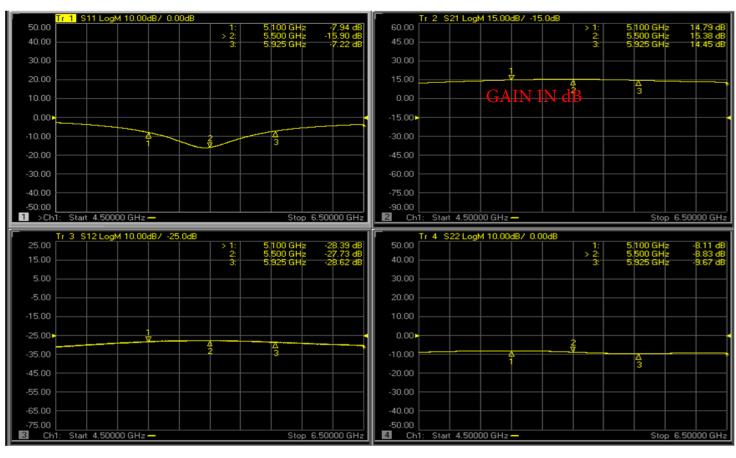
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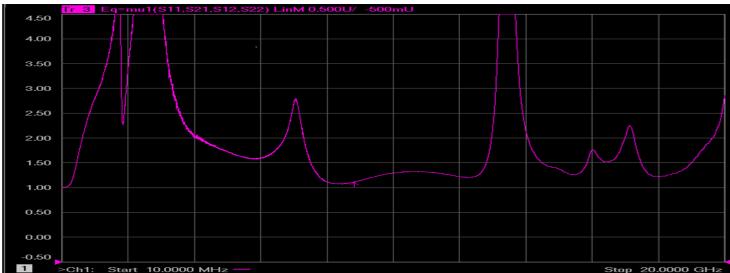


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High Frequency Power-LNA™ Tuning Range: 0.7 – 8.0 GHz

GRF5511 Evaluation Board S-Pars: (5.1 to 5.9 GHz Tune)



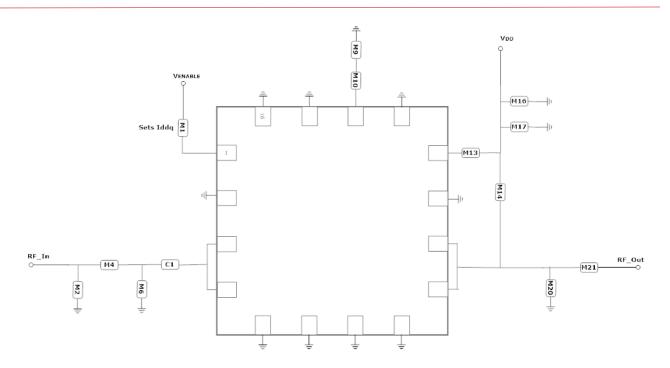


Note: Mu factor >= 1.0 implies unconditional stability.

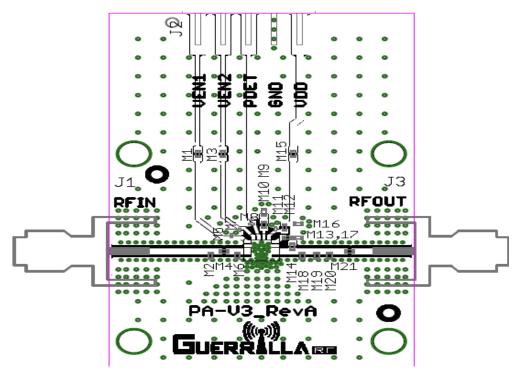


GRF5511

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GRF5511 Application Schematic



GRF5511 Evaluation Board Assembly Drawing



GRF5511

High Frequency Power-LNA™ Tuning Range: 0.7 – 8.0 GHz

GRF5511 Evaluation Board BOM: (2.3 to 2.7 GHz Tune)

Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M1 (See curves)	Resistor	Various	5%	Sets Iddq	0402	ok
M2	Inductor	Murata	LQP	4.3 nH	0402	ok
M4	Capacitor	Murata	GJM	1.2 pF	0402	ok
M6	Capacitor	Murata	GJM	1.8 pF	0402	ok
M9	Resistor	Various	5%	50 Ohm	0402	ok
M10	Resistor	Various	_	0 Ohm	0402	ok
M13	Resistor	Various	5%	500 Ohm	0402	ok
M14	Inductor	Coilcraft	НР	15 nH	0402	ok
M16	Capacitor	Murata	GRM	0.1 uF	0402	ok
M17	Capacitor	Murata	GJM	15 pF	0402	ok
M20	Capacitor	Murata	GJM	0.5 pF	0402	ok
M21	Capacitor	Murata	GJM	15 pF	0402	ok
Evaluation Board	PA-V3_RevA					

GRF5511 Evaluation Board BOM: (5.1 to 5.9 GHz Tune)

Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M1 (See curves)	Resistor	Various	5%	Sets Iddq	0402	ok
M4	Capacitor	Murata	GJM	2.0 pF	0402	ok
M6	Inductor	Murata	LQP	2.0 nH	0402	ok
C1 (spliced into EVB)	Capacitor	Murata	GJM	0.3 pF	0402	ok
M9	Inductor	Murata	LQP	1.5 nH	0402	ok
M10	Resistor	Various	_	0 Ohm	0402	ok
M13	Resistor	Various	5%	500 Ohm	0402	ok
M14	Inductor	Coilcraft	НР	4.7 nH	0402	ok
M16	Capacitor	Murata	GRM	0.1 uF	0402	ok
M17	Capacitor	Murata	GJM	8.2 pF	0402	ok
M19	Capacitor	Murata	GJM	0.6 pF	0402	ok
M21	Capacitor	Murata	GJM	6.8 pF	0402	ok
Evaluation Board	PA-V3_RevA					

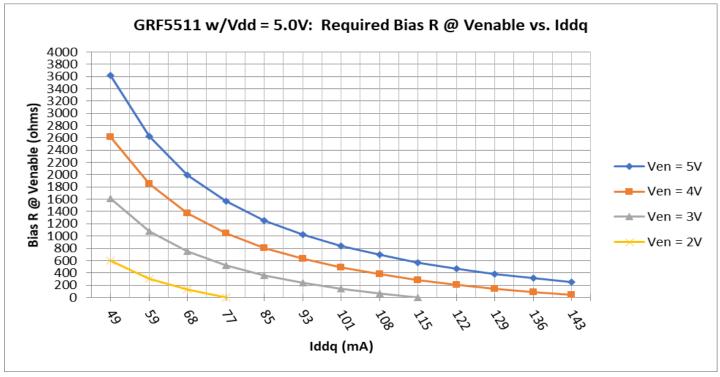
Note: Standard evaluation board bias: Vdd: 5.0V; Venable: 5.0V; M1:

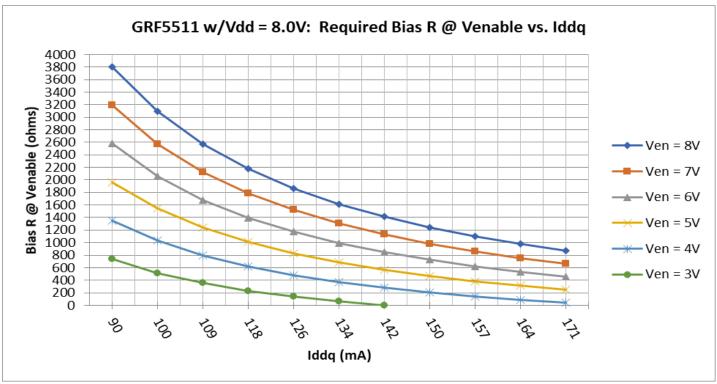


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GRF5511 Bias Resistor Curves:





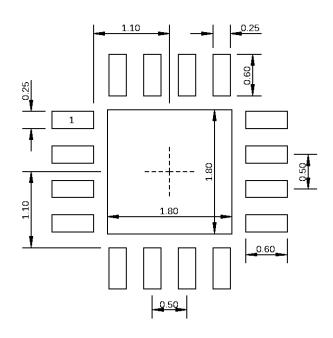


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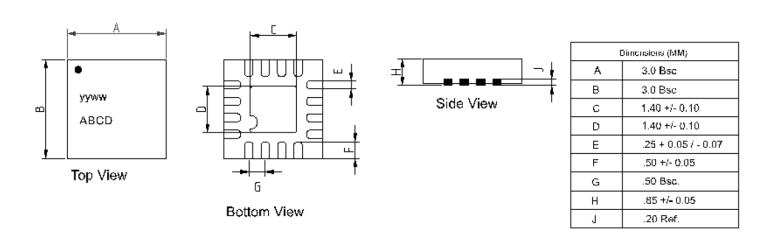
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Dimensions in millimeters

3.0 mm QFN-16 Suggested PCB Footprint (Top View)



3.0 mm QFN-16 Package Dimensions



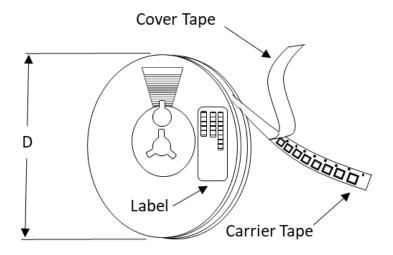
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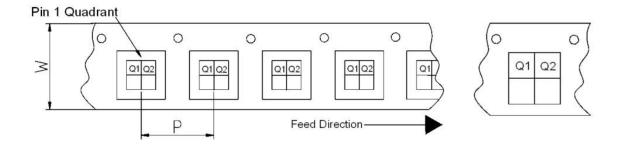
Tape and Reel Information:

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling". Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



GRF5511

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Tape and Reel Specification and Device Package Information Table

	Package				Carrier Tape	Reel		
Туре	Dimensions (mm)	Leads	Weight (mg)	Width (W) (mm)	Pocket Pitch (P) (mm)	Pin 1 Quad- rant	Diameter (D) (inches)	Units per Reel
QFN	2.0 x 2.0 x 0.50	12	7	8	4	Q1	7	2500
QFN	3.0 x 3.0 x 0.85	16	24	12	8	Q1	7	1500
DFN	1.5 x 1.5 x 0.45	6	4	8	4	Q1	7	2500
DFN	2.0 x 2.0 x 0.75	8	12	8	4	Q1	7	2500
LFM	3.5 x 3.5 x 0.75	See note	TBD	12	8	Q2	7	1500
LFM	4.0 x 4.0 x 0.75	See note	TBD	12	8	Q2	7	1500

Note: Lead count may vary. Reference applicable product data sheet



GRF5511

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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 05/21/20

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