Practical No.3

Aim: Implement Min, Max, Sum & Average operations using Parallel Reduction.

Objective & The objective of implementing min, max, Sum & Average operations using parallel Reduction is to efficiently compute thage on large sets of data using multiple processors or nodes.

Theory 3

Parallel Reduction &

Parallel Reduction is a technique used to

perform aggregate functions on large set of data
in parallel computing environment. The basic idea is
to divide the data into smaller chunks of perform
the reduction operation on each chunk independently
before combining the intermediate results to obtain
the final result. This technique can significantly
reduce the computation time for large datasets.

Min, max, sum & Average operations:

Min, max, sum & average operations are

commanly used aggregate functions in parallel

seduction. The "min" operation returns the

smallest value in the dataset. "max" returns

the largest value, "sum" returns the total sum

of all values, & "average" returns the mean

Value.



	These operations can be used in various fields,
	including statistics, data analysis & machine
	learning.
	Syntax used in Parallel Execution includes 8.
	O Loops: Loops are used to divide the data into Smaller
0	Chunks & distribute them among the availa
	-ble processors.
	1 Conditional Statement & Conditional Statements are
	used to apply binary reduction
	operations to the intermediate results.
	3 Functions & Functions are used to send & receive
	intermediate results between processors.
	For example, in MPI (Message passing Inter
0	face), you might use the "mp1 send" &
	umpl_Recu" functions to send & receive
	data between processors.
	9 parallel Computing Specific Syntax:
	Depending on the parallel Computing transmork
	being used, there may be additional syntax specific
	to that framework.



	Conclusion: In Conclusion, implementing Min, Max, Sum & Average operations using parallel Reduction can significantly improve the performance of computation by efficiently
	distributing the workload among main
	processors.
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