

Practical No.4

Aim & write a CUDA program for &

1 Addition of two large vectors.

. @ Matrix multiplication using CODA C.

Objective & The objective of this task is to write a CUDA program to perform vector addition & matrix multiplication using CUDAC.

Theory &

CUDA programming model &

The CUPA programming model is a parallel computing platform & programming model designed to use NVIDIA GPUS to accelerate computation.

It allows programmers to write parallel code that suns on the GPU, which is designed to handle massive parallelism & compute-intensive tasks.

CUDA Kernel?

A CUDA kernel is a function that owns on the GPU & is executed by many threads in parallel.

It is written in CUDA C & & is designed to perform a specific task on the GPU, such as vector addition or matrix multiplication.



Vector addition:

Vector addition is a simple operation that adds two vectors element-wise to produce a new vector. In CUDA, the operation that adds two vectors can be parallelized by dividing the vectors into smaller chuncks of assigning each chunk to a different thread. The threads can then perform the addition operation in parallel, resulting in faster computation times.

Matrix Multiplication:

matrix multiplication is a computationally intensive operation that multiplies two matrices together to produce a new matrix. In CUDA, the operation can be parallelized by dividing the matrices into smaller chumcks & assigning each chunk to a different thread block. The threads within each block can then perform the multiplication operation in parallel, resulting in faster computation times.

addition & matrix multiplication includes:

kernel definition: The kernel function is defined using the "global" keyword, which indicates that the function runs on the GPU.



Thread Indexing & Threadlex & block Dim variables are used to calculate the index of each thread & the total number of threads in the block.

memory Allocation: The "cudamalloc" function is used to

allocate memory on the GPU, & the

"Cuda Memopy" function is used to

transfer data between the CPU & GPU.

Execution configuration : The " <<<....>>> " syntax is used to specify the number of thread blocks & threads per block to be used for the kernel execution.

Reduction: For matrix multiplication, a reduction operation is used to combine the results of the thread blocks.

Conclusion: In conclusion, whiting a CUDA program
for vector addition & matrix multipli
cation using CUDA C can significantly
accelerate computation by leveraging the
massive parallelism of NUIDIA Gpus.