How to derive a state diagram?

State diagrams are not only used in Digital Design, but are also used in programming.

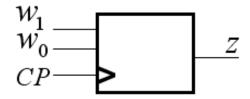
State charts are included in the diagram standard UML.

Task

A synchronous Moore machine has two inputs w_1w_0 and one output z.

- For the input sequence w_1w_0 10, 11 the output will be z = 0.
- For the input sequence w_1w_0 01, 11 the output will be z=1.
- For w_1w_0 01, 10 the output will **change value**.
- For other input sequences the output will **keep** its **value**.

Derive the automato **state table**, minimize it as far as possible. Then draw the automato **state diagram**.



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How many states?

All the described sequences are no longer than two steps. We therefore does not need (this time) to "handle" longer sequences than so.

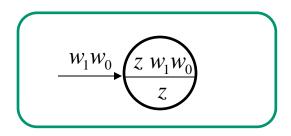
Input w_1w_0 can have four different values, 00 01 10 and 11. Start by Start by assuming that one may need to go to two different states from each input value, one with z = 0 and one with z = 1 (because the output for some conditions should "toggle"). This gives us 8 states.

One need not to be afraid of using too many states, because we can later reduce the number with state minimization.

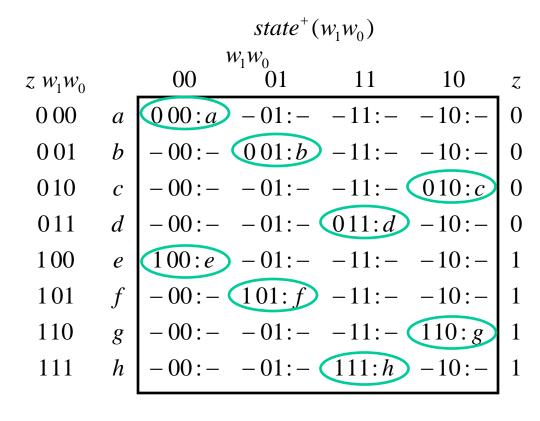
"Temporary" state numbers

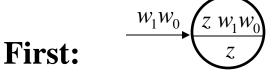
It will be easier to reason the way to the state table if state symbols (or state numbers) reflects the "characteristics" of the states. Eg "z w_1w_0 ". **Temporary state numbers** are here derived from output value in states and from the input combination that leads to the state.

If you just name the states $a \dots h$ it may be a little harder to follow the transitions in the table.



State: "output input"

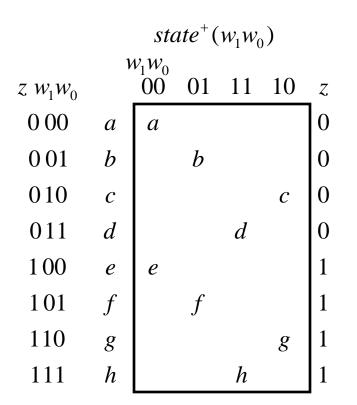




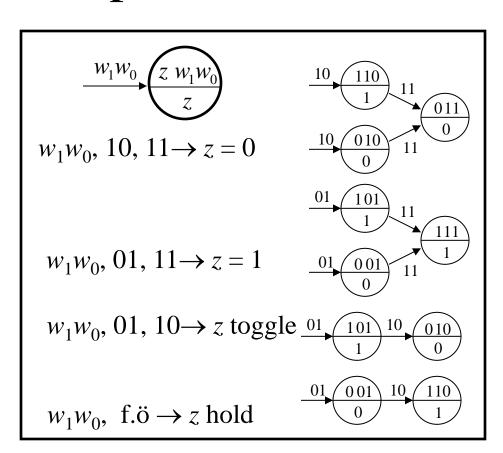
Every input leads to one of the two states that has this input in it's state number (eg. 00 a or e, 01 b or f).

For this input one stays in the state.

The description

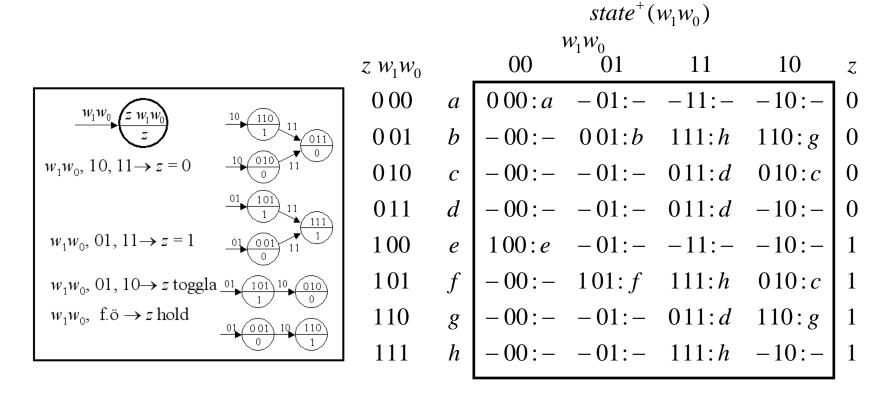


The state table is now underway.



The state transitions from the description.

The description – state transitions



Then you enter the state transitions as described.

Other state transitions

 $state^+(w_1w_0)$ W_1W_0 00 01 11 10 $z W_1 W_0$ 0.00:a0.01:b011:d010:c0.00 \boldsymbol{a} 0.01h 0.00:a0 01:*b* 111:*h* 110: g 0.00:a001:*b* 011:*d* 010:c010 $\boldsymbol{\mathcal{C}}$ 0.00:a010:c011 d0.01:b011:d110: g 100 100:e101: *f* 111:*h* 100:e 010:c101 101: f 111:h 110 100:e101: f 011: d 110: gg 101: f 111:h 110: g 111 100:e

The other state transitions leads to the state with the input number that has the same output – so no change in output.

State table

state $(w_1 w_0)$									
1	$v_1 w_0 \\ 00$	01	11	10	z				
		01		10	ا				
a	a	b	d	C	0				
b	a	b	h	g	0				
c	a	b	d	C	0				
d	а	b	d	C	0				
e	e	f	h	g	1				
f	e	f	h	С	1				
g	e	f	d	g	1				
h	e	f	h	g	1				

at at a + (.)

Now you can remove the temporary numbering of the states and present the state table with letter designations.

One sees immediately that a number of the states are equivalent, so the process of state minimization will follow.

State minimization

	st/	ato ⁺ (142 142)		$P_0 = (a, b, c, d, e, f, g, h)$						
$state^+(w_1w_0)$						$P_1 = (a,b,c,d)(e,f,g,h)$						
1	W_1W_0	0.4		4.0		$P_2 = (a, c, d)(b)(e, h)(f)(e, h)(f)(f)(g, h)(f)(g, h)(f)(g, h)(f)(g, h)(f)(g, h)(g, h)($						g)
	00	01	11	10	\mathcal{Z}	-	_					
a	а	b	d	С	0	P_{i}	$P_3 = P_2$	a_2	, b, e,	f,g	—	Designation of
b	a	b	h	g	0						the new states.	
c	а	b	d	c	0		00^{w_1}	01	11	10	_ <i>z</i>	
d	a	b	d	\mathcal{C}	0	a	a	b	a	a	0	
e	e	f	h	g	1	b	a	b	e	g	0	
f	e	f	h	C	1	e	e	f	e	g	1	
g	e	f	d	g	1	f	e	f	e	a	1	
h	e	f	h	g	1	g	e	f	a	g	1	

Minimized state diagram

