**New CCS Project**

## CCS Project

(i) Project name must be specified

Target:  &lt;select or type filter text&gt; ▾  | LCDKC6748 ▾

Connection: Texas Instruments XDS100v3 USB Debug Probe ▾  | Verify...

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### 🔒 C67XX [C6000]

Project name: [                                    ]

☑ Use default location

Location: C:\workspace-dsp  | Browse...

Compiler version: TI v7.4.12 ▾  | More...

▶ Advanced settings

▼ Project templates and examples

[type filter text                              ]

```
▲ 📑 Empty Projects
    🔒 Empty Project
    🔒 Empty Project (with main.c)
    🔒 Empty Assembly-only Project
    🔒 Empty RTSC Project
▲ 📑 Basic Examples
    🔒 Hello World
▷ 📑 IPC and I/O Examples
```

Creates an empty project fully initialized for the selected device. The project will contain an empty 'main.c' source-file.

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(?)   | &lt; Back | Next &gt; | Finish | Cancel

## Console ⊠

dft:CIO

```
[C674X_0] enter the length of the first sequence
4
  enter the first sequence
1 2 3 4
10.000000 + j0.000000
-2.007959 + j1.995212
-1.999967 + j-0.012741
-1.976076 + j-2.014236
```

## Console ⊠

IDFT_1:CIO

```
[C674X_0] 70.000000 +j 0.000000
-10.047754 +j -9.968081
-9.999797 +j 0.079632
-9.856435 +j 10.094960
```

## Console ⊠

linear convolution:CIO

```
[C674X_0]   enter the length of the first sequence
4
  enter the length of the second sequence
3
  enter the first sequence
1 2 3 4
  enter the second sequence
1 1 1
1
3
6
9
7
4
```