



Certificate

This is to certify that

Sandipan Mallik
of

National Institute of Science and Technology

participated in a Two-week ISTE STTP on

CMOS, Mixed Signal and Radio frequency VLSI Design

Conducted by Indian Institute of Technology Kharagpur

from 30th January, 2017 to 4th February, 2017

*(Online activity equivalent to one week from 26th December, 2016 to 4th February, 2017 and
physical participation at Remote Center from 30th January to 4th February, 2017)*

Grade	Excellent
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This workshop was held under the National Mission on Education through ICT (MHRD)

A. Goswami
Prof. A. Goswami
Dean, Continuing Education
IIT Kharagpur

Tarunk Bhattacharyya
Prof. T.K. Bhattacharyya
Course Coordinator
IIT Kharagpur

Satyā Sopan Mahato
Satya Sopan Mahato
Workshop Coordinator
National Institute of Science & Technology,
Berhampur