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## Electronics & ICT Academy

Supported by Ministry of Electronics and Information Technology (MeitY), Govt. of India  
Indian Institute of Technology Guwahati

# Participation Certificate



This is to certify that

Mr./Ms. .... Sandipan Mallik.....

of ..... N.I.T., Berhampur..... has participated

in a one-week Faculty Development Programme on "VLSI Chip Design Hands-on using open source EDA" jointly organised by Electronics and ICT Academies held from 08 – 12 July 2019, through National Knowledge Network under the "Scheme of financial assistance for setting up of Electronics and ICT Academies" of the **Ministry of Electronics and Information Technology (MeitY), Government of India**.

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