Mubashir Ho	Ussain 200396797 Date Feb 7
ENEL 351:	LAB 4 interupts
Objectivo: fo	amialirize the student with the operation
	upts using the STM3210x
PANC: Pak 1.	
NIMS NISK HS	sessmend and Mitigation strategy.
Risks	Mitigation stoategy
o Spills	okeep liquids and anything plse
· Tripping	that can spill away or in a locker.
Delectro Static	- locker.
STM board	the working telde clean and
	organized.
	organizea.
	band protection provided.
	band protection provided.
	49

	Date
P. 11	
Pro-lab	
What is an interupt?	
What is an initial	
(start) 4 flow of a program.	
init ITSR #1	
while(1) 138/#2	
ISR => looks for an event to occur	
	3 sa - 3
One of the ISR will connect to Systick	/
it runs independently antis the syst	ick
is a part of ARM chip.	, e.
The other ISR will look for the Pb	5 10
The other ISR will look for the Pb have a falling edge.	
All peripherals have capability to interupts.	USC
interupts.	

	Date
	How does it know what to do with events that
	occurs.
	4 defined in the vector table in the
	Start up files.
	vector table handlers points to handlers (ISR)
	Stack
	odefined in start up
	Prinst in - last out
	Interupts has masks
	1) local => located in a subsystem
	2) global=>
	what's the process?
-	
	· Systick time operate
7	Systick time
	flow: stack to handler to vector table then pop stack.
	stack 10

-,-

53

Date falling / trigger selection negistos NVIC Register Section 10.1.2 turnal on the bit & positionin EXTIO, NVIC -> LEGEN ISER[0] = NVIC JSR - SETEN-6 Systick helps to schedule tasks in program by way of interupts, ExTIO is an example of an interrupto allows to detect and neighbors to
external logic transitions. To we an interupt: 1) ISR (interupt service Loutine) roads se 2) Provide address to JSR from the vector table 3) sixtem that is generating the interrupt must be setup properly. it is not good to stay in an interupt for long- Should be fact.

	u to it and afiles
	using Interupt system with the keil setupfiles.
	/
	1 1 1 Call 12 is (181 monither
	1) disable Systick, O in CIRC pregitter
	2) clas - courter o in VAI register
Çe	3) Configure clock interval
\v	S configure con a 24 MHz
યું હ	· Automatically relocided.
	Tuto mattacky vero case.
70	
<u> </u>	W -1 7 1 Ha API manter
<u> </u>	4) write 7 into the CTRL negative
97	Ax
	void Systick_Handler(void)
<u> </u>	
20	
	2
	4-
\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
~~	i) turn clocks on for AIJO, port B
	2) Select Port B as source for EXTID events
<u> </u>	3)
	3) unmark PBO as interupt source, 4) Select falling events as trigger e) unmade £XTIO as interupt source in the NUTC.
1	4) Select falling events as trigger
	el unmade £x710 as interest south in the NUGL.
1/11	
Selen Transport	void EXTJ-TRQHandler (void)
65 -	
ey San	EXTZ-RR => EXTZ_PR_RRO;
v,	
N.	