

Lab 4

Task 2:

```
LIST P=16F877
#include <P16F877.inc>
ORG 0x00 ; Program start address
GOTO MAIN ; Jump to main program

COUNT1 EQU 0x20
COUNT2 EQU 0x30

MAIN:

    BANKSEL TRISB      ; Select bank containing PORTC
    CLRF TRISB         ; Clear PORTC (all outputs low initially)
    BANKSEL PORTB
    CLRF PORTB

BLINK1:

    BANKSEL PORTB
    BSF PORTB, 0

CALL DELAY

    BANKSEL PORTB
    BCF PORTB, 0

CALL DELAY

    GOTO BLINK1

DELAY:

    MOVLW D'10'
    MOVWF COUNT1

OUTER:

    MOVLW D'40'
    MOVWF COUNT2

INNERLOOP:
    NOP                → 1
    NOP                |
    DECFSZ COUNT2, F    → 1
    GOTO INNERLOOP      → 2
    DECFSZ COUNT1, F
    GOTO OUTER

RETURN
END
```


Task 3:

```
LIST P=16F877
#include<P16F877.inc>
ORG 0x00;
GOTO MAIN

    COUNT1 EQU 0x20
    COUNT2 EQU 0x30

MAIN:

    BANKSEL TRISB;
    CLRF TRISB;

    BANKSEL PORTB
    CLRF PORTB

        MOVLW 0x0F
        MOVWF PORTB

    CALL DELAY

        SWAPF PORTB, F

DELAY:
    MOVLW D'10'
    MOVWF COUNT1
OUTER:
    MOVLW D'40'
    MOVWF COUNT2
INNERLOOP:
    NOP
    NOP
    DECFSZ COUNT2, F

    GOTO INNERLOOP

    DECFSZ COUNT1, F

    GOTO OUTER

    RETURN

END
```

