National University of Computer and Emerging Sciences Lahore Campus

0033

Electrical Network Analysis

(EE2004)

Date: September 21st, 2024

Course Instructor(s)

Mr. Abdul Majid

Ms. Beenish Fatima

Mr. Haris Mujtaba

Roll No

Section

Sessional-I Exam

Total Time (Hrs):

1

Total Marks:

40

Total Questions:

2

Student Signature

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1. Attempt all the questions.

2. Attempt all parts of the same question together.

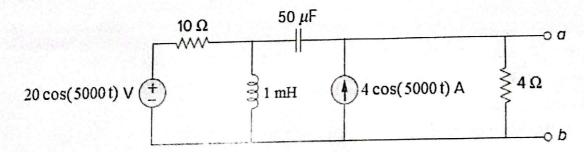
3. Show all the steps with proper labelled circuit diagrams, and answers with proper units.

CLO #1: Apply phasor-domain analysis to solve circuits containing R, L, C, and mutual inductance

Q1: Solve the circuit below to

[20 marks]

- a) Construct the frequency domain circuit
- b) Construct the Thévenin Equivalent circuit w.r.t. terminals 'a' and 'b' using source transformations
- c) Determine the steady state expression of Thévenin Equivalent voltage



V = 26.8 < 63.4 V

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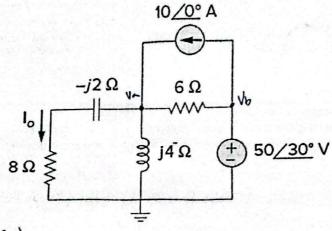
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CLO #1: Apply phasor-domain analysis to solve circuits containing R, L, C, and mutual inductance

Q2:

[20 marks]

- a) Calculate the current I_o in the circuit shown below using node voltage method
- b) Determine the expression of $i_o(t)$



15. +5.0<30+ 16-Va +(10<0) + 0

Va = 403.07 < 25.7 V

$$T_{0} = 13.2 < 156.8 \text{ A} 2$$

$$T_{0} = -12.6 + 5.21; \text{ A}$$

$$V_{0} = -12.6 + 5.21; \text{ A}$$

$$V_{$$

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Page 2 of 2 Va = 46-1 <- 27.07 V= 1) RV= 5.7 <63