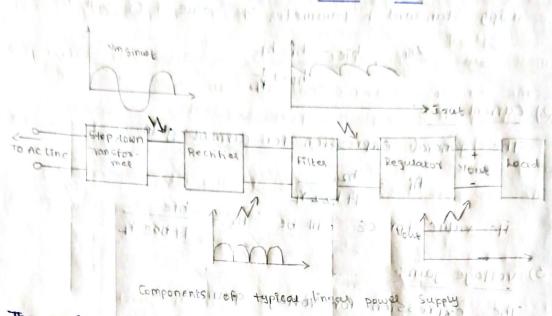
Electronics Circuits and Instrumentation.

Black diagram Description of DC power supply:



- constant DC output voltage.
- The Ac signal of 230v, 50 Hz is applied to step down Transformer.
- The Step down Transformer, Down the input voltage into
- into pusating DC signal, which consists of DC component as well as Ac component.
- By using filter we can eliminate undesired frequencies that are present at the output of rectified!

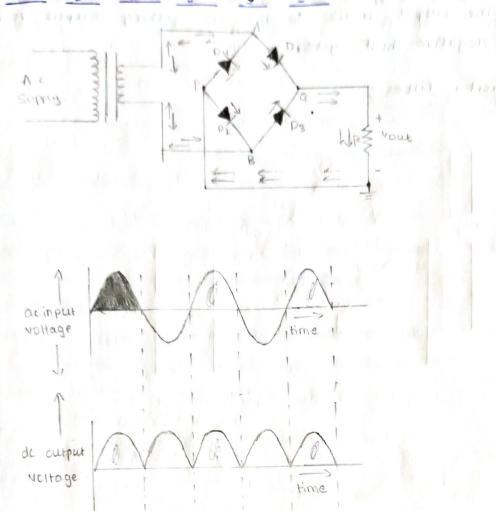
 The capacitive reactants

 $X_c = \frac{1}{2\pi F_c}$

- For DC Signal frequency \$ =0 and \$c becomes infinity(\infty) which means that the capacitor act as open circult for DC signal and by pass the DC current.
- For Ac signal we have frequency and capacitive reactance becomes decreases i.e., capacitor act as short circuit and will allow Ac current.

- the bad Resistance.
- Here, we are using zener diode as voltage Regulator.

working of full wave Bridge Rectifies ?



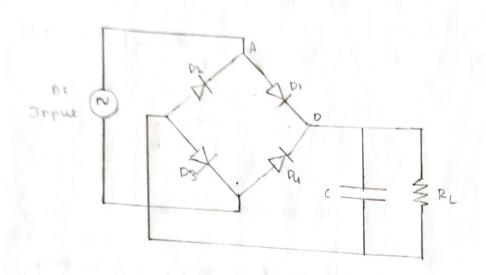
- form of bridge to construct full wave bridge rectifies.
- The Ac line voltage of 2304, 50 Hz signal is applied to Step down transformer.
- The transformer down the input voltage into lower value that may be 12 v Ac or 24 v Ac signal.
- Rectified convert the signal finto pulsating De signal which consists of both De component and the component
- During positive houf cycle of Ac input signal, diodes D1, D2 are in forward Bias where as diodes D3, D4, are in veverse Bias.
- The conducting diodes D₁, D₂ are in series with load

 Resistor R and Hence Current Flows in the output circuit.

and we are getting output voltage during positive has Lycie.

For negative half cycle Diodes D3, D4 are in forward bio and these conducting diodes are in series with load Resistor R and form clusted circuit Hence current flows in the output circuit so we are getting output voltage for Negative half cycle.

Capacitor files :





Ac input

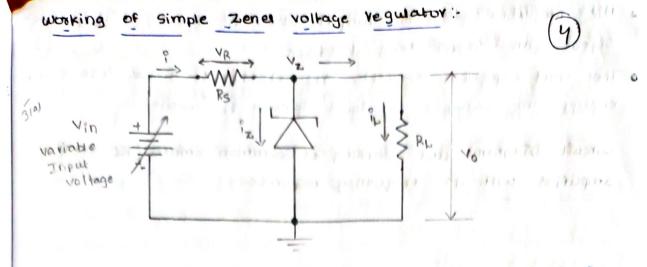


De output

- o Here it civiles 14, 25, 45 and out order The unwanted frequencies that are present at output of rectifies can be eliminated by using simple filter.
- the fitter circuit consists of capacitor and the capacitive reactants is given by Xc = mc.

$$X_c = \frac{1}{2\pi F_c}$$

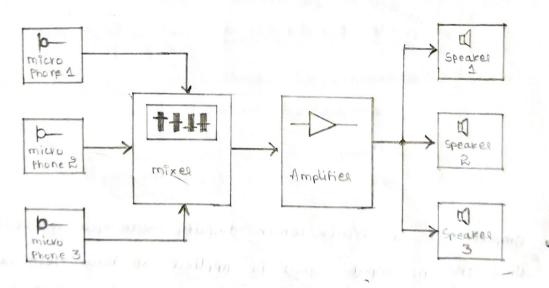
- on varie to the sea for south for the De signal, frequency f=0, and Xe becomes Prinity (00) i.e., the capacitor act as open circuit and will not allow Dc current it by pass the oc current.
- For nc signal, the capacitive reactants decreases and capacitor act as short circuit and will allow the Ac current through it. in signor of the way morner was a



- · Voltage Regulator is a circuit that provides constant Dc output voltage even though there are furtuations in the input voltage and variations in the load resistance.
- . The Zenea biode under reverse bias act as voltage
 Regulator i.e., if you increase the reverse voltage and
 it reaches to Zenea breakdown voltage then the Zenea diode
 will provide constant voltage across 7t.
- even though there is a change in input voltage and load Assistance value, begans as to samplest paragraph

Block diagram of Public Address system:

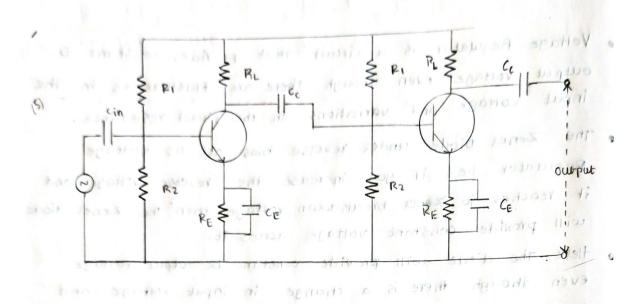
14)



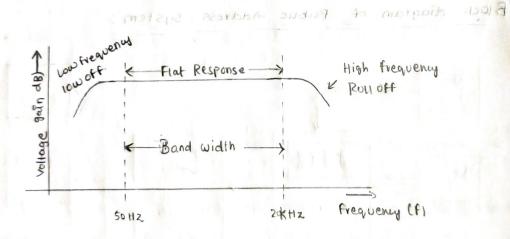
- As the Sound signal travel with distance the intensity of Sound signal decreases. In order to overcome this PA System was introduced.
- Here microphone act as transducer which convert input sound signar into electrical signal by using mixer we can combine different input signals.

- The Amplifier provides Amplification of signal, so that
- The loud speaker converts the electrical signal into Sound energy. The example for loud speaker is "woofer."

Circult Diagram and working of common Emitted (RC coupled)
amplified with its frequency response.



Frequency Response of Rc coupled Ampufied -



- Amplifier is a circult which Amplify weak input signal. In there, the Ac input signal is Applied at base terminal. Output is available across load resistor R. which is Connected at Collector terminal.
- To provide proper DC brasing the voltage divider network R., R2 and + vcc is used so the Transistor is operated in active region.
- The output of first Stage CE Amplified is given as input to the sewnd stage through the Resistor and

capacitor Co. Hence it is caused as Rc coupled Amplifies

Frequency Response:

Poly in the territorial and a moral of agent when it will

At low frequencies (i.e., below 50 HZ):

- . The capacitive Reactants Xc = 1/2nfc in this Xc and frequency (f) are both in inversly proportional.
- ent low frequency, as the frequency is low, the capacitive veactants is high, so only small part of the input signal appear at base terminal, that's why voltage gain decreases.

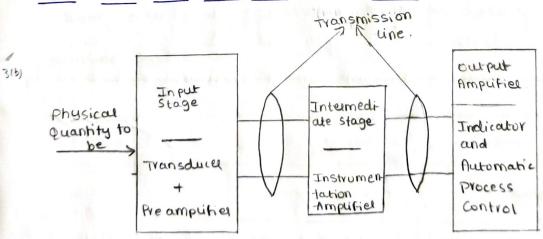
At high frequencies (i.e., above 20KHZ):

nt high Frequencies, as the frequency 9s high, the capacitive reactance 9s low so the capacitor act as short circuit and loading effect of next Stage 9ncreases. Hence gain decreases.

At mid- Frequencies (i.e., 50 Hz to 20 KHZ):

If you increase frequency with in the medium frequency range the Capacitive reactants decreases and Hence voltage gain increases. However the loading effect of next stage increases which causes decreases involtage gain. Due to these two factors the gain remain constant.

Block diagram of an Electronic Instrumentation System:



• The Instrumentation system is an electronic system, which is used in Industries in order to measure the physical quantities such as temperature, pressure, velocity, etc....

- Quantity into electrical signal.
- · Pre Ampufies provides the -Ampufication of signal.
- The Instrumentation Amplifier, Amplify the Signal in order to drive output stage.
- · The Indicator provides the display of measured value.
- By using transmission line au these devices can be connected together.