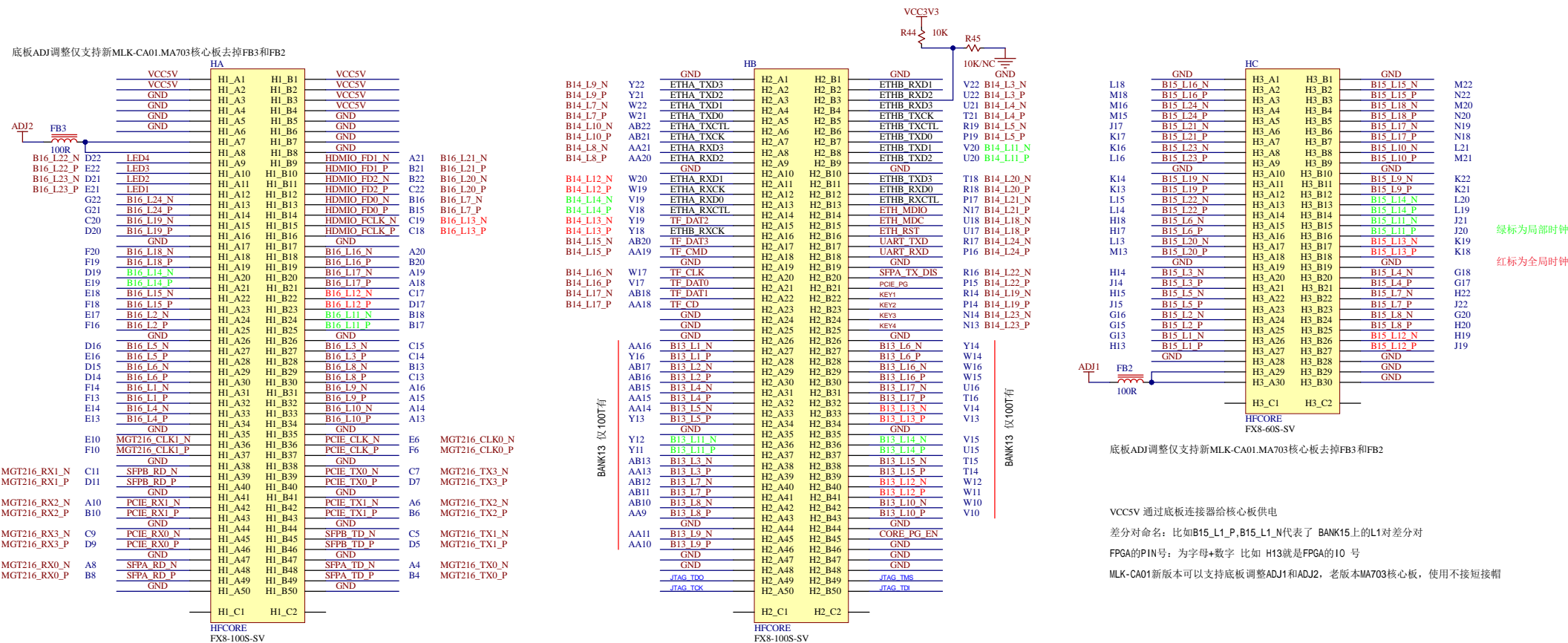


说明:此图核心板为MLK-CA01-100T,焊接2片DDR, MLK-CA01-35T焊接1片DDR

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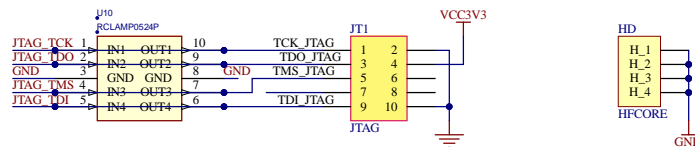
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红标为全局时钟

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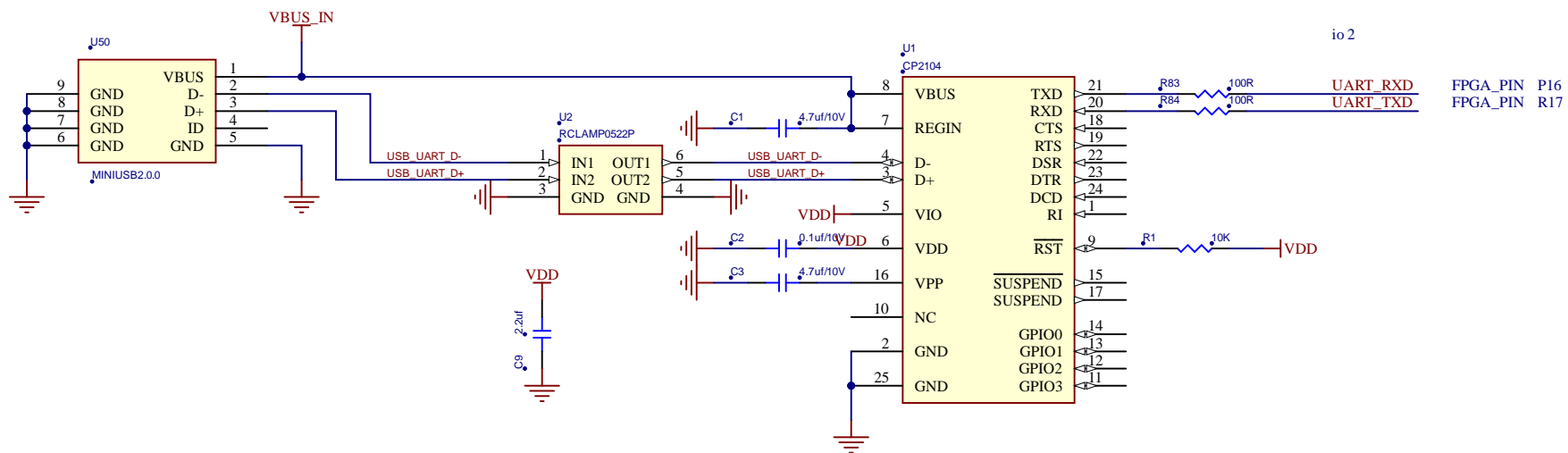
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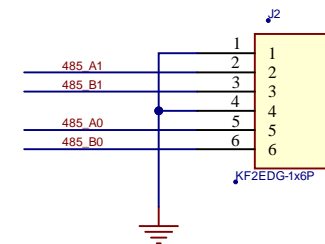
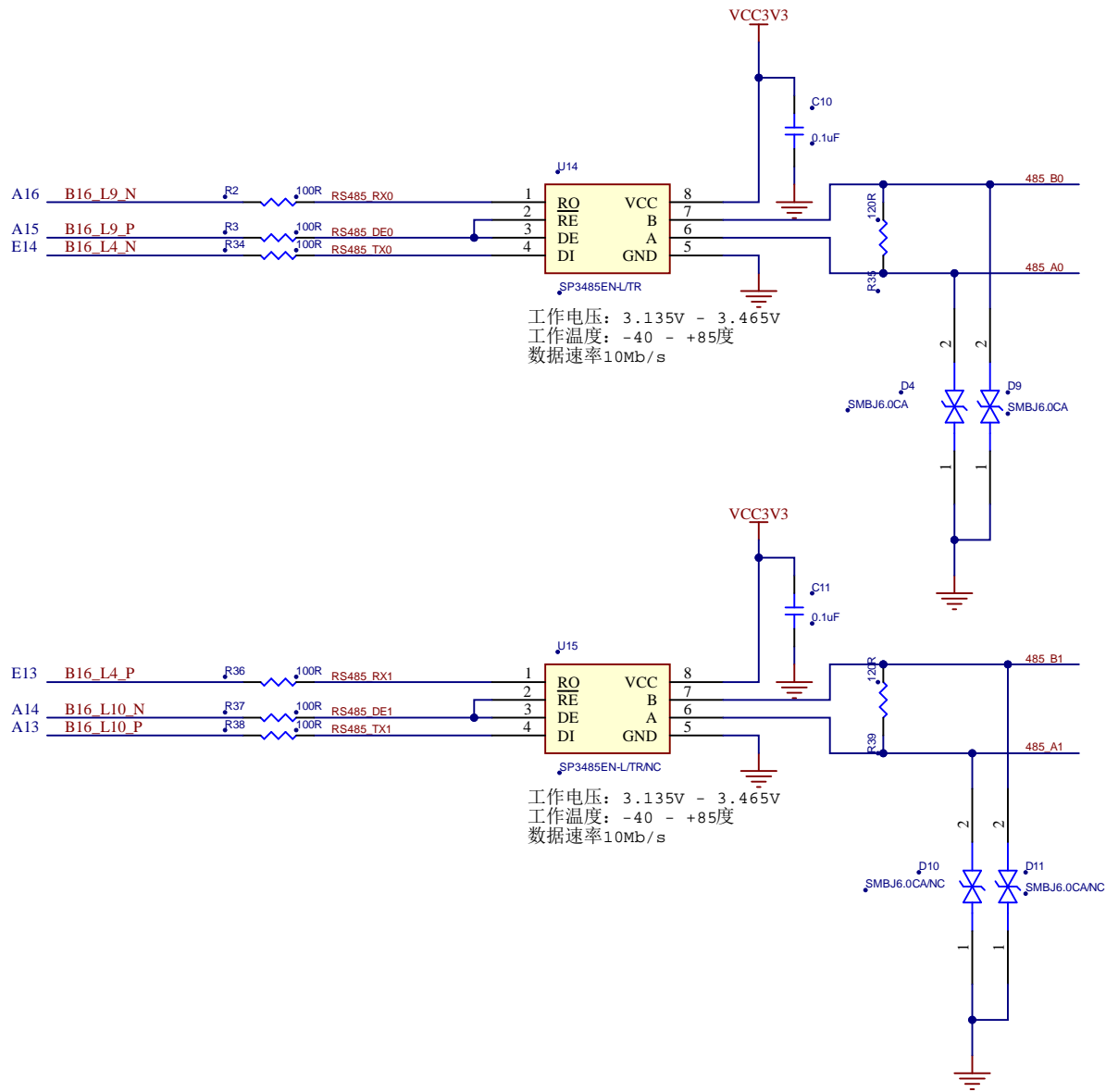
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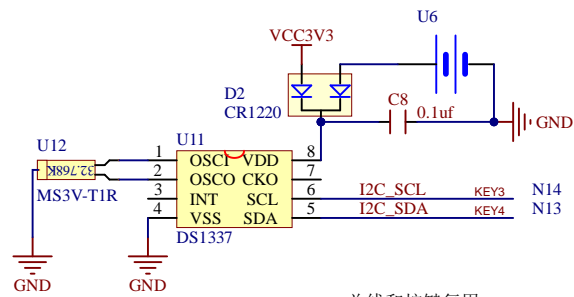
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Date:		Sheet	



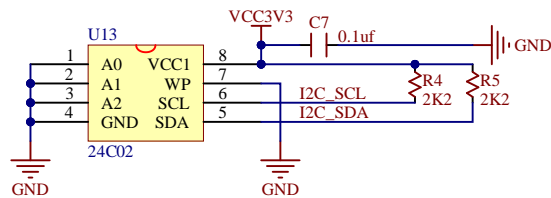
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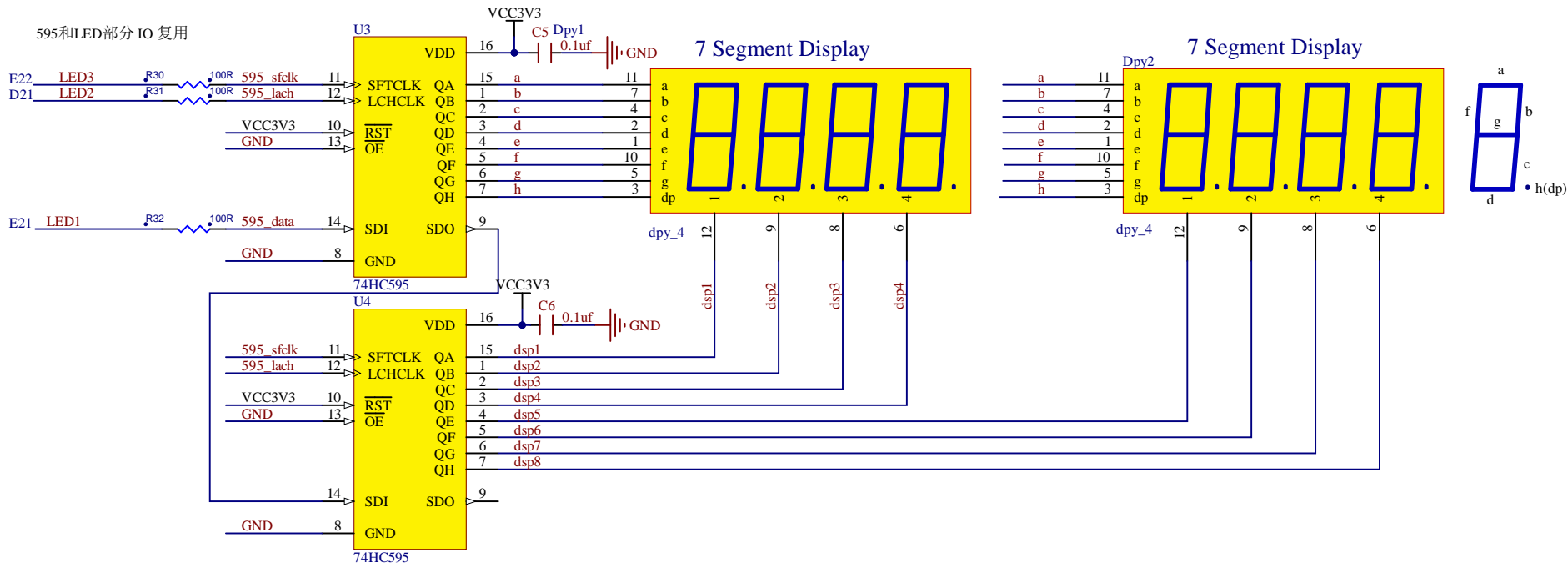
I2C总线和按键复用



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MSXBO

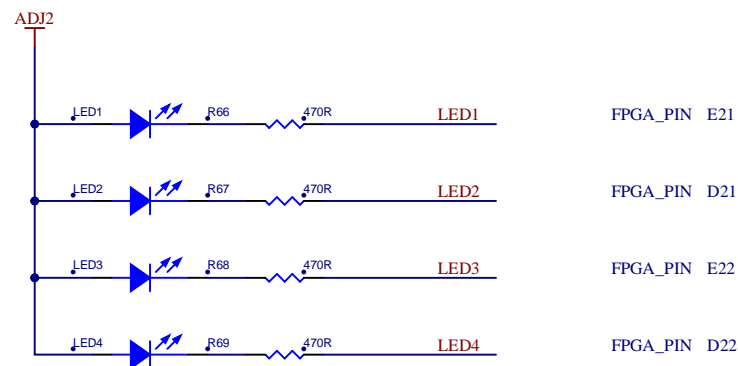
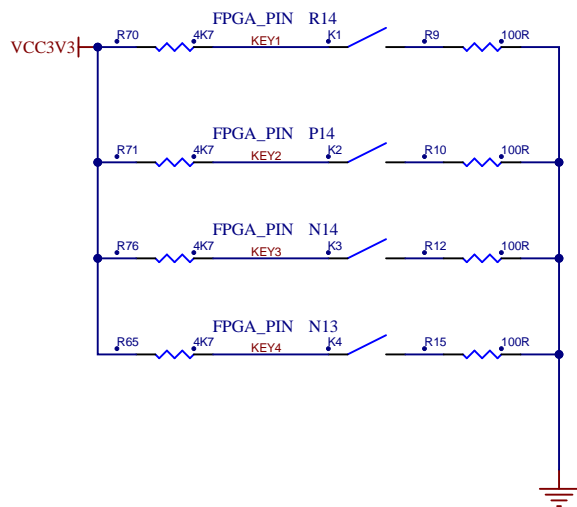
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Page Title:	05_RTC_EEPROM.SchDoc	Rev:	20230115
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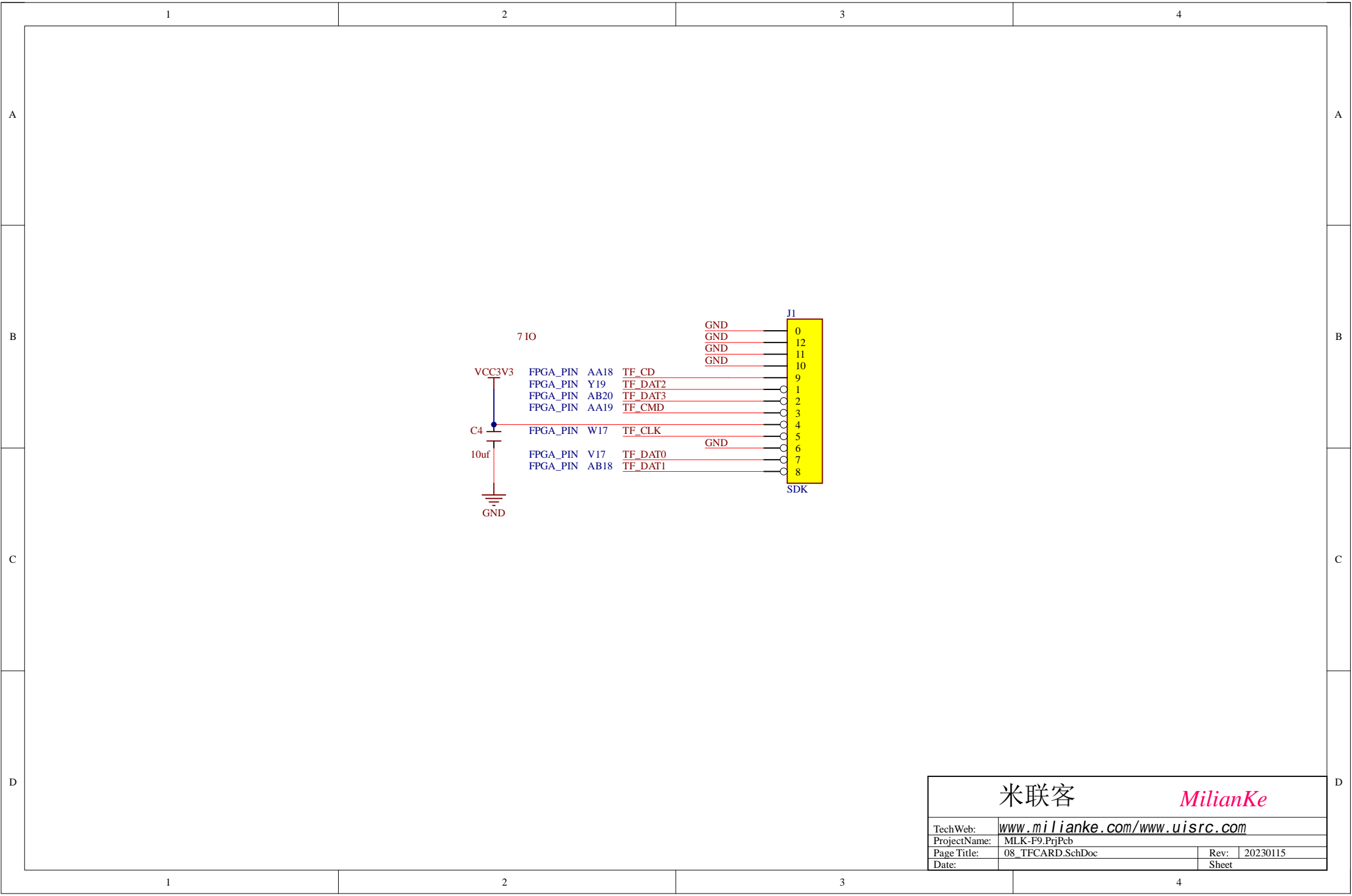
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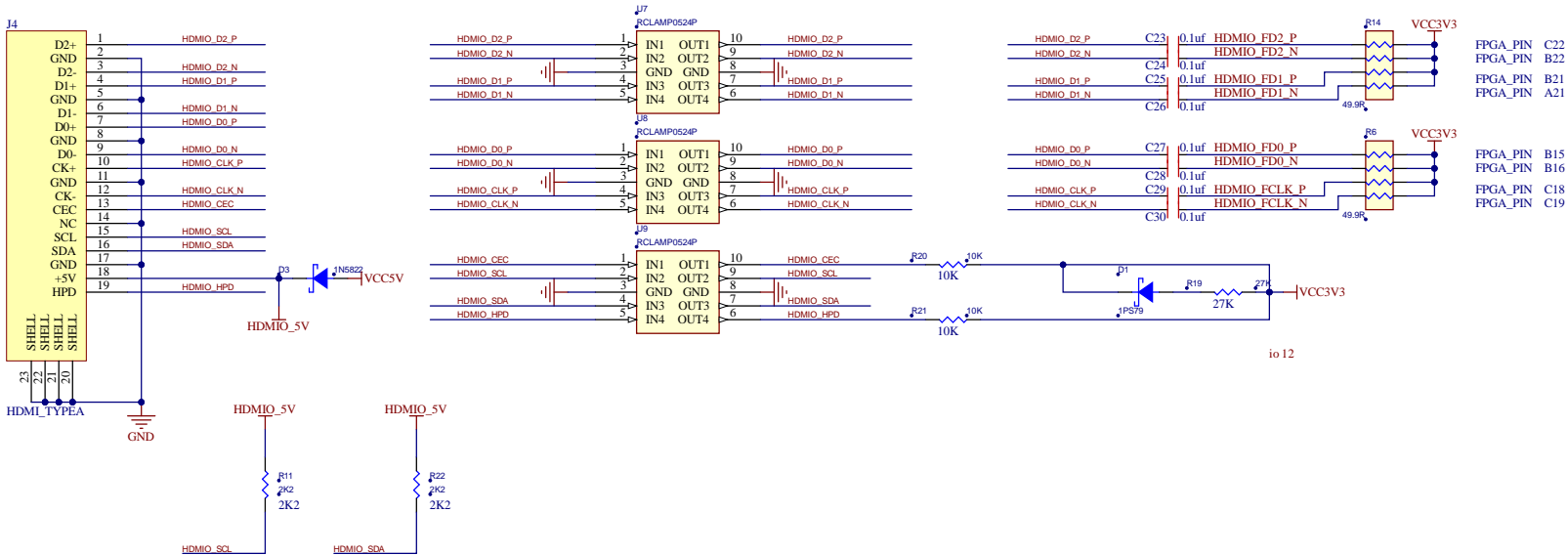


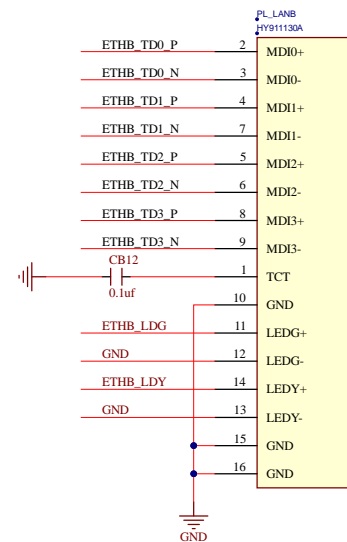
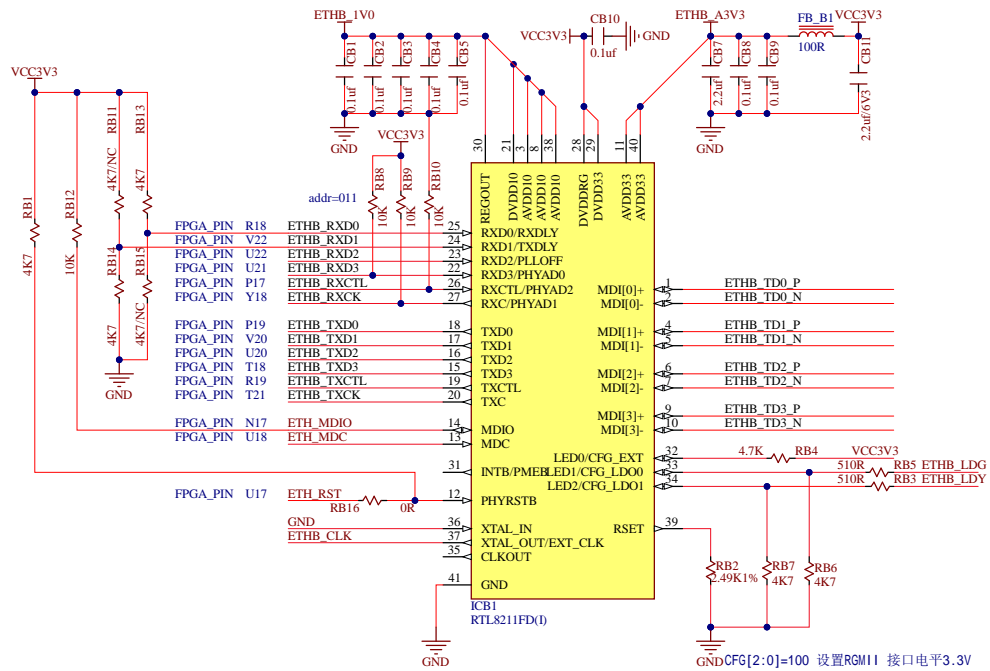
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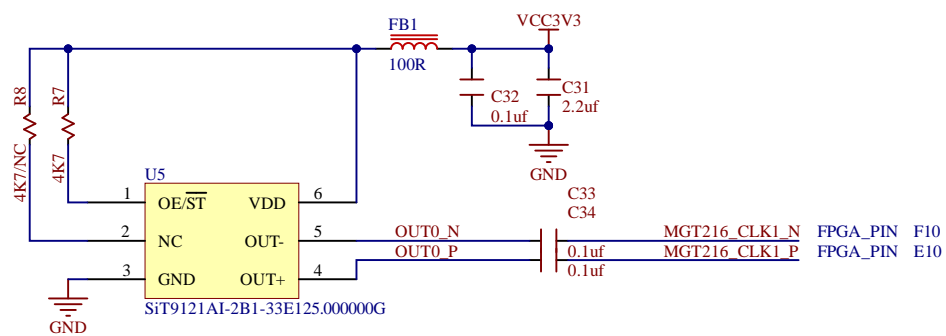




GND CFG[2:0]=100 设置RGMII 接口电平3.3V

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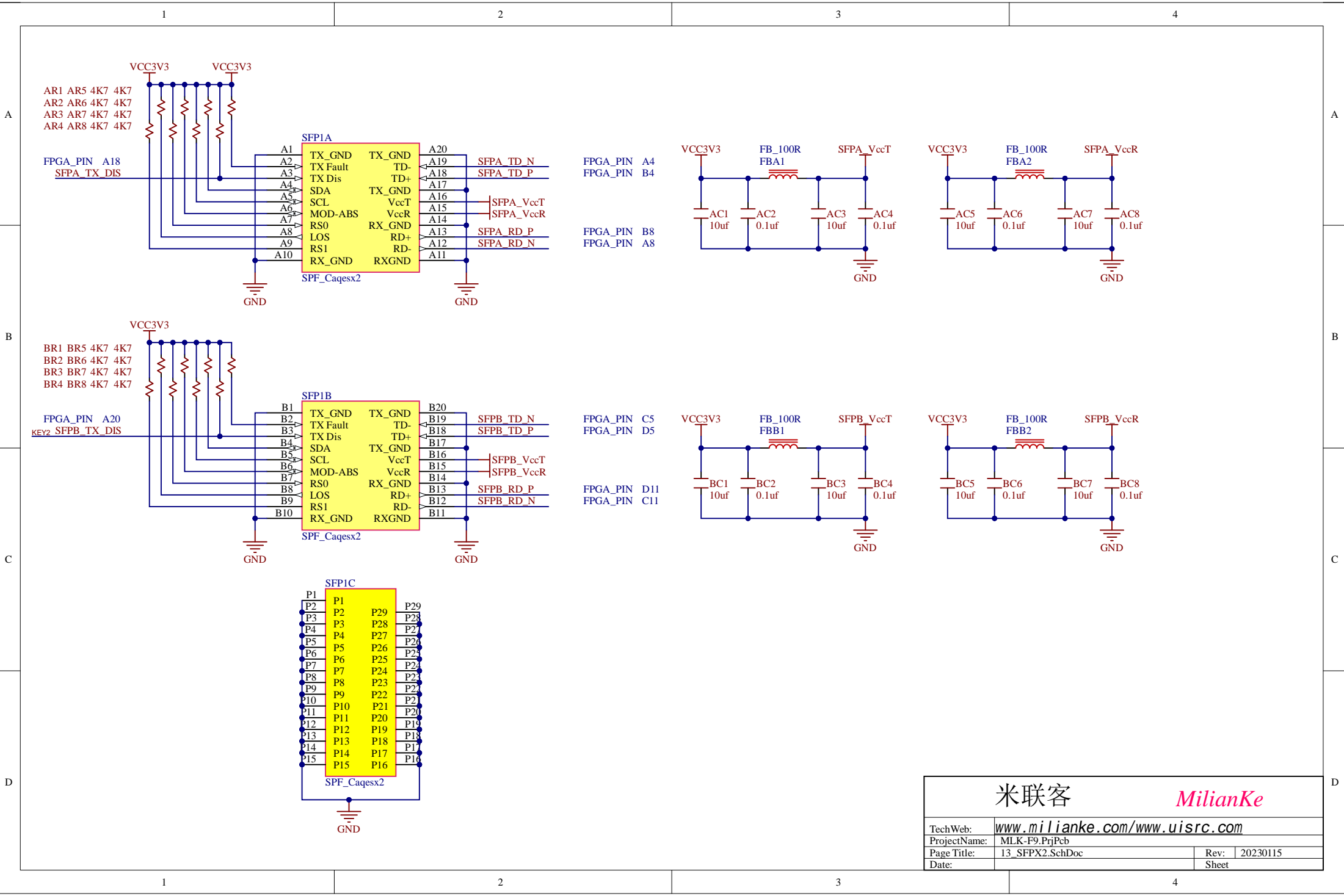
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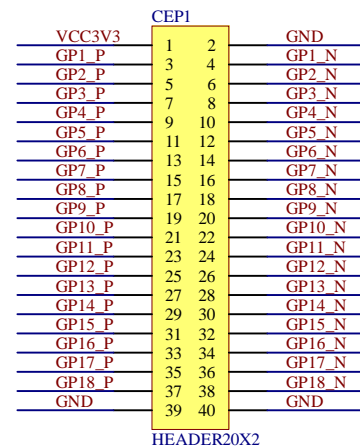
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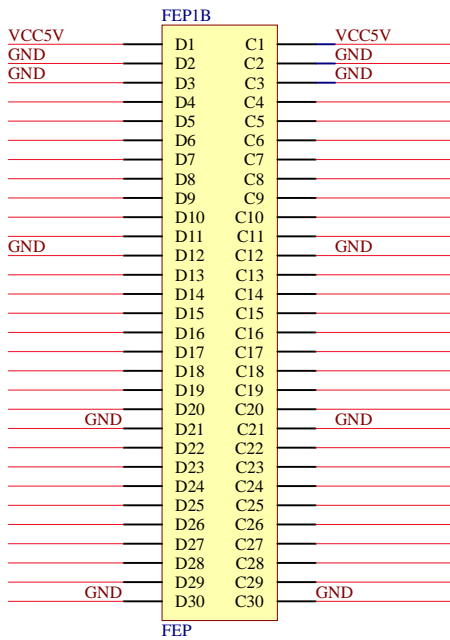
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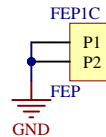
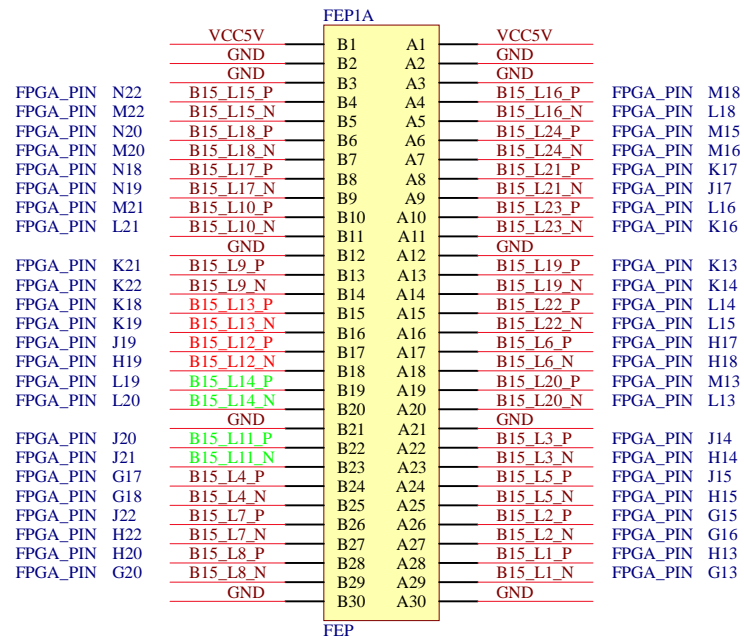
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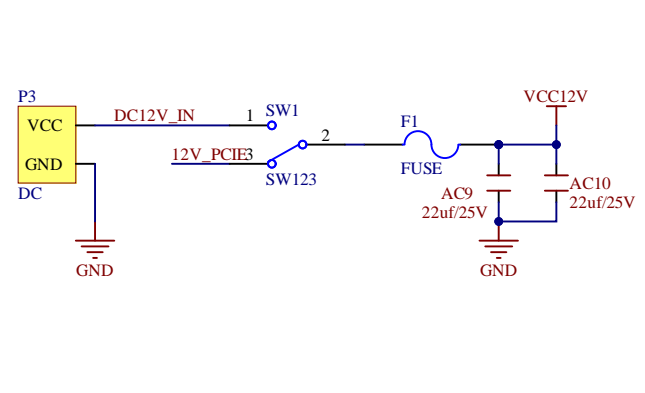
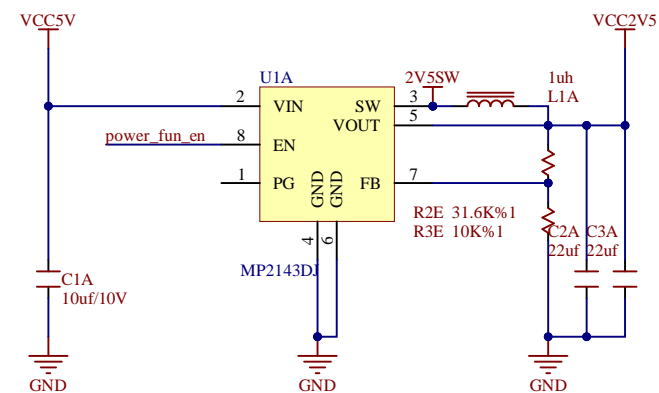
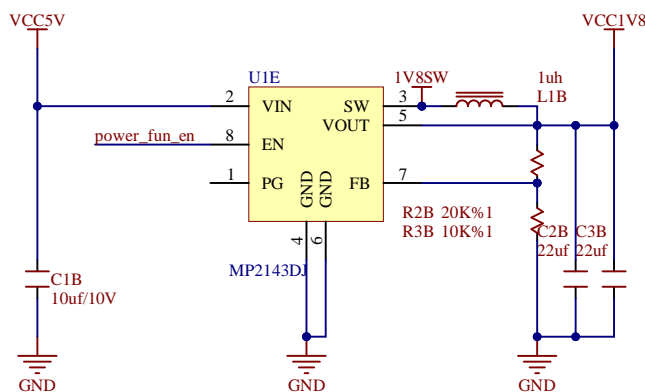
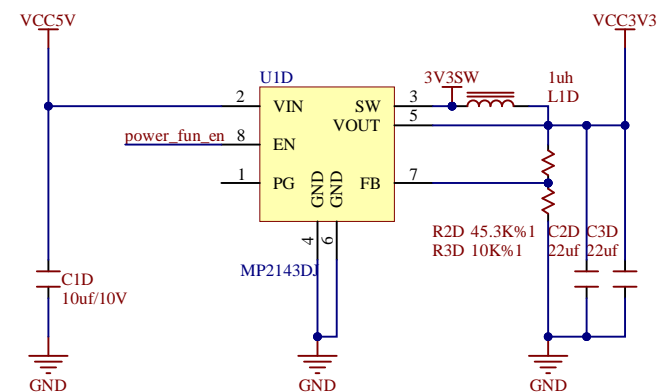
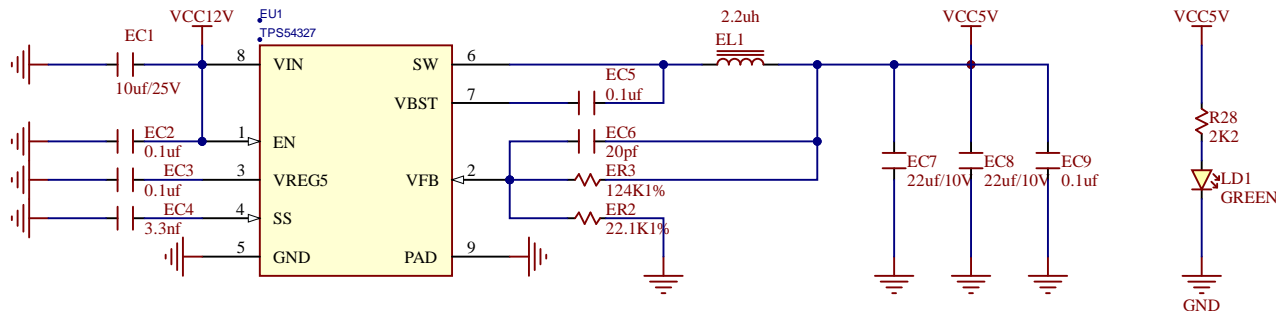
B15/16/17/18为全局时钟



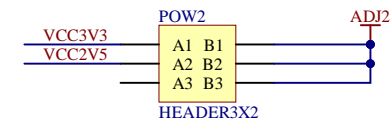
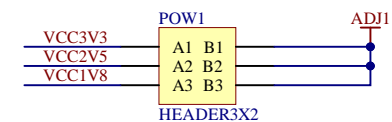
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MLK-CA01新版本核心板可以支持底板调整ADJ1和ADJ2
如果核心板安装老版本MA703核心板以下短接帽不短接



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