

# Linker Control cont'd

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/* The memory space denoting the maximum possible amount of
   addressable memory. */
define memory Mem with size = 4G;

/* Memory regions in an address space */
define region ROM = Mem:[from 0x00000 size 0x10000];
define region RAM = Mem:[from 0x20000 size 0x10000];

/* Create a stack */
define block STACK with size = 0x1000, alignment = 8 { };

/* Create a Heap */
define block HEAP with size = 0x2000, alignment = 8 { };

/* Handle initialization (or not) of .bss */
do not initialize { section .noinit };

/* Initialize .data section; exclude zero-initialized sections */
initialize by copy { readwrite };

/* Place startup code at a fixed address - address 0x00000 */
place at start of ROM { readonly section .cstartup };

/* Place code and data */
/* Place constants and initializers in ROM: .rodata and .sdata */
place in ROM { readonly };

/* Place .data, .bss, and .noinit and STACK */
place in RAM { readwrite, block HEAP, block STACK };
```

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