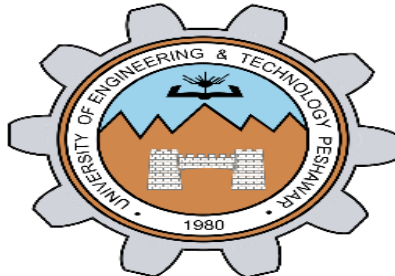


## LAB REPORT NO 4



### **CSE-206L Electronic Circuits Lab**

Submitted by: **Muhammad Ali**

Registration No:- **19PWCSE1801**

Class Section: A

“On my honor, as student of University of Engineering and Technology, I have neither given nor received unauthorized assistance on this academic work.”

Submitted to:

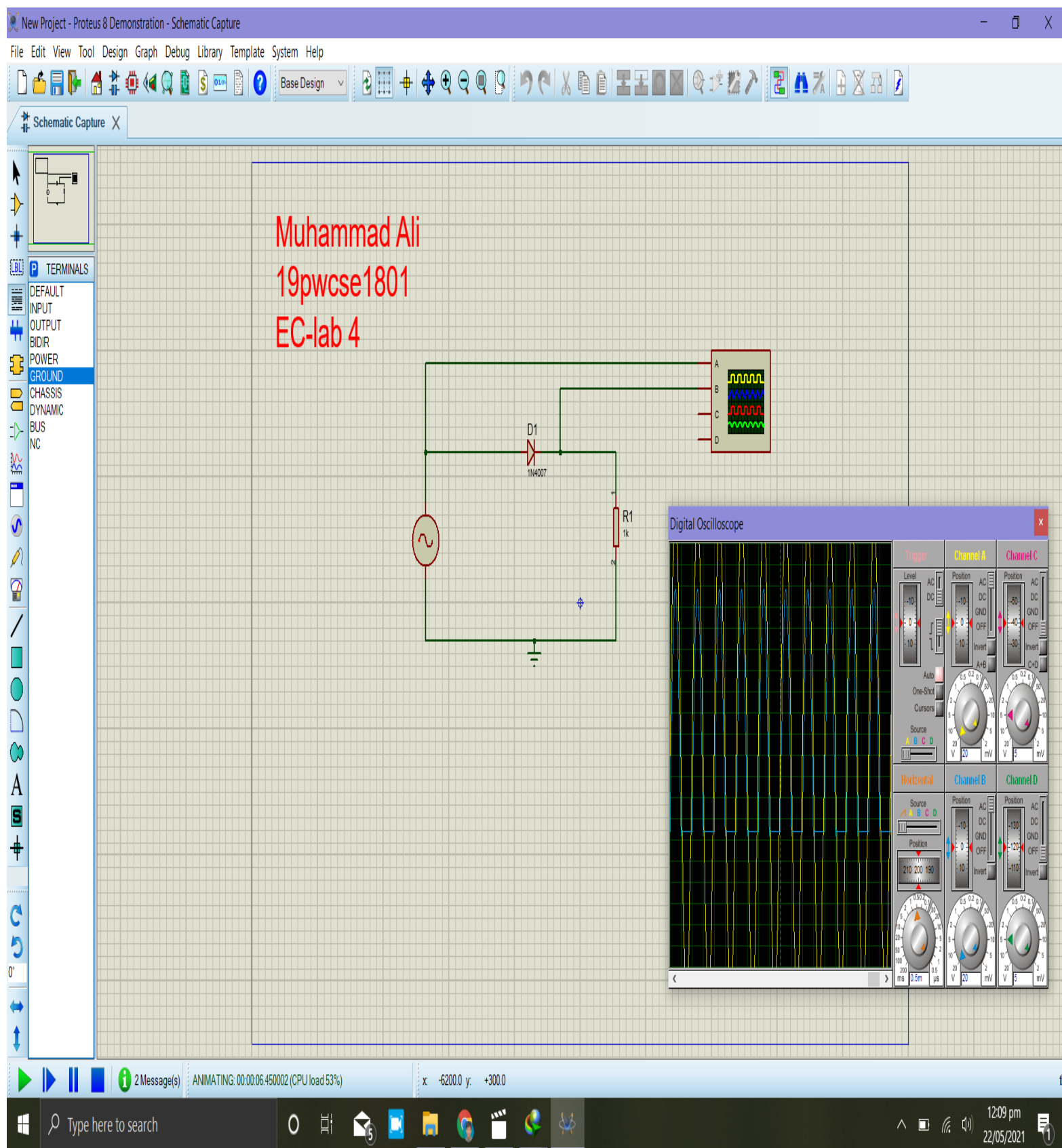
**Engr. Abdullah Hamid**

Data:(22,05,2021)

Department of Computer Systems Engineering

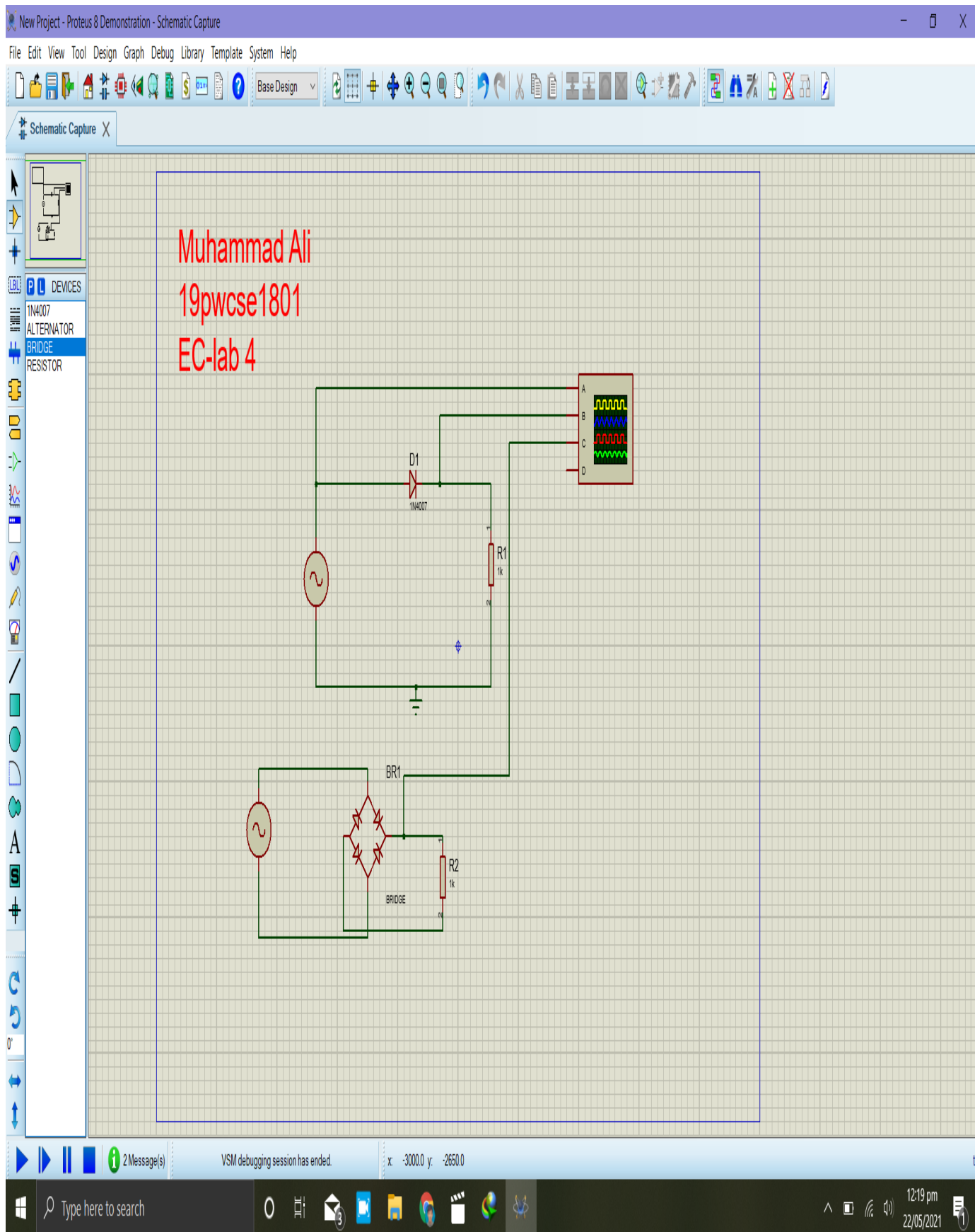
University of Engineering and Technology, Peshawar

## Half wave rectifier: -



The screenshot displays the Proteus 8 Digital Oscilloscope interface. The main window shows a digital waveform on a grid. The left sidebar contains a 'Schematic Capture' window and a 'TERMINALS' list. The right sidebar contains six control panels for Trigger, Channel A, Channel C, Horizontal, Channel B, and Channel D. The status bar at the bottom shows 'ANIMATING: 00:00:21.200002 (CPU load 53%)' and coordinates 'x -4500.0 y +2750.0'.

## Full wave rectifier: -



## Oscilloscopes view: -

