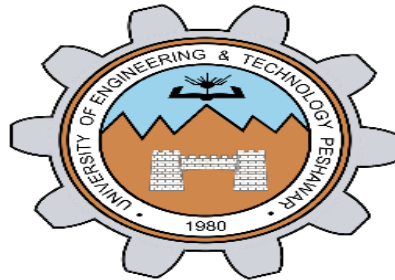


LAB REPORT NO 4



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CS-II lab

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Lab 4: Series R, L, C Circuits

This exercise examines the voltage and current relationships in series R, L, C networks. Of particular importance is the phase of the various components and how Kirchhoff's Voltage Law is extended for AC circuits. Both time domain and phasor plots of the voltages are generated.

Theory Overview

Each element has a unique phase response: for resistors, the voltage is always in phase with the current, for capacitors the voltage always lags the current by 90 degrees, and for inductors the voltage always leads the current by 90 degrees. Consequently, a series combination of R, L, and C components will yield a complex impedance with a phase angle between +90 and -90 degrees. Due to the phase response, Kirchhoff's Voltage Law must be computed using vector (phasor) sums rather than simply relying on the magnitudes. Indeed, all computations of this nature, such as a voltage divider, must be computed using vectors.

Equipment

1. AC Function Generator
2. Oscilloscope

Components

1. 10 nF actual: _____
2. 10 mH actual: _____
3. 1 k Ω actual: _____

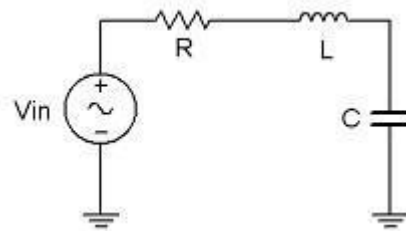


Figure 1

Procedure

1. Using Figure 1 with $V_{in}=2V_{p-p}$ sine at 10 kHz, $R=1k\Omega$, $L=10mH$ and $C=10nF$, determine the theoretical inductive and capacitive reactance and circuit impedance, and record the results in Table 1 (the experimental portion of this table will be filled out in step 4). Using the voltage divider rule, compute the resistor, inductor and capacitor voltages and record them in Table 2.
2. Build the circuit of Figure 1 using $R=1k\Omega$, $L=10mH$ and $C=10nF$. Set the generator to a 10 kHz sine wave and $2 V_{p-p}$. Using oscilloscope measure the signals. Unfortunately, it is impossible to see the voltages of all the three components simultaneously using only two probes of the oscilloscope. To obtain the proper readings, place one probe on the function generator to see the input signal and the second probe across the last element. This step is repeated three times. The first time the components are so arranged that capacitor is the last component, the second time inductor is connected as the last component and finally resistor is made the last component. The peak-to-peak voltages and phase angles of each one of the three components, relative to the source are thus determined in turn. Thus V_s , V_C , V_L and V_R are measured. Record in Table 2.



3. Compute the deviations between the theoretical and experimental values of Table 2 and record the results in the final columns of this table.
4. Based on the experimental values, determine the experimental Z , X_L and X_C values via Ohm's Law ($i=V_R/R$, $X_L=V_L/i$, $X_C=V_C/i$, $Z=V_{in}/i$) and record back in Table 1 along with the deviations.
5. Create a phasor plot showing V_{in} , V_L , V_C , and V_R .
6. Repeat the experiment for 1nF capacitor, 1mH inductor and 1k Ω resistor.

Data Tables for 10nF capacitor and 10mH inductor:-

	Theoretical	Experimental	%Deviation
X_C	1592.3	1632.6	2.5%
X_L	628	680.2	8.3%
Z	1389.19	1490	7.2%
θ	-40.6	-44.7	8.9%

Theoretical current	Experimental current
143mA	1.47mA

Table 1

	Theoretical	Experimental	%Deviation
V_C	2.27	2.39	5.2%
V_L	0.89	0.96	7.8%
V_R	1.43	1.47	2.7%
V_S	1.98	2	1%
θ	47.72	45	2.6%

Table 2

Data Tables for 1nF capacitor, 1ohm resistor and 1mH inductor:-

	Theoretical	Experimental	%Deviation
X_C	159235	176990	9.8%
X_L	6.28	7.03	10%
Z	159238.7	176982.97	10%
θ	-44.7	-44.9	0.4%

Theoretical current	Experimental current
0.012mA	0.0113mA

Table 1

	Theoretical	Experimental	%Deviation
V_C	2	1.91	4.5%
V_L	0.00008	0.00007	11%
V_R	0.11	0.12	8%
V_S	1.96	2	4.2%
θ	86.6	86.3	3%

Questions

1. **What is the phase relationship between R, L, and C components in a series AC circuit?**

Answer:-

phase relationship between R, L, and C components in a series AC circuit is differentiated by 90 degree.

2. **Based on measurements, does Kirchhoff's Voltage Law apply to the tested circuits?**

Answer:-

Yes, Kirchhoff's Voltage Law does apply to the circuit because sum of voltage across resistor, capacitor and inductor is equal to zero.

3. **In general, how would the phasor diagram of Figure 1 change if the frequency was raised?**

Answer:-

The frequency of capacitor phasor diagram is decreases and frequency of inductor phasor diagram is increases with raised in frequency. Because inductance has direct and capacitance inverse relation with frequency.

4. **In general, how would the phasor diagram of Figure 1 change if the frequency was lowered?**

Answer:- The frequency of capacitor phasor diagram is increases and frequency of inductor phasor diagram is decreases with lowered in frequency. Because inductance has direct and capacitance inverse relation with frequency.