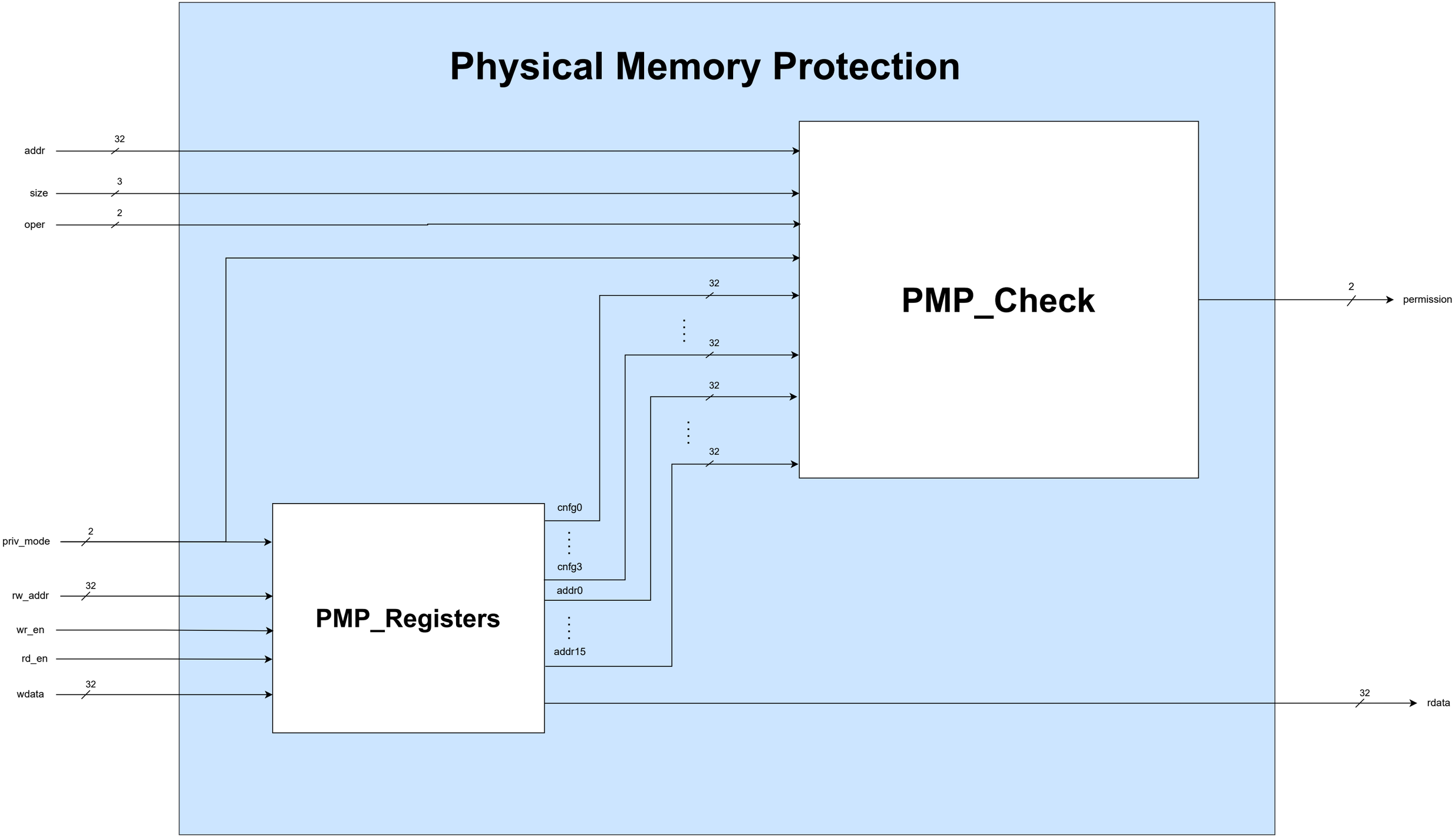
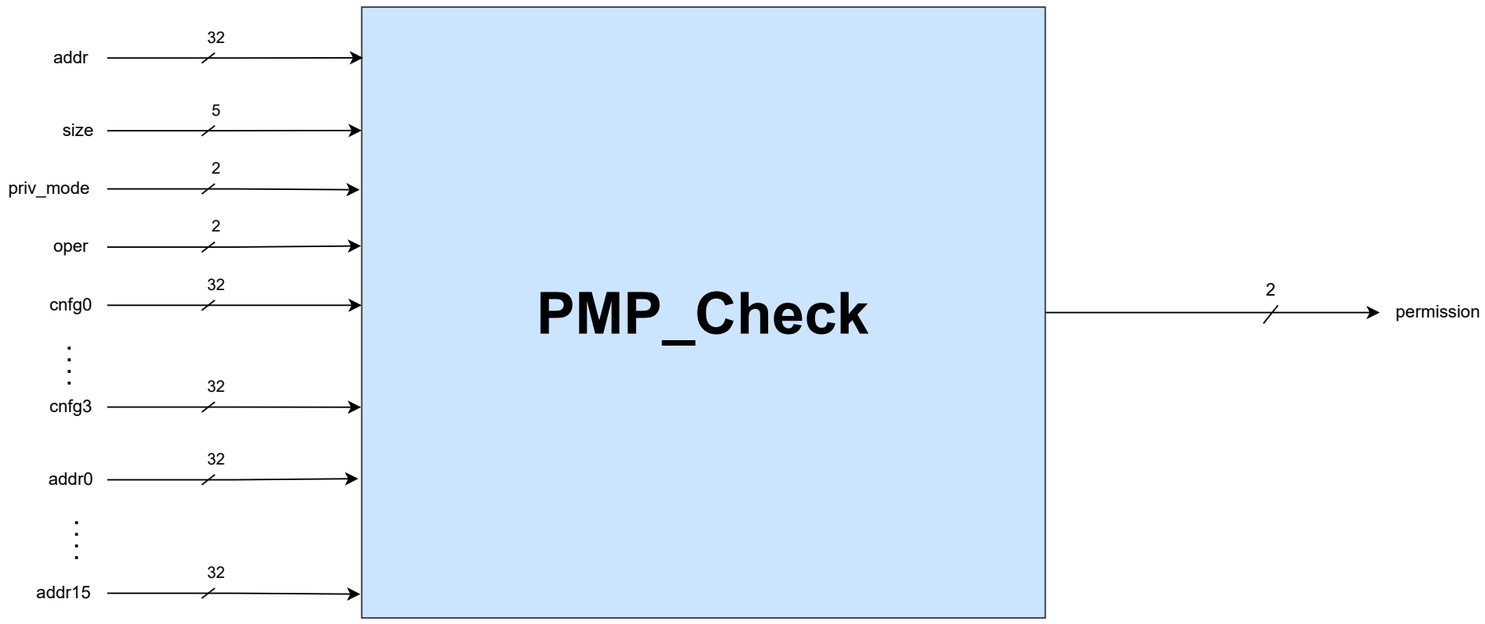
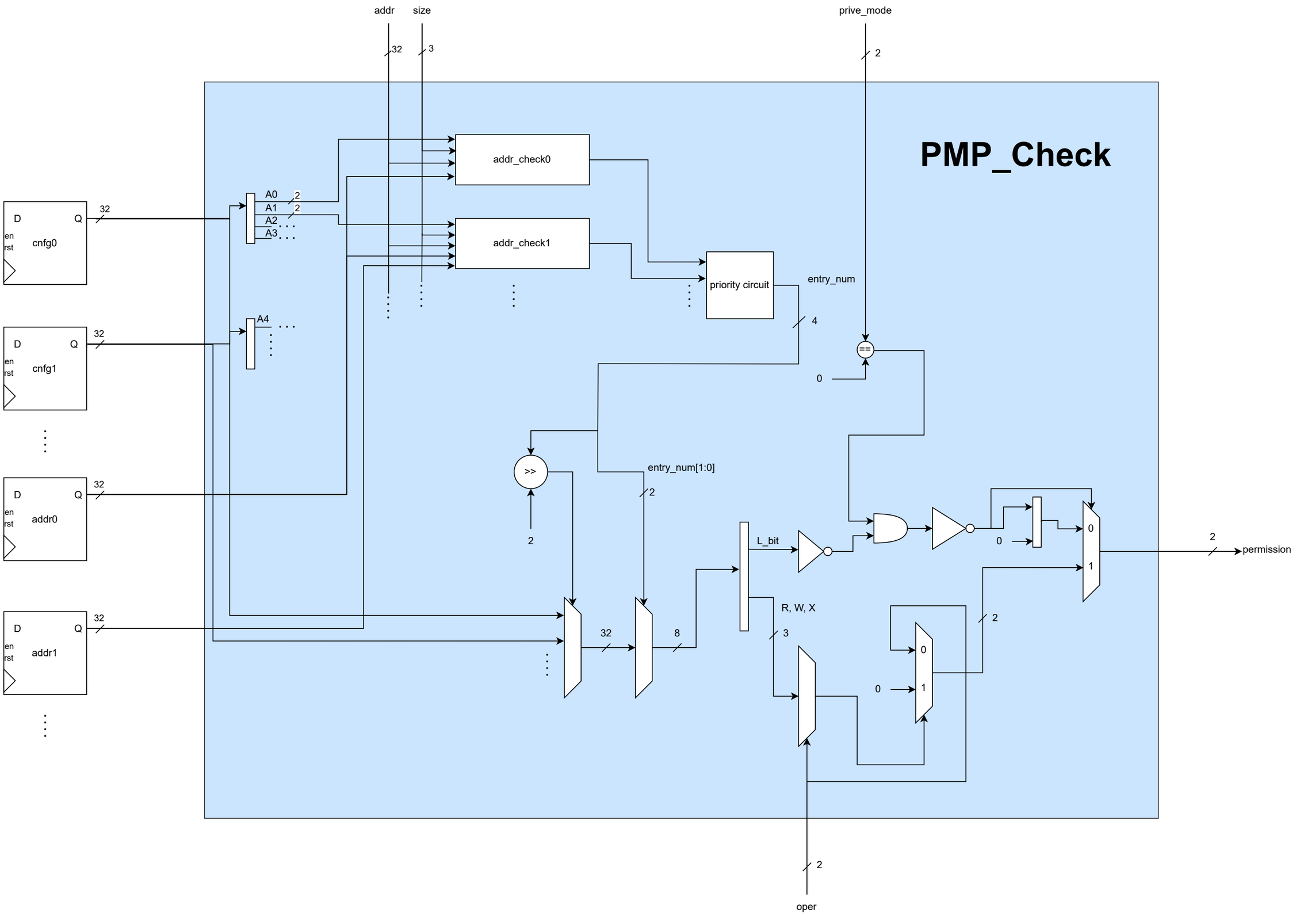
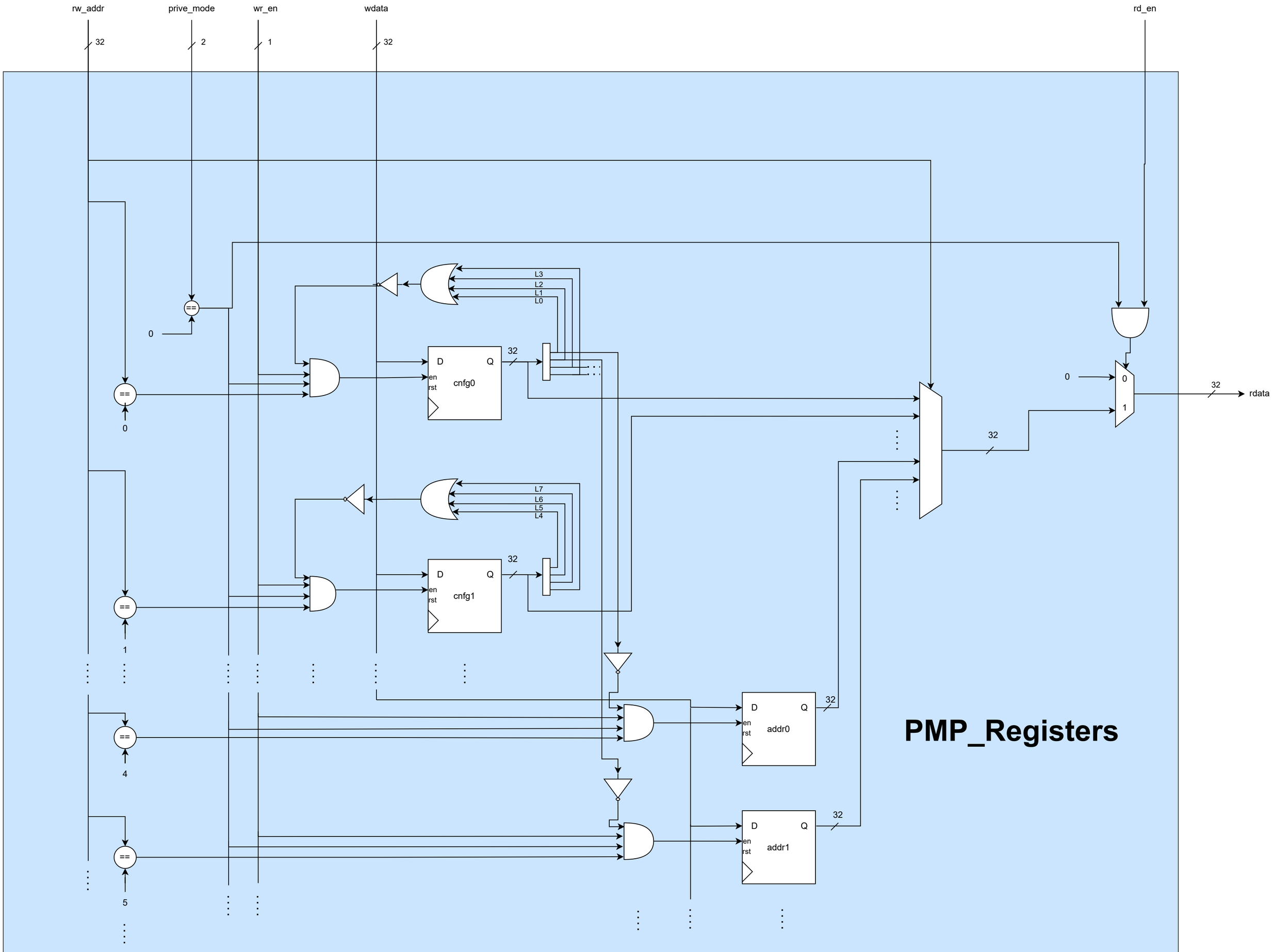

Physical Memory Protection
addr
32
size
3
oper
2
PMP_Check
2
permission
32
32
32
32
cnfg0
cnfg3
addr0
addr15
priv_mode
2
rw_addr
32
wr_en
rd_en
wdata
32
PMP_Registers
32
rdata

Pinout Diagram

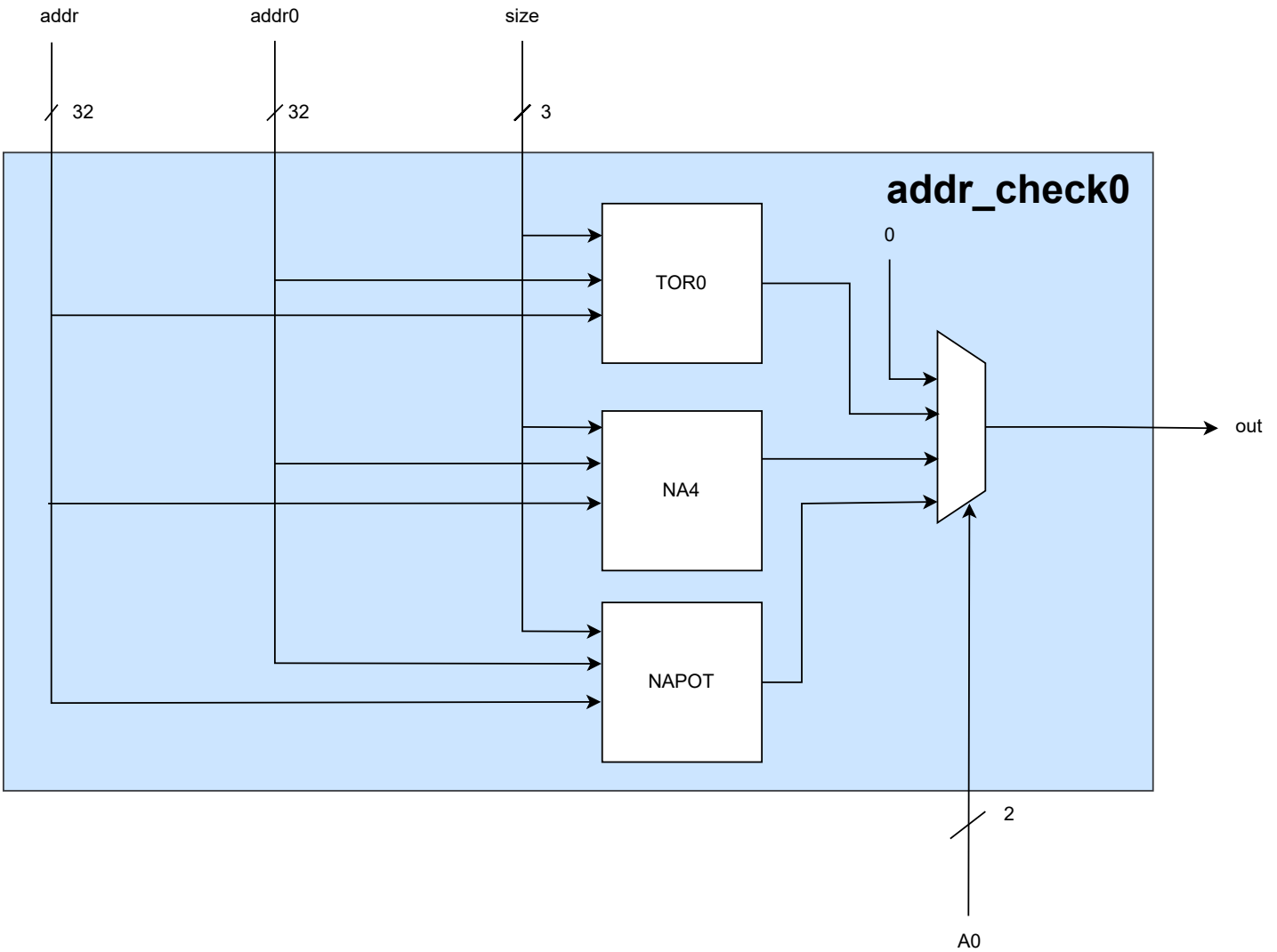




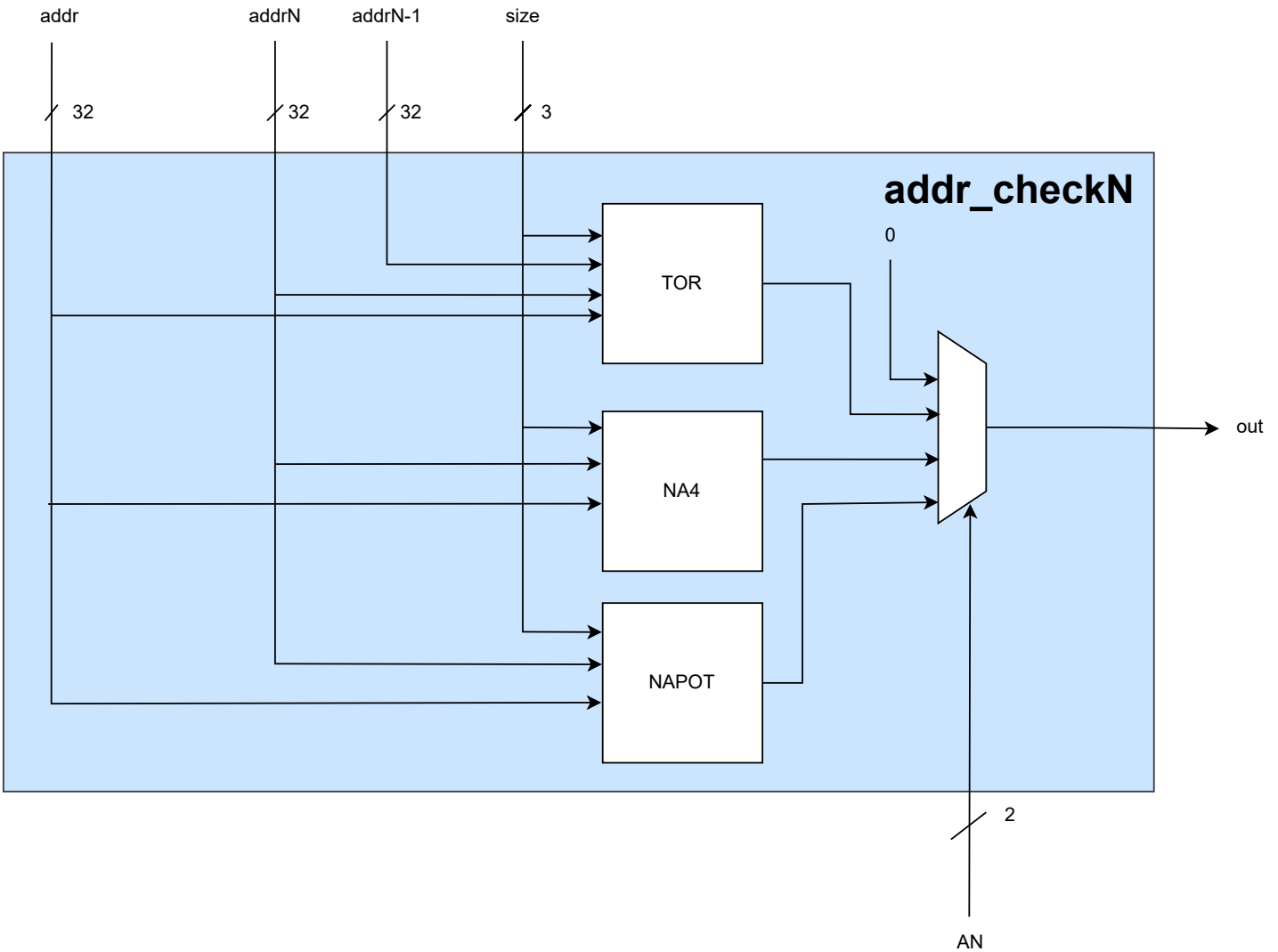
Pinout Diagram

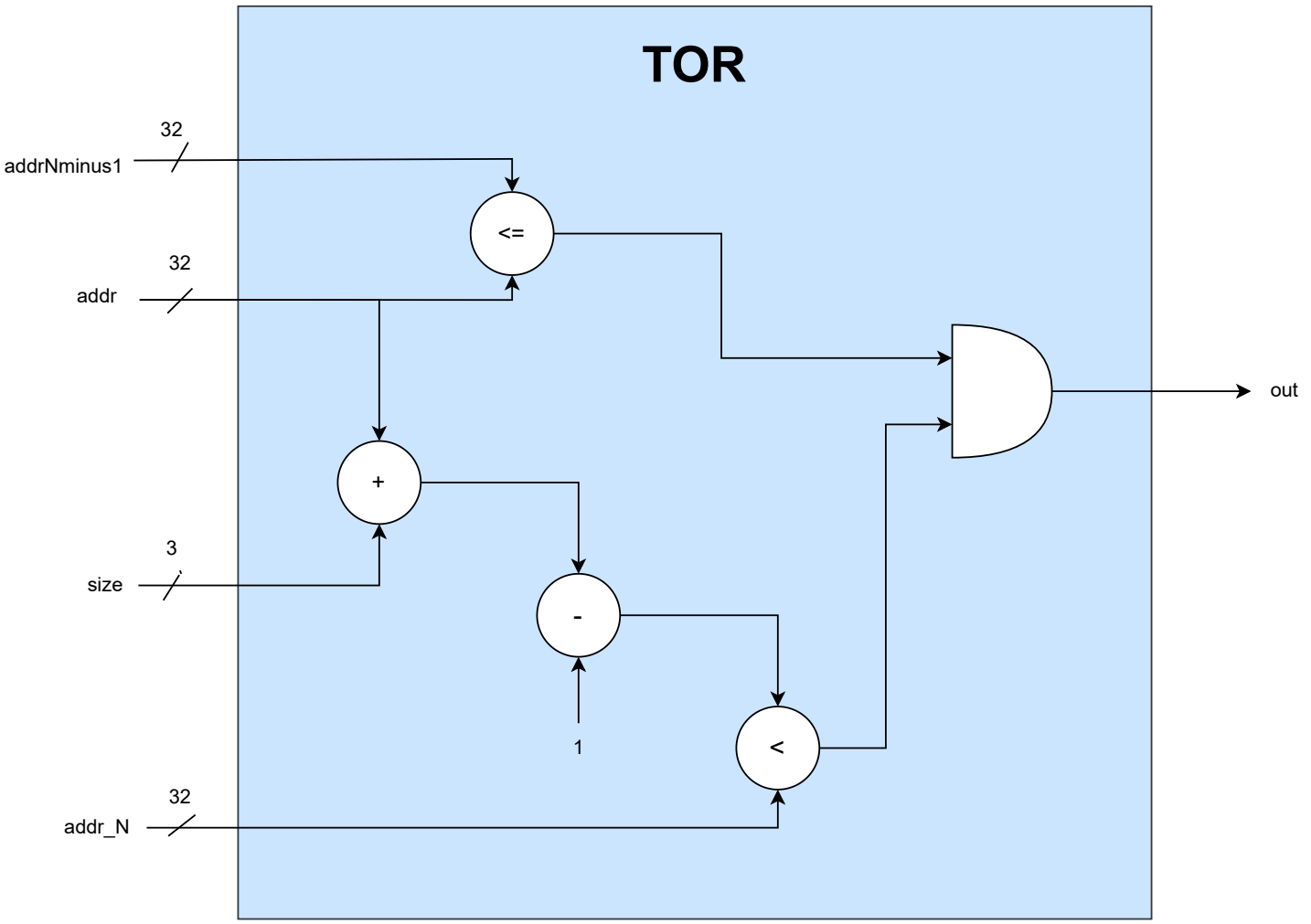


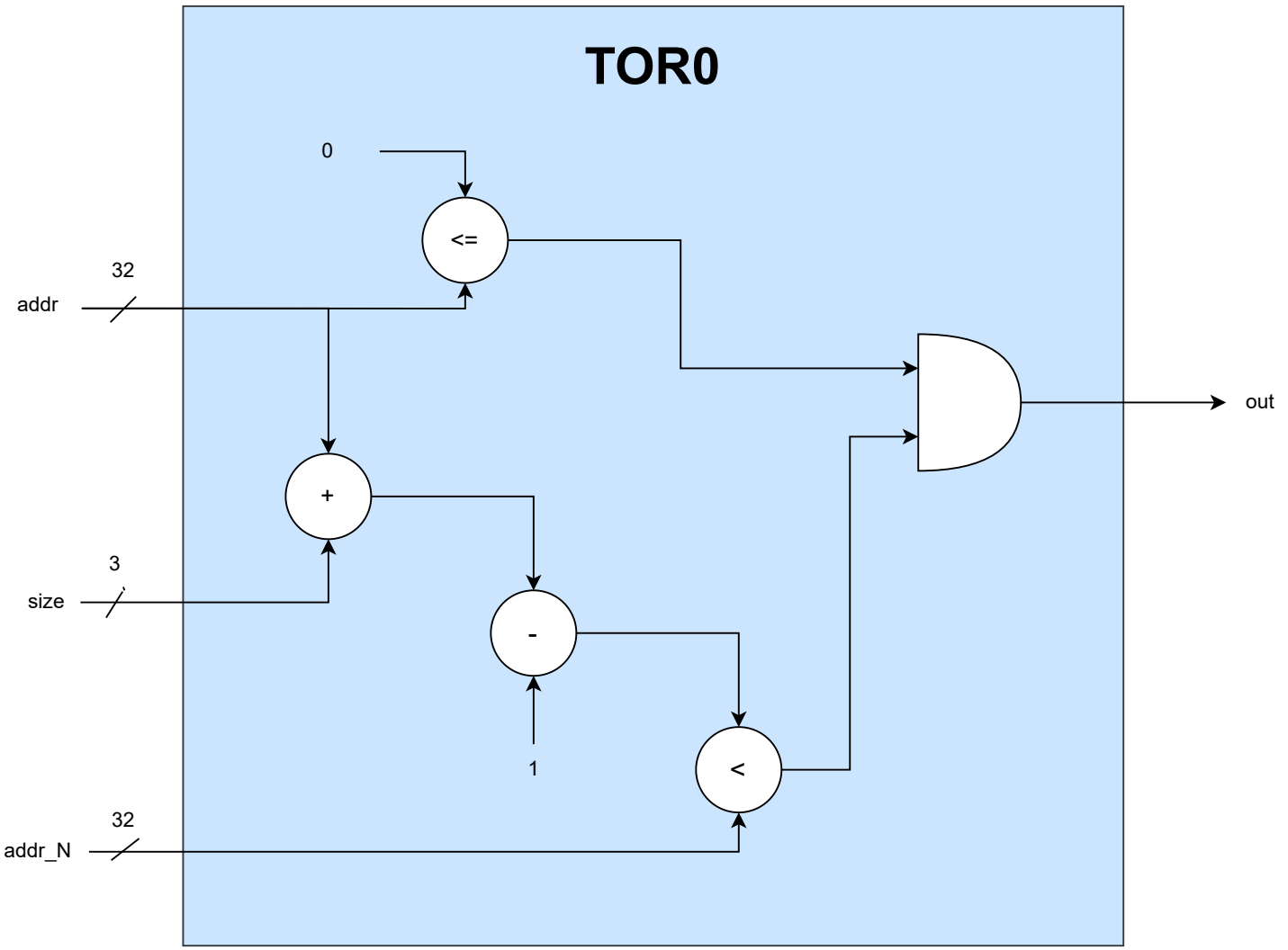
Pinout Diagram

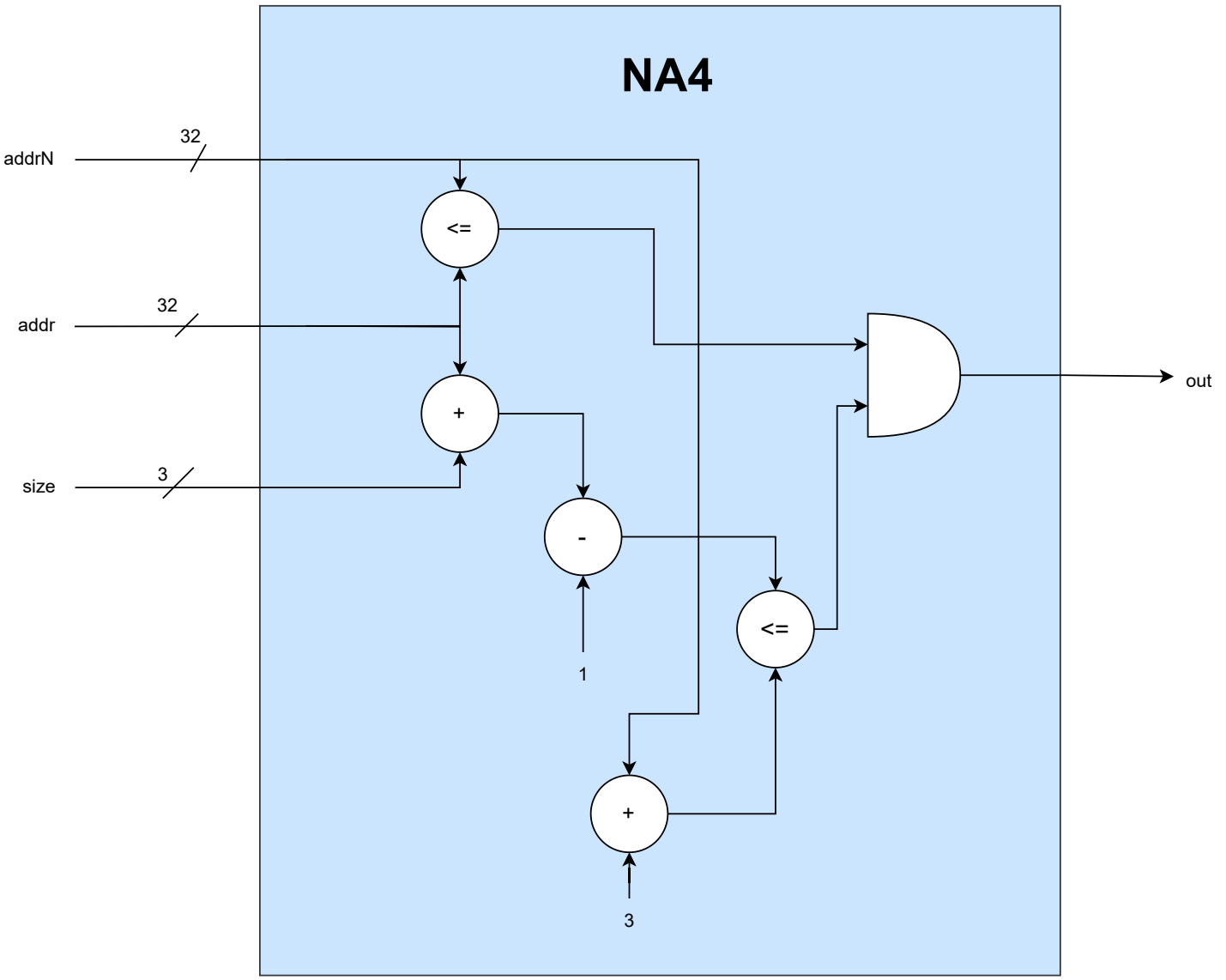


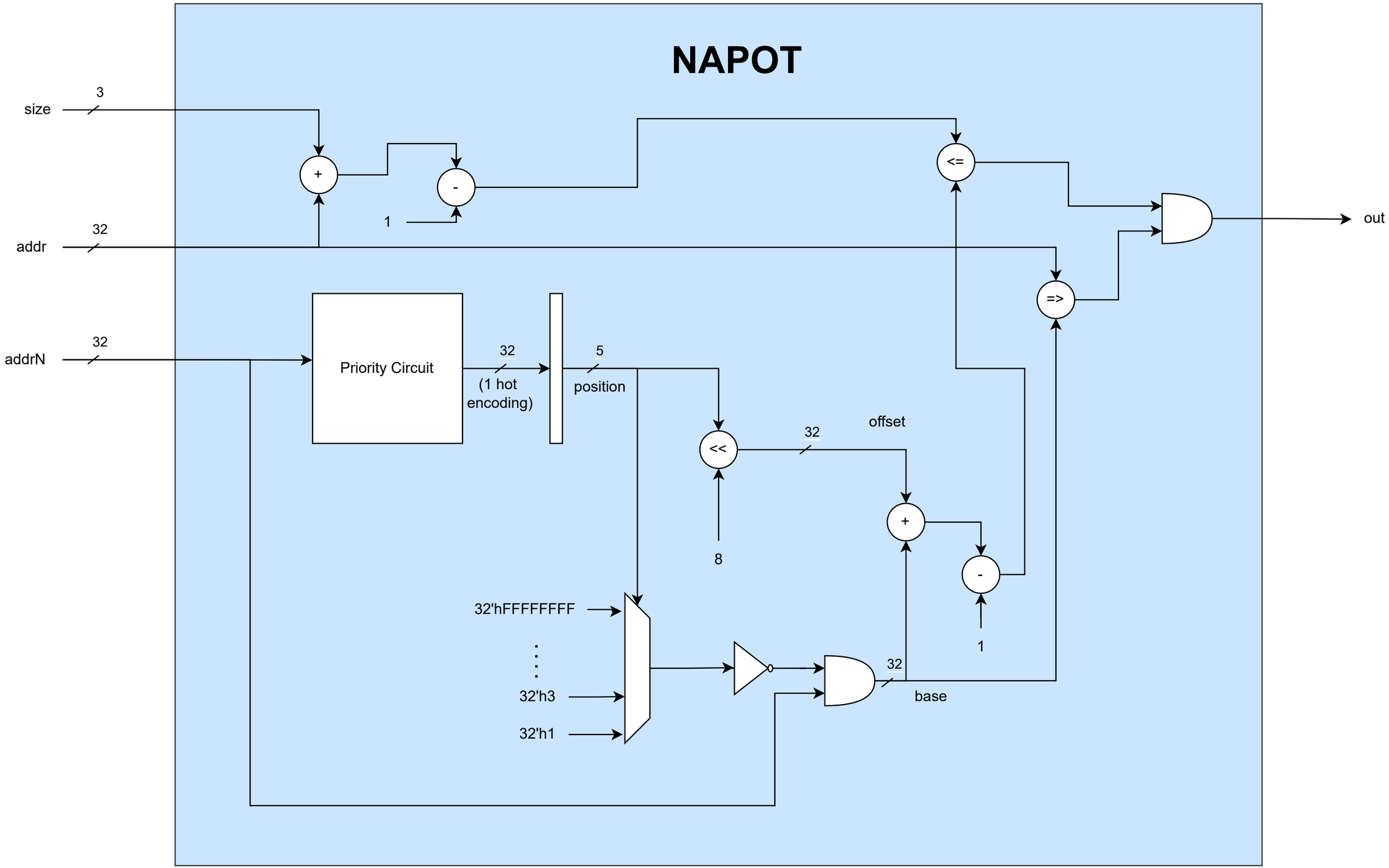
Pinout Diagram




TOR
32
addrNminus1
<=
32
addr
+
3
size
-
1
<
32
addr_N
out


TOR0
0
<=
32
addr
+
3
size
-
1
<
32
addr_N
out


NA4
addrN
32
addr
32
size
3
<=
+
-
1
<=
+
3
out


NAPOT
size
3
addr
32
addrN
32
+
-
1
<=
=>
out
Priority Circuit
32
(1 hot
encoding)
5
position
<<
32
offset
8
+
-
1
32'hFFFFFFFF
32'h3
32'h1
32
base