

Lab 01: UVM Environment Setup to Display "Hello UVM"

Objective: To Learn the basic structure of a UVM testbench and display "Hello UVM" using UVM reporting.

Step 1:

- Create a UVM Test Class
- Create a new file named `hello_uvm_test.sv`.
- Extend the `hello_uvm_test` class from `uvm_test`.
- Register the class with the factory using `uvm_component_utils`.
- Use the `uvm_info` macro to print "Hello UVM" inside the `run_phase()`.

Step 2:

- Create another file named `top.sv`.
- Include the `hello_uvm_test.sv` file.
- Use an initial block to call `run_test("hello_uvm_test")`.
- Include the required uvm macros and package files by online searching.

Step 3:

- Use (VCS or Xcelium) to compile the files.
- Run the test and observe the output.