

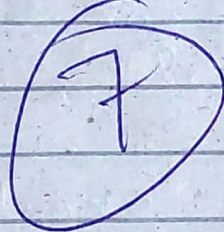
## COA Lab Quiz no 1

Name: Aimal Khan  
Reg #: 21 pwece 1996  
Section: "A"  
Date: 13 oct, 2023.

Q1: If  $m$  select line is  $m$  bits, how many input lines are there?

Ans: If we have  $m$  select line. then  $n$  are input lines so

$$2^m = n$$



Q2: If RAM has  $n$  locations, what is the width of address bus?

Ans:  $m$



Q3:

Q3: If register has 32 bits to store, should the output be based on clear edge or level of clock signal?

Ans: It should be edge based on ~~level~~ of clock.