	COA Lab Quizz no 1	
	Name: Aimal Khan	
	Reg #: 21 peucse 1996	
	Sections A	
	Date 13 oct, 2023.	
	Q1: If mun select line is	in
	bits, how many input gi	
	cere there:	
	Ans: If we have 'm' sele	c l'
	line. Her 'n' are i	
		тера
	Junes 50	
	$2^{m}=m$	
	Q2: If RAM has in Joration	5.
9	what is the width of	
	Corear 15 fee coron	
	address buss	
	address bus?	
	address bus? Ans: m	

	The state of the s	
* HA		7,
	Q3:	
	Committee of the Commit	- 6
	Q3:1f register has 32	1
	bits to store, should the	2
	output be based on C	ar .
	output be based on clock	ar
	edge or Jevel of clock	arr
	edge or Jevel of clock signel?	asv
	edge or Jevel of clock signel? Ans: It should be edge	arr
	edge or Jevel of clock signel? Ans: It should be edge based on Jevel of	arr
	edge or Jevel of clock signel? Ans: It should be edge	ar
	edge or Jevel of clock signel? Ans: It should be edge based on Jevel of	arr
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	edge or Jevel of clock signel? Ans: It should be edge based on Jevel of	ar
	edge or Jevel of clock signel? Ans: It should be edge based on Jevel of	acr
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	edge or Jevel of clock signel? Ans: It should be edge based on Jevel of	ar
	edge or Jevel of clock signel? Ans: It should be edge based on Jevel of	acr