**TITLE OF LAB, CENTERED, IN ALL CAPITALS,**

**AND ON MULTIPLE LINES (AS NEEDED)**

**OF DECREASING LENGTH**

**LAB # 00**

**Fall 2023**

**CSE-402L**

**Digital Signal Processing Lab**

Submitted by: **AIMAL KHAN**

Registration No.: **21PWCSE1996**

Class Section: **A**

“On my honor, as student of University of Engineering and Technology, I have neither given nor received unauthorized assistance on this academic work.”



Student Signature: \_\_\_\_\_\_\_\_\_\_\_\_\_\_

Submitted to:

**Dr. Yasir Saleem Afridi.**

Wednesday, September 27, 2023

Department of Computer Systems Engineering

University of Engineering and Technology, Peshawar

**CSE 402L: Digital Signal Processing**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Demonstration of Concepts** | **Poor (Does not meet expectation (1))**  The student failed to demonstrate a clear understanding of the assignment concepts | **Fair (Meet Expectation (2-3))**  The student demonstrated a clear understanding of some of the assignment concepts | **Good (Exceeds Expectation (4-5)**  The student demonstrated a clear understanding of the assignment concepts | **Score**  **30%** |
| **Accuracy** | The student completed ( <50%) tasks and provided MATLAB code and/or Simulink models with errors. Outputs shown are not correct in form of graphs (no labels) and/or tables along with incorrect analysis or remarks. | The student completed partial tasks (50% - <90%) with accurate MATLAB code and/or Simulink models. Correct outputs are shown in form of graphs (without labels) and/or tables along with correct analysis or remarks. | The student completed all required tasks (90%-100%) with accurate MATLAB code and/or Simulink models. Correct outputs are shown in form of labeled graphs and/or tables along with correct analysis or remarks. | **30%** |
| **Following Directions** | The student clearly failed to follow the verbal and written instructions to successfully complete the lab | The student failed to follow the some of the verbal and written instructions to successfully complete all requirements of the lab | The student followed the verbal and written instructions to successfully complete requirements of the lab | **20%** |
| **Time Utilization** | The student failed to complete even part of the lab in the allotted amount of time | The student failed to complete the entire lab in the allotted amount of time | The student completed the lab in its entirety in the allotted amount of time | **20%** |

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Dr. Yasir Saleem Afridi

**Topic Here**

Objectives:

* Objective 1, ….

Tasks:

**Task 1**: Question here …

**Code:**

clc;

clear;

close all;

**Output:**

**Task 2**: Question here …

**Code:**

clc;

clear;

close all;

**Output:**

**Task 3**: Question here …

**Code:**

clc;

clear;

close all;

**Output:**

Reference:

To view my codes, please refer to [my GitHub account](https://github.com/aimalexe/DCSE/tree/main/semester_5_(fall-23)/digital_signal_processing_lab/).

Conclusion:

In conclusion, …

The End.