

ASSESSMENT RUBRICS LAB # 7

Node Voltage Analysis using PSPICE

LAB REPORT ASSESSMENT				
Criteria	Excellent	Average	Nil	Marks Obtained
1. Objectives of Lab	All objectives of lab are properly covered [Marks 0.5]	Objectives of lab are partially covered [Marks 0.25]	Objectives of lab are not shown [Marks 0]	
1. Node Voltage Analysis (Theory, Circuit Diagram)	Brief introduction about Node Voltage Analysis (what is Node voltage analysis, What are nodes, How to apply KCL equations at each node) is shown along with properly labeled circuit diagram [Marks 1]	Some of the points about Node Voltage Analysis are missing and circuit diagram is not properly labeled [Marks 0.5]	Introduction about Node Voltage Analysis and circuit diagram is not shown [Marks 0]	
2. PSPICE Simulator	Brief introduction of PSPICE simulator [Marks 1]	Brief introduction of PSPICE simulator Is not shown [Marks 0]		
3. Procedure	All experimental steps are shown in detail along with how to verify Node Voltage Analysis. [Marks 1.5]	Some of the experimental steps are missing [Marks 1]	Experimental steps are missing [Marks 0]	
4. Observations & Calculations	Mathematical calculations are shown and comparison with PSPICE results. [Marks 5]	Mathematical calculations are shown but no comparison with PSPICE results [Marks 2.5]	No mathematical calculations are shown [Marks 0]	
5. Conclusion	Conclusion about experiment is shown [Marks 1]	Conclusion about experiment is partially shown [Marks 0.5]	Conclusion about experiment is not shown [Marks 0]	
<div style="text-align: right;">Total Marks Obtained: _____</div> <div style="text-align: right;">Instructor Signature: _____</div>				