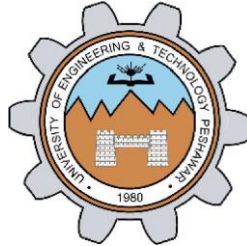


LAB #07

Generating unit impulse and unit step sequences,
Basic signal operations



Spring 2023

CSE-301L Signals & Systems Lab

Submitted by: MUHAMMAD SADEEQ

Registration No.: 21PWCSE2028

Section: C

“On my honor, as a student of the University of Engineering and Technology, I have neither given nor received unauthorized assistance on this academic work”

Submitted to:

Engr. Sumayyea

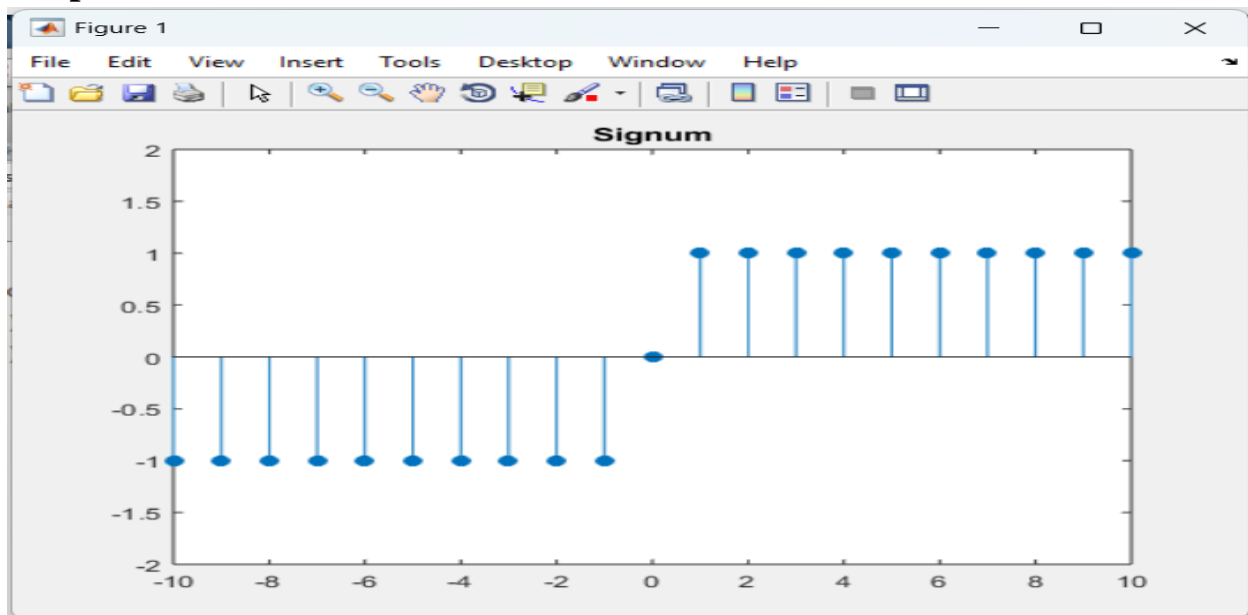
(19 May 2023)

Department of Computer systems engineering
University of Engineering and Technology,
Peshawar

Task 1

Code:

Output:



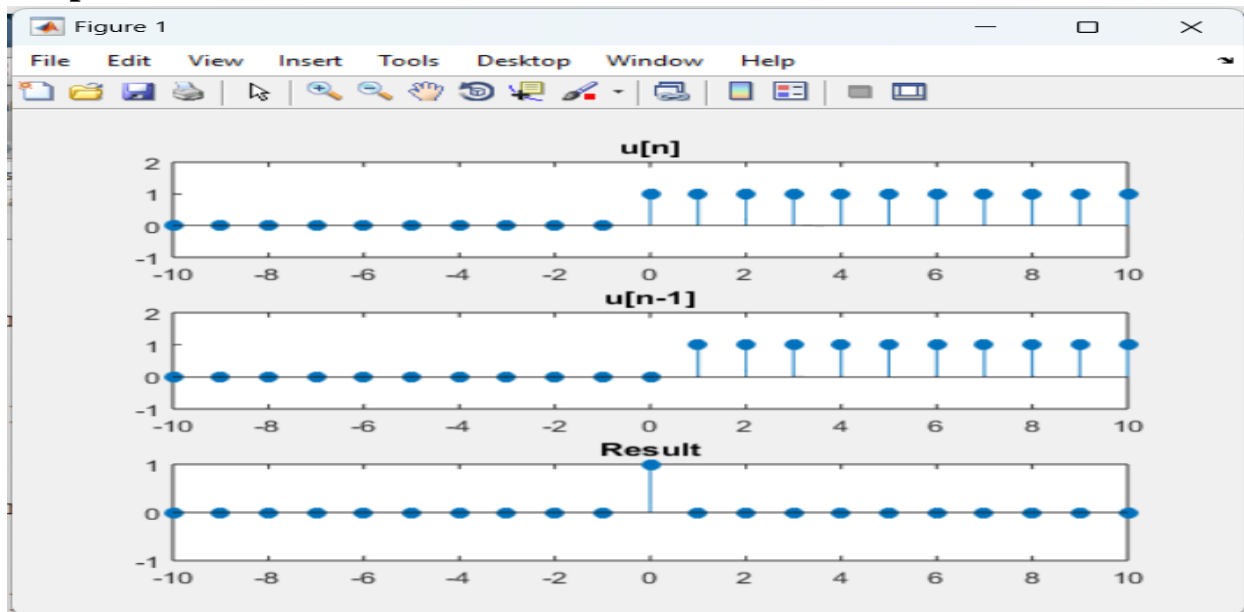
Task 2

Code:

[illegible]

[illegible]

Output:



Task 3

Code:

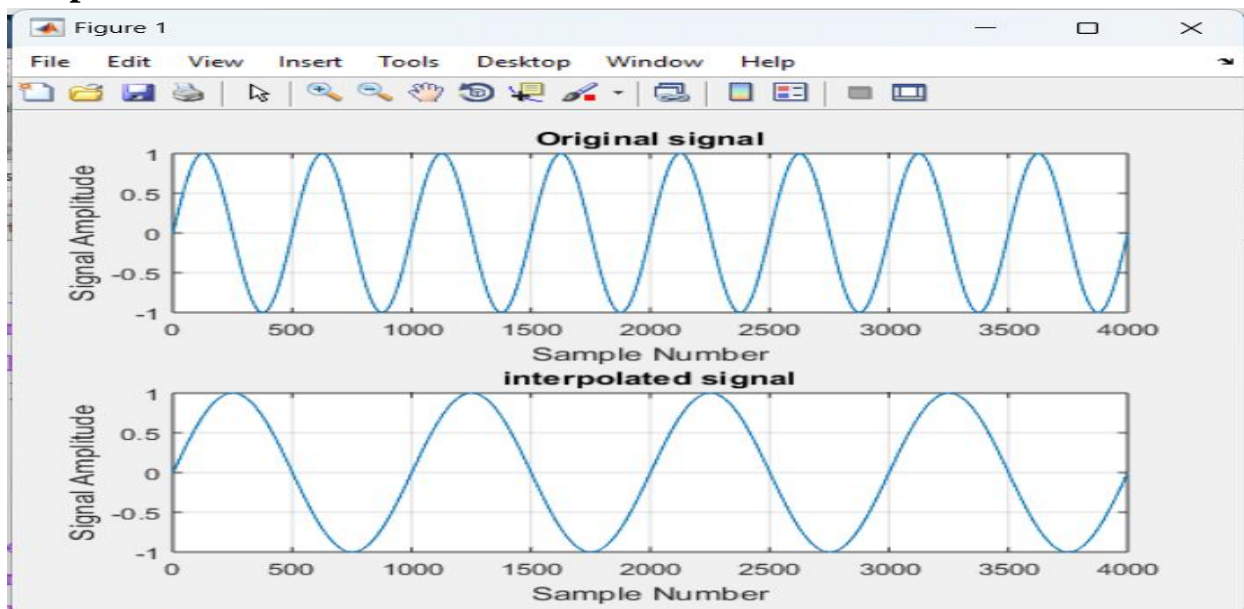
[illegible]

Output:



[illegible]

Output:



[illegible]