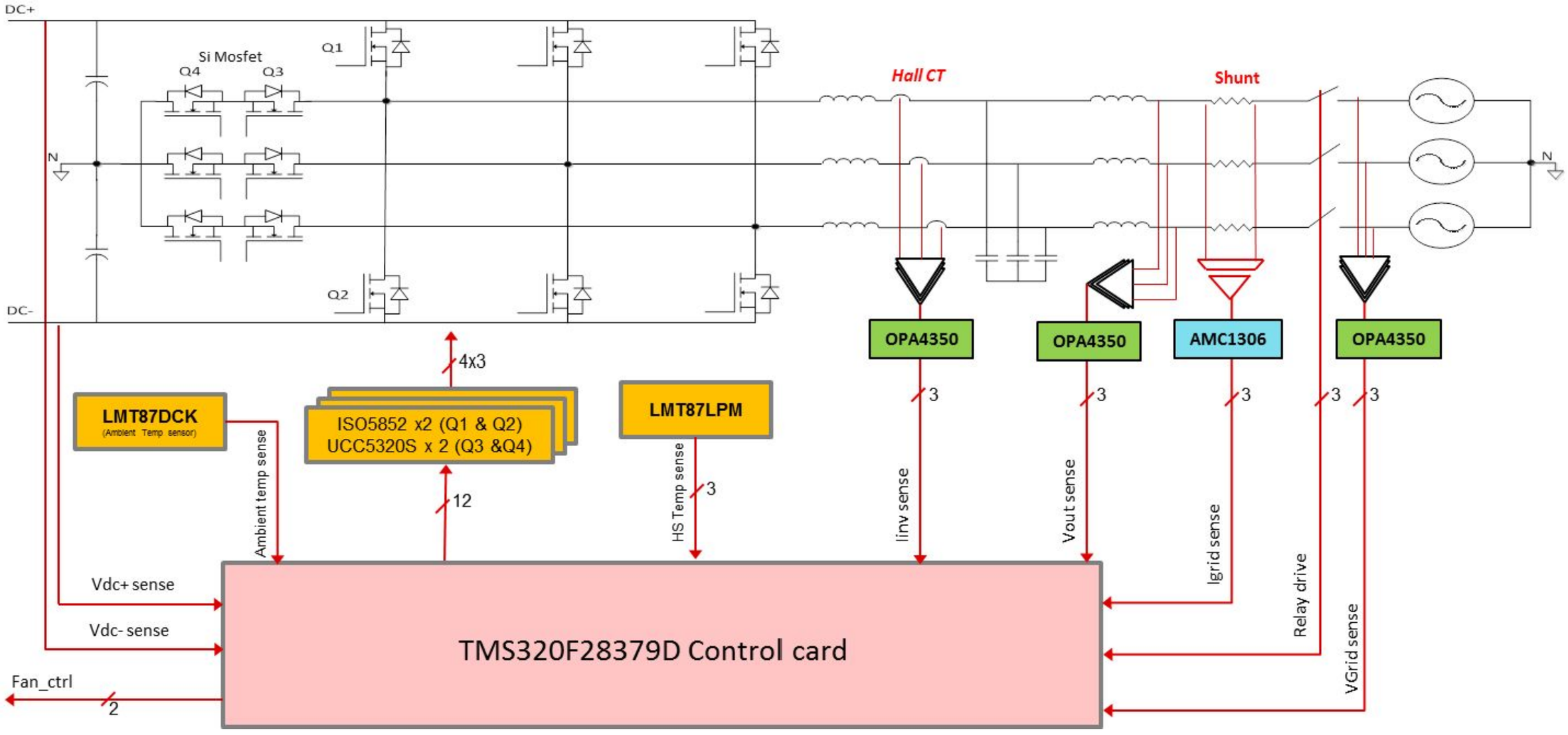
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 12/27/2017
TID #: TIDA-010039	Project Title: 10KW PFC	
Number: TIDA-010039_Power Card	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 7
Drawn By: Murali P	File: TIDA-010039E4_POWERCARD_Block Diagram.SchDoc	Size: B
Engineer: Bart Basile	Contact: http://www.ti.com/support	

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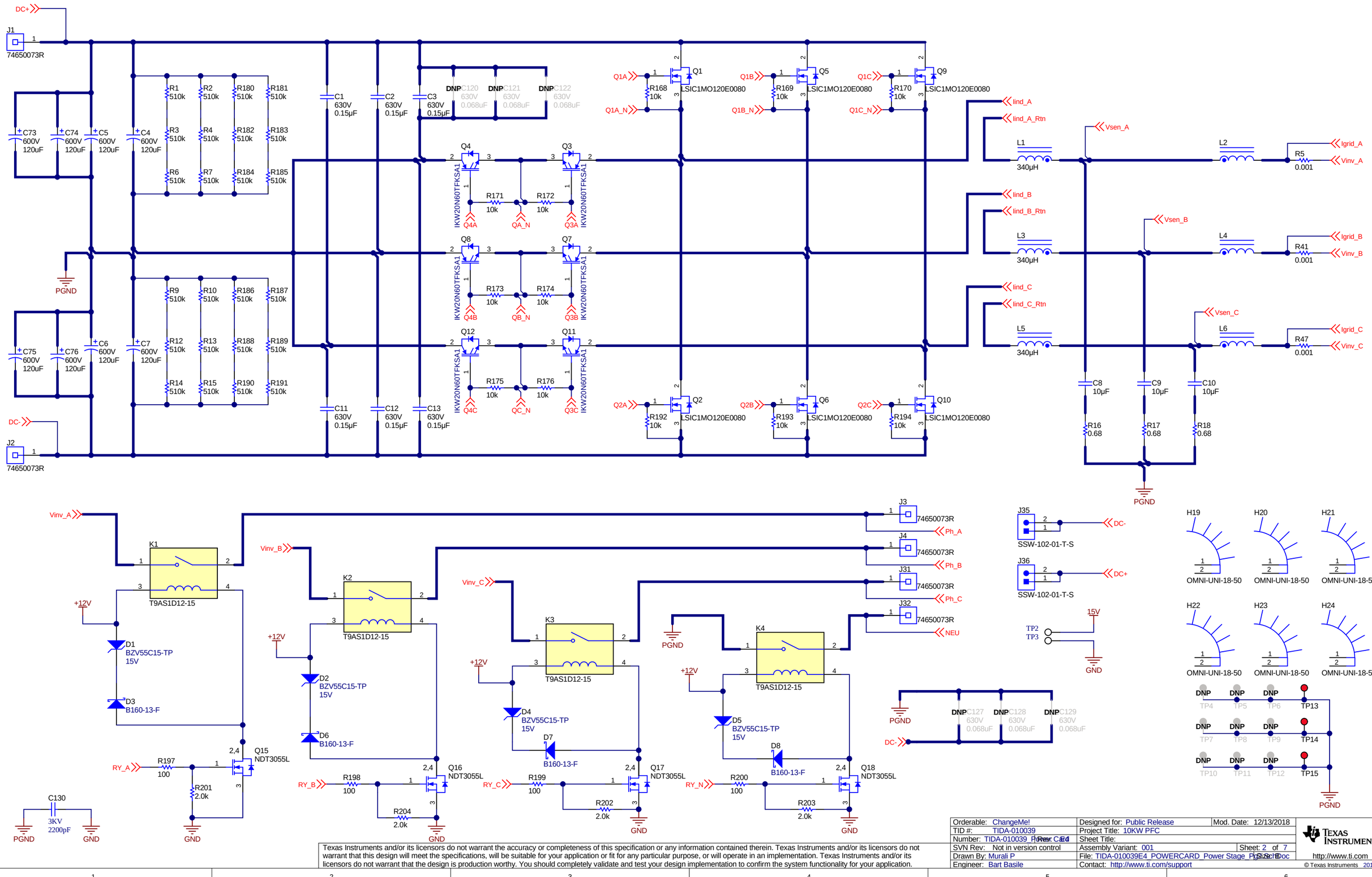

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Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 12/13/2018
TID #: TIDA-010039	Project Title: 10KW PFC	
Number: TIDA-010039_Power Card	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 7
Drawn By: Murali P	File: TIDA-010039E4_POWERCARD_Power Stage_Pg2.SchDoc	
Engineer: Bart Basile	Contact: http://www.ti.com/support	

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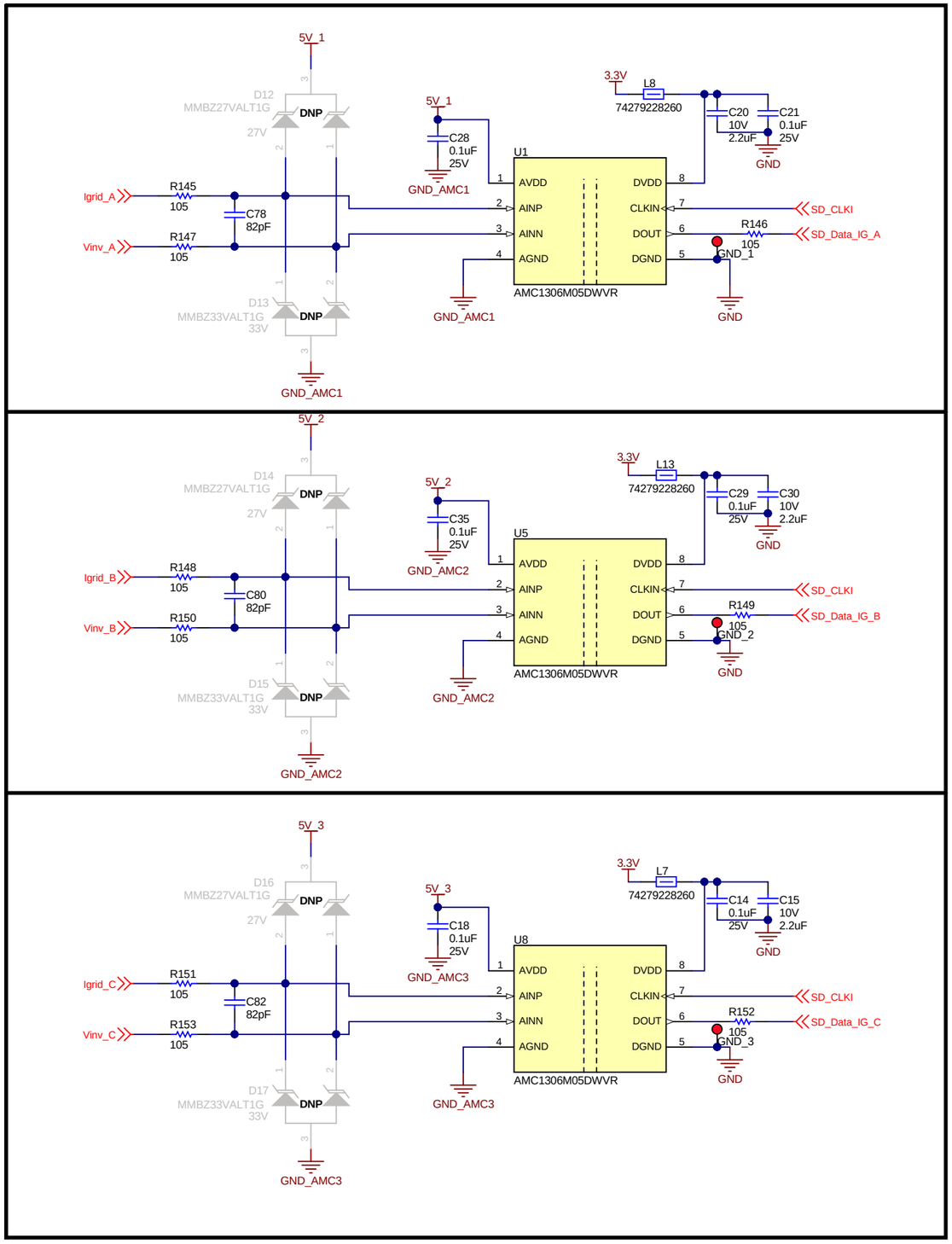

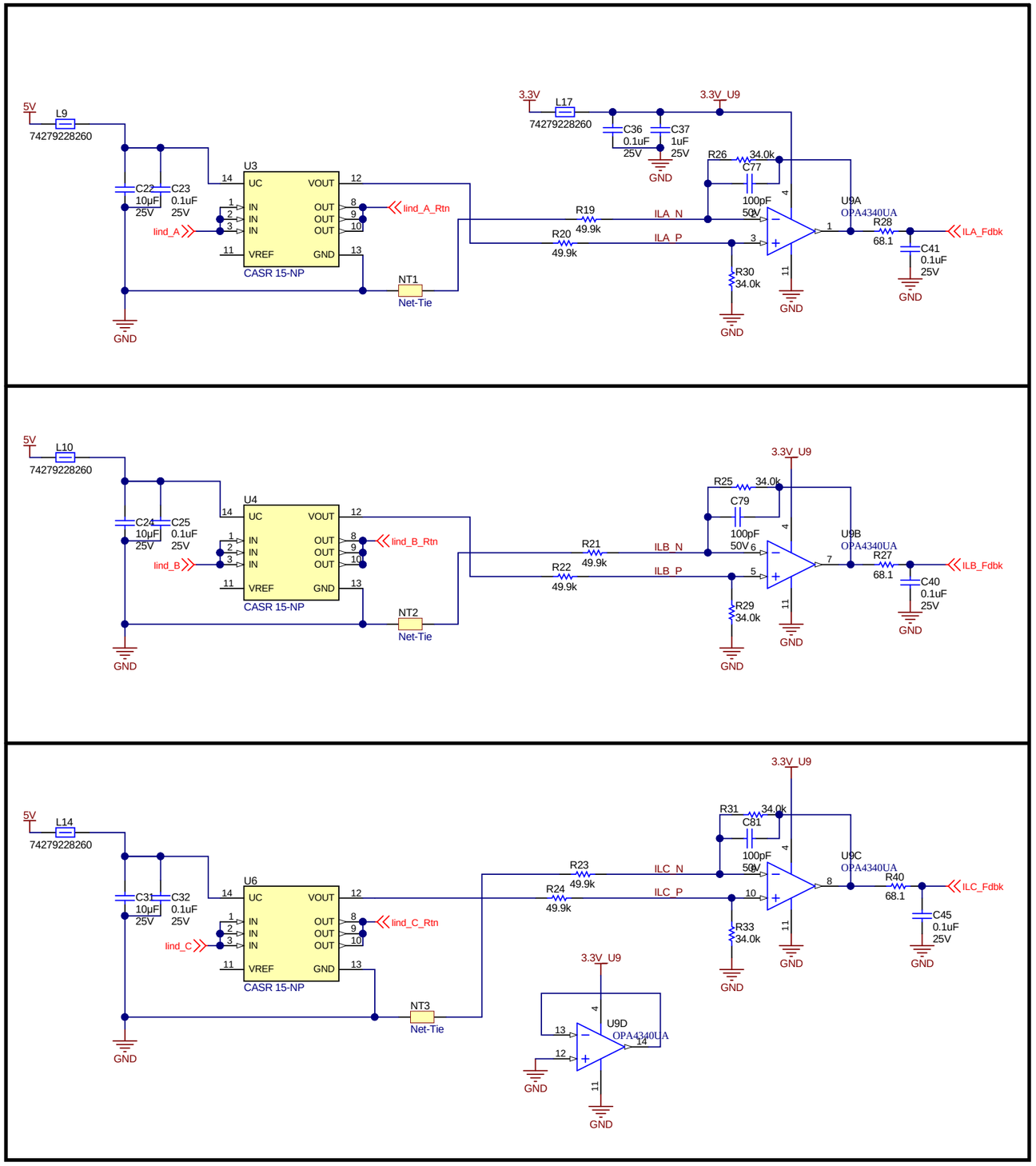

Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 12/14/2018
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Number: TIDA-010039_Rev_C1.1	Sheet Title: Inductor Current Sensing Interface Circuit	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 7
Drawn By: Murali P	File: TIDA-010039E4_POWERCARD_Current Sensing_Rev5.SchDoc	
Engineer: Bart Basile	Contact: http://www.ti.com/support	

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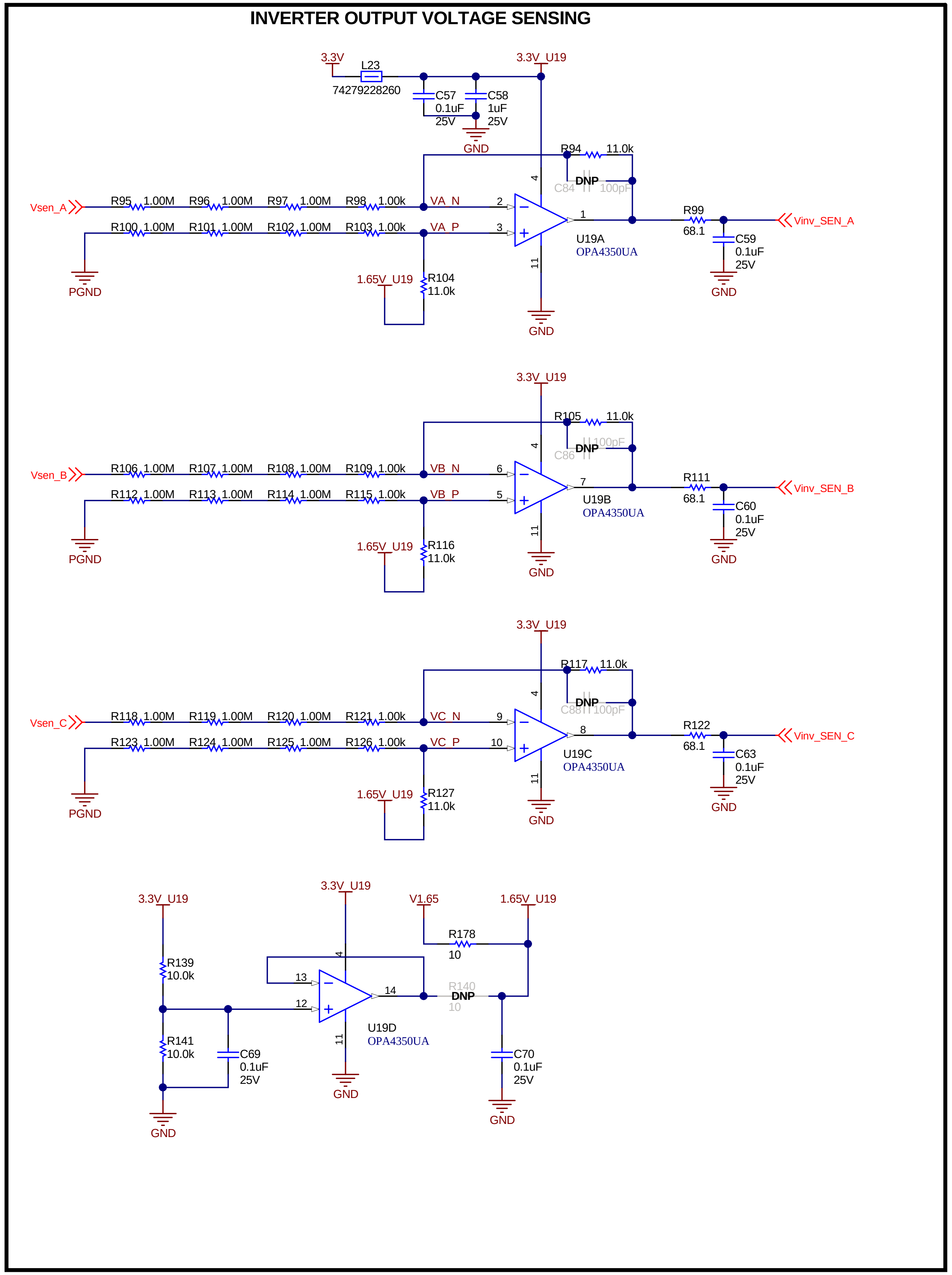

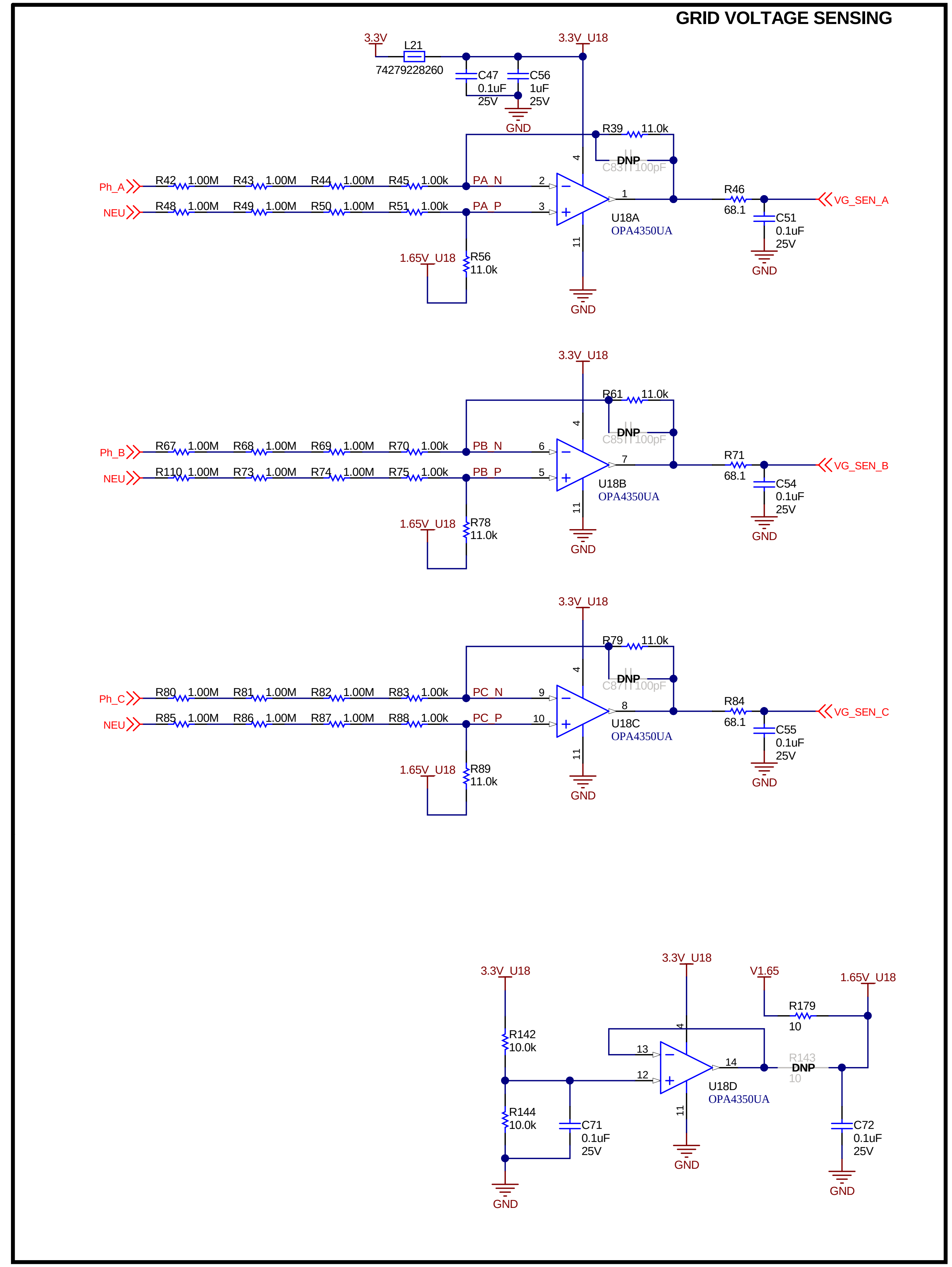

Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 12/13/2018
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Number: TIDA-010039_Power Card Rev: E4	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 7
Drawn By: Murali P	File: TIDA-010039E4_POWERCARD_Volage Sensing Stage.SchDoc	Size: B
Engineer: Bart Basile	Contact: http://www.ti.com/support	

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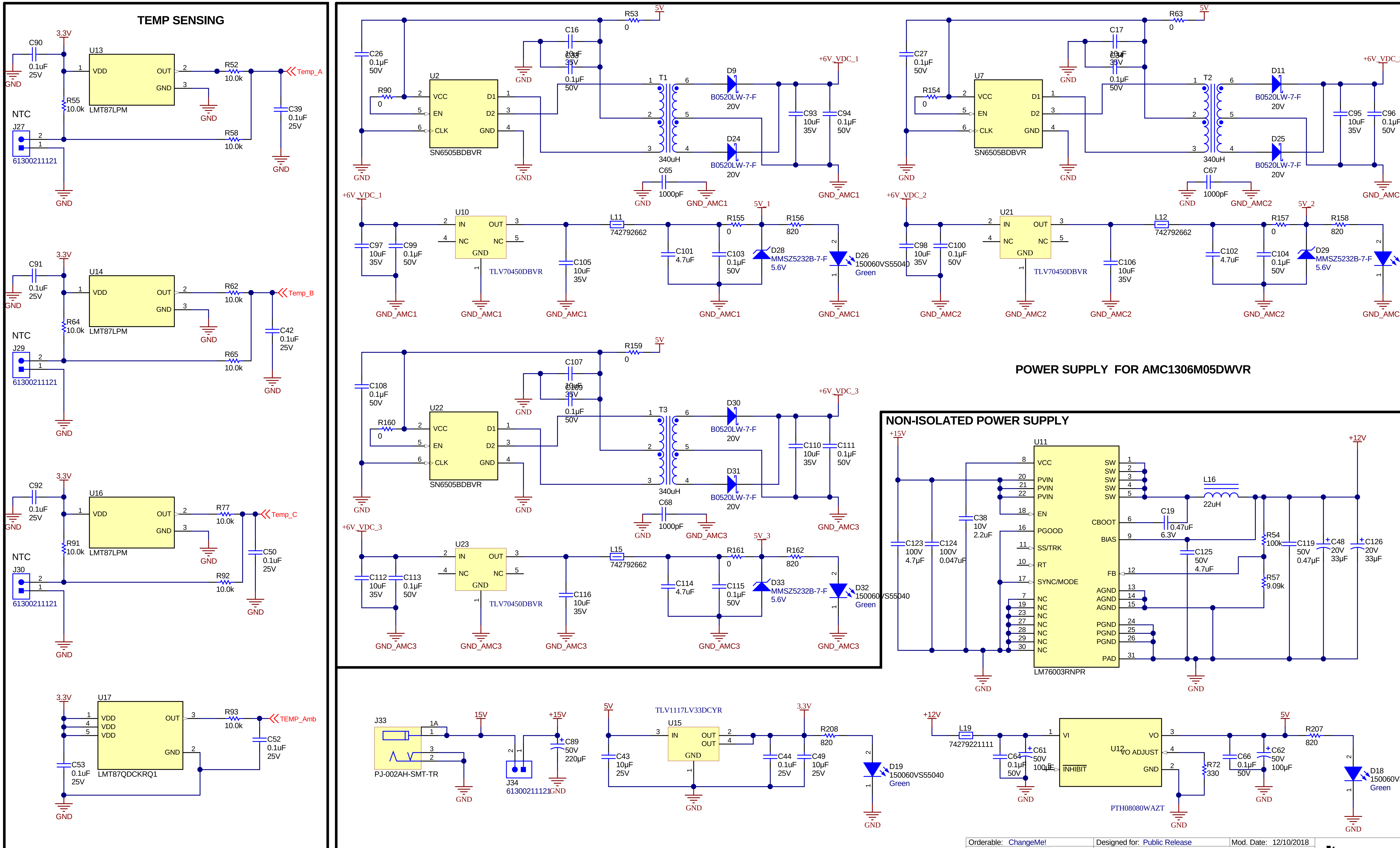

Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 12/10/2018
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Number: TIDA-010039_Power Card Rev: E4	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 5 of 7
Drawn By: Murali P	File: TIDA-010039E4_POWERCARD_Power Supply and Protection_Pg3.SchDoc Size: B	http://www.ti.com
Engineer: Bart Basile	Contact: http://www.ti.com/support	© Texas Instruments 2018

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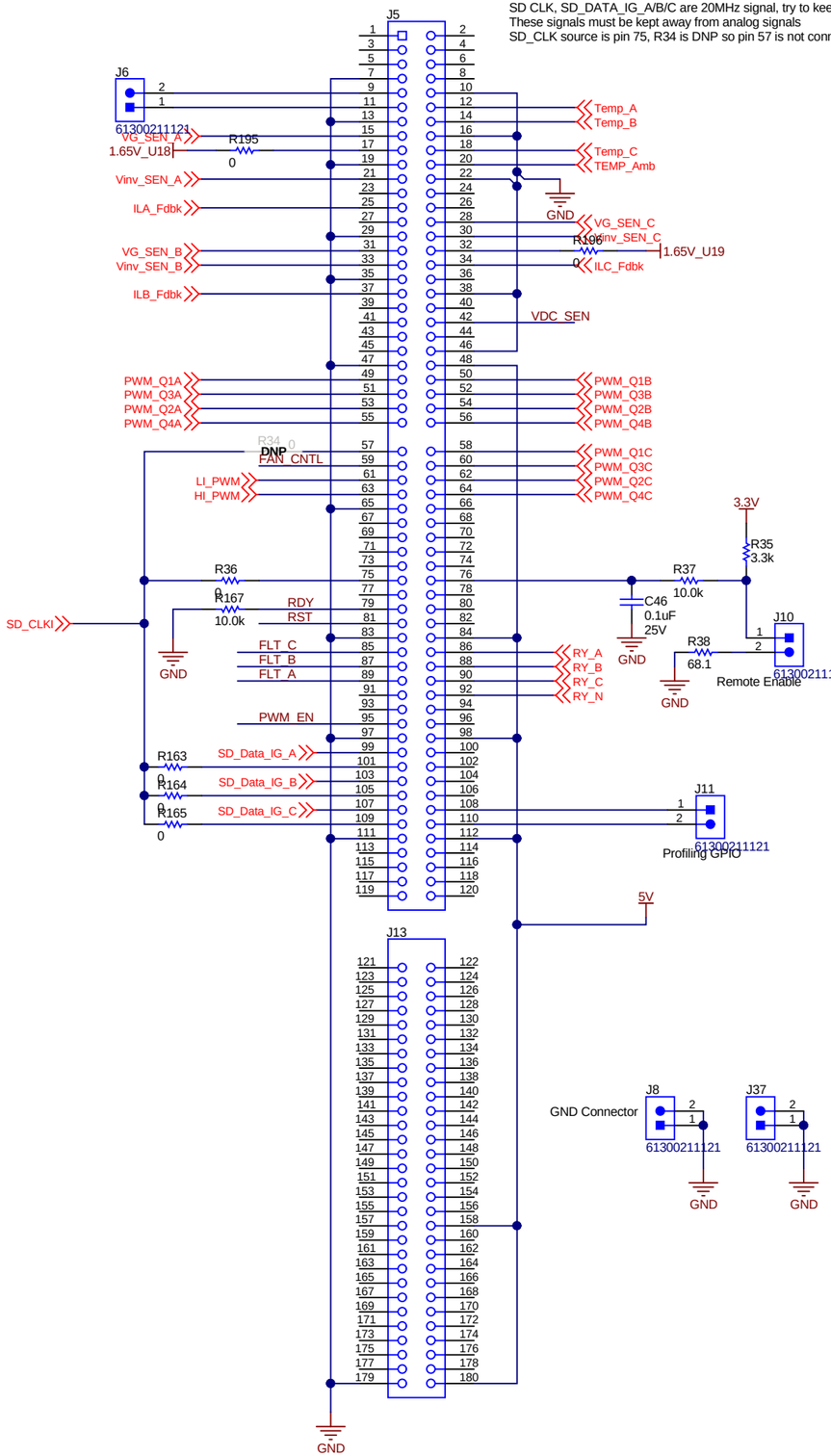
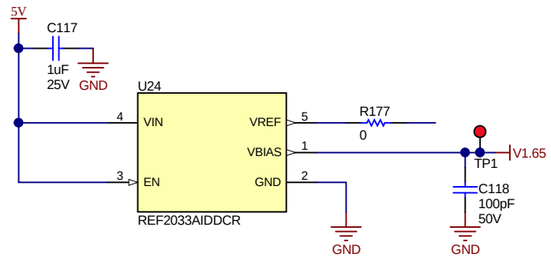
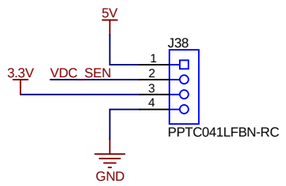
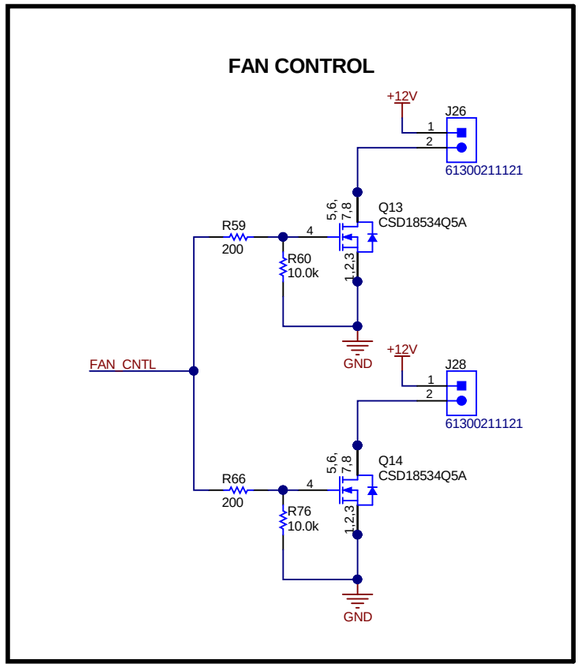
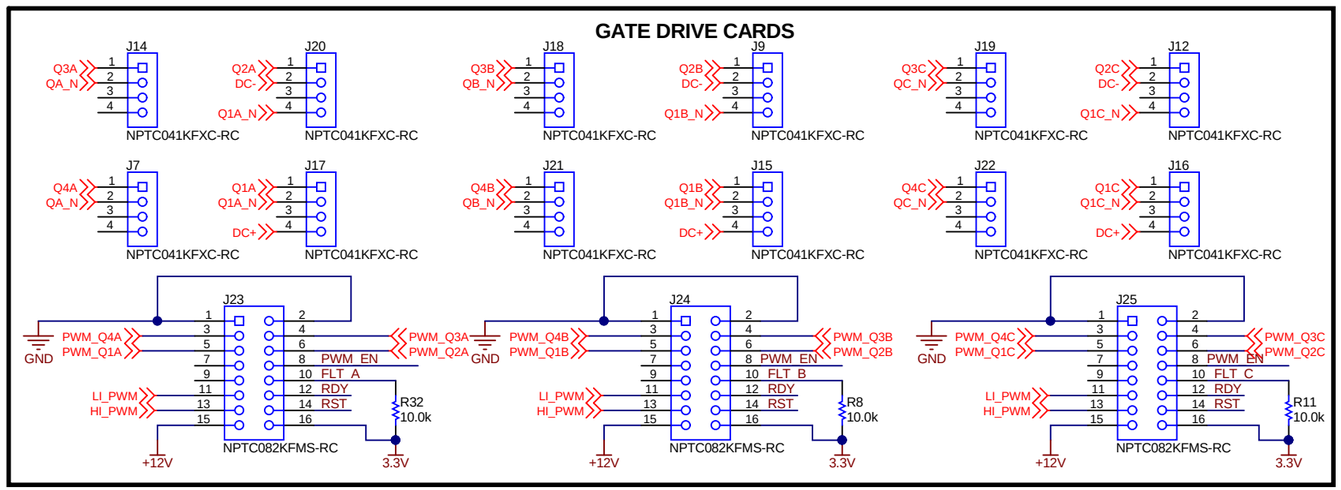
SD CLK, SD_DATA_IG_A/B/C are 20MHz signal, try to keep lengths the same
These signals must be kept away from analog signals
SD_CLK source is pin 75, R34 is DNP so pin 57 is not connected











Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 12/14/2018
TID #: TIDA-010039	Project Title: 10KW PFC	
Number: TIDA-010039_Power Card	Rev: E4	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 7
Drawn By: Murali P	File: TIDA-010039E4_POWERCARD_Controller Card.SchDoc	Size: B
Engineer: Bart Basile	Contact: http://www.ti.com/support	

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H1 H2 H3 H4 H9 H10 H11 H12 H13

MPMS 003 0005 PH MPMS 003 0005 PH MPMS 003 0005 PH MPMS 003 0005 PH MPMS 003 0005 PH MPMS 003 0005 PH MPMS 003 0005 PH MPMS 003 0005 PH MPMS 003 0005 PH

H5 H6 H7 H8 H14 H15 H16 H17 H18

24438 24438 24438 24438 24438 24438 24438 24438 24438

DNP FID1 DNP FID2 DNP FID3 DNP FID4 DNP FID5 DNP FID6

PCB Number: TIDA-010039_Power Card
PCB Rev: E4

PCB LOGO
Pb-Free Symbol

PCB LOGO
FCC disclaimer

PCB LOGO
WEEE logo

LOGO8 LOGO9 LOGO10 LOGO7 LOGO6 LOGO11 LOGO12

PCB LOGO
Logo4

PCB LOGO
Logo5

LOGO13

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1
PCB Label
THT-14-423-10

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 2/15/2019
TID #: TIDA-010039	Project Title: 10KW PFC	
Number: TIDA-010039_Power Card	Rev: E4	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 7
Drawn By: Murali P	File: TIDA-010039E4_POWERCARD_Hardware_Pg7.SchDoc	Size: B
Engineer: Bart Basile	Contact: http://www.ti.com/support	

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