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Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Orderable: NA

TID #: TIDA-010039

Number: TIDA-010039\_Gate\_Driver\_4

SVN Rev: Not in version control

Drawn By: Murali P

Engineer: Bart Basile

Designed for: Public Release

Project Title: 10kW PFC Gate Driver

Sheet Title:

Assembly Variant: 001

File: TIDA-010039E4\_Gate\_Driver\_CoverSheet\_Pg1.SchDoc

Contact: http://www.ti.com/support

Mod. Date: 2/13/2018

Sheet: 1 of 4

Dec 13, 2018

TEXAS INSTRUMENTS

http://www.ti.com

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1

2

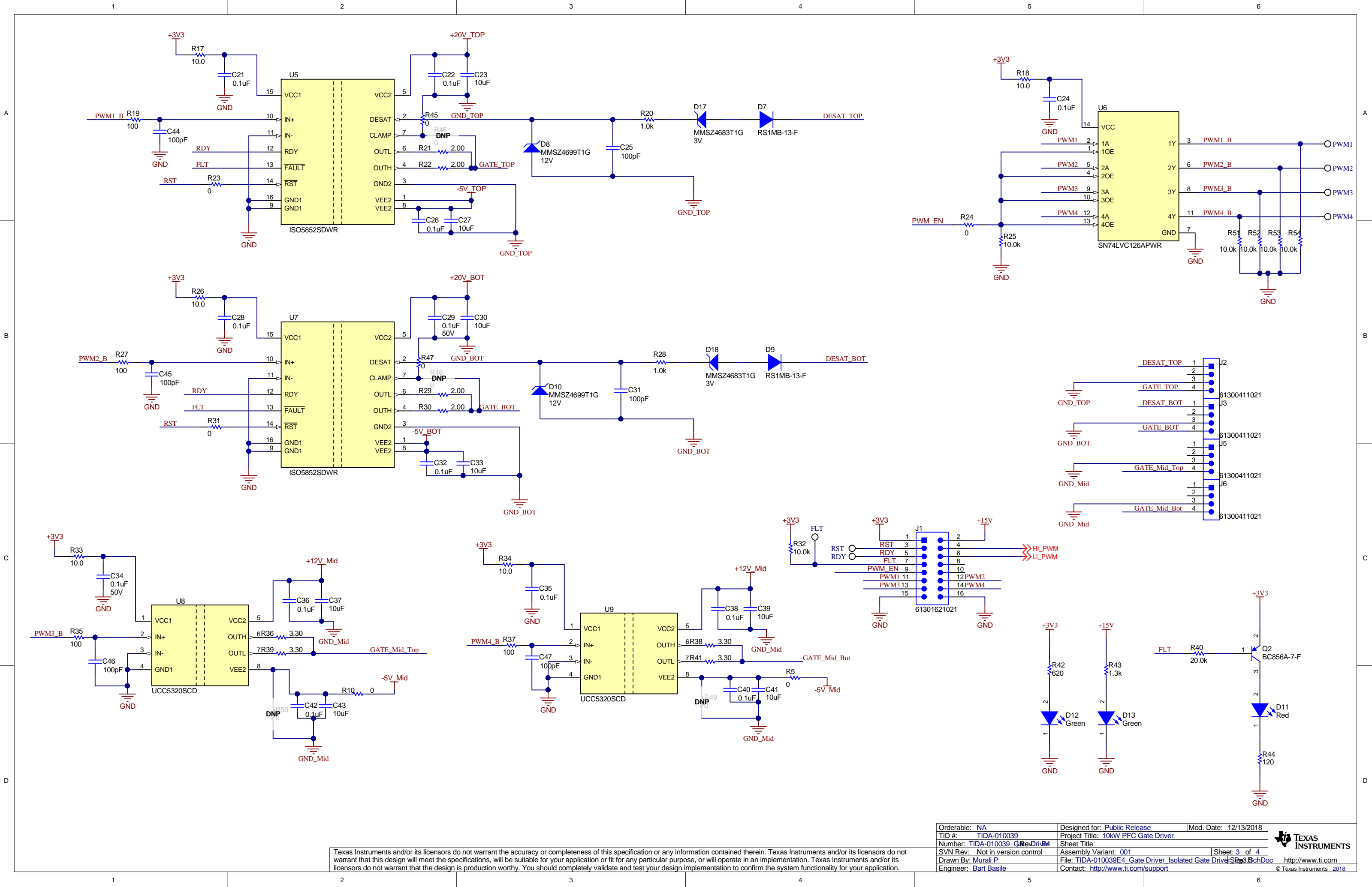
3

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Orderable: NA	Designed for: Public Release	Mod. Date: 12/13/2018
TID #: TIDA-010039	Project Title: 10kW PFC Gate Driver	
Number: TIDA-010039_GateDriver4	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 4
Drawn By: Murali P	File: TIDA-010039E4_Gate_Driver_Isolated Gate Driver_SchDoc	
Engineer: Bart Basile	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

DNP

FID1

DNP

FID2

DNP

FID3

PCB Number: TIDA-010039\_Gate Driver

PCB Rev: E4

PCB

LOGO

Pb-Free Symbol

PCB

LOGO

FCC disclaimer

PCB

LOGO

WEEE logo

LBL1

PCB Label

THT-14-423-10

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

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Orderable: <a href="#">NA</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 2/15/2019
TID #: <a href="#">TIDA-010039</a>	Project Title: <a href="#">10kW PFC Gate Driver</a>	
Number: <a href="#">TIDA-010039_Gate Driver E4</a>	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">4</a> of <a href="#">4</a>
Drawn By: <a href="#">Murali P</a>	File: <a href="#">TIDA-010039E4_Gate Driver_EVM_Hardware_PcbSchematic</a>	
Engineer: <a href="#">Bart Basile</a>	Contact: <a href="#">http://www.ti.com/support</a>	