# CIS 2107 Computer Systems and Low-Level Programming Fall 2011 Final

## December 15, 2011

Name: \_

| Page   | Points | Score |
|--------|--------|-------|
| 1      | 10     |       |
| 2      | 10     |       |
| 3      | 9      |       |
| 4      | 10     |       |
| 6      | 5      |       |
| 7      | 10     |       |
| 8      | 10     |       |
| 9      | 6      |       |
| 10     | 10     |       |
| 11     | 10     |       |
| 12     | 10     |       |
| Total: | 100    |       |

### Instructions

The exam is closed book, closed notes. You may not use a calculator, cell phone, etc.

For each of the questions of this quiz, you can assume the following sizes for C data types:

| $\mathbf{type}$ | bytes |
|-----------------|-------|
| char            | 1     |
| short           | 2     |
| int             | 4     |
| long            | 8     |
| float           | 4     |
| double          | 8     |
| void*           | 4     |

```
CIS 2107 Summary Card 28-jan-08
_____
_____
Compiling and executing a C program
unix> nano hello
#include <stdio.h>
int main()
  printf("Hello world\n");
unix> gcc hello.cn
unix> ./a.out
Hello world
unix>
unix>hexdump -C hello.c
00000000 23 69 6e 63 6c 75 64 65 20 3c
  73 74 64 69 6f 2e |#include <stdio.|
00000010 68 3e 0a 69 6e 74 20 6d 61 69
  6e 28 29 0a 7b 0a |h>.int main().{.|
00000020 20 20 20 20 70 72 69 6e 74 66
 28 22 48 65 6c 6c | printf("Hell|
00000030 6f 20 44 72 2e 20 42 6f 62 20
  5c 6e 22 29 3b 0a |o Dr. Bob \n");.|
00000040 7d 0a |}.|
unix>
_____
unix> gcc -E -o hello.i hello.c
unix> gcc -S -o hello.s hello.i
unix> gcc -c -o hello.o hello.s
unix> gcc -o hello hello.o
unix> ./hello
Hello world
unix>
    +----+
hello.c| pre- |hello.i| |
---->|processor|---->|Compiler|-->|
    | (cpp) | | (cc1) | |
+-----+ |
|<----
    +----+ +----+
V hello.s| |hello.o| |hello
---->|Assembler|---->|linker|--->
      | (as) | | (ld) |
_____
gcc commands
gcc -E -o prog.i prog.c preprocess only
qcc -S -o prog.s prog.c plus compile
gcc -c -o prog.o prog.c plus assemble
gcc -o prog prog.c plus link
gcc -v prog.c see detail
gcc -o prog one.c two.s three.o
```

```
_____
Other Commands
hexdump -C file.c
                  hex dump
objdump -d prog.o
                  disassemble .o
objdump -d prog
                  disassemble exe
                  header contents
objdump -x prog.o
gdb prog
                  debug prog
 x/20b main examine 20 bytes of main
For help with any of the following, use
"cmd --help" or "man cmd" or "info cmd"
 as, cpp, gcc, gdb, help, info,
ld, man, objdump, od
_____
_____
C Programming Language
_____
Identifiers
1. Letters, numbers, underscores " ".
2. Begin with a letter (library routines
  use names beginning with " ").
Keywords reserved.
4. Declaration - interpretation of
  identifier, many times.
5. Definition - declares and reserves
  storage, only once.
auto double int
                     struct
break else long switch
case enum register typedef
char extern return union
const float short
                     unsigned
continue for
              signed
                     void
default goto
              sizeof
                     volatile
do if
              static while
_____
type specifiers
              unsigned union
char
       int
              struct unsigned
double long
enum short typedef void
float
Basic Data Types on x86 unix
1. char - 8-bit signed int, -128 to 127.
ASCII character from 0 to 127.
2. short - 16-bit signed integer.
3. long - 32-bit signed integer.
4. int - 32-bit signed integer.
5. long long - 64-bit signed integer
6. float - 32-bit floating point number.
7. double - 64-bit floating point.
8. long double - 80-bit floating point.
9. void - nonexistant value
_____
```

Data Type Modifiers

unsigned or signed (default)

2. const (never changed)

3. volatile (don't use optimization)

\_\_\_\_\_

```
_____
Storage Classes and Linkage of Objects
(An object is a named region of storage)
1. Internal (to a block or function)
 a. automatic - known only in block
   after def. No init. Discard on
     exit. (int i; auto int i;).
  b. static - known only in block after
    def. Init to zero. Preserved on
     exit. (static int i;)
2. External (to all blocks).
 a. external linkage (default). Known
    in file after def (1) or anywhere
    after extern (many). Init to zero.
     (int i; extern int i;)
  b. internal linkage using static
   keywork - known in file below def.
     or in file after extern. Init to
     zero. (static int i;)
Derived Types
1. arrays of objects of a given type
2. functions returning objects of type
3. pointers to objects of a given type
4. structures containing a sequence of
 objects of various types
Declaration examples.
int var;
```

#### 5. unions containing any one of several objects of various types \_\_\_\_\_ 32-bit signed int int \*var; pointer to int int var[3]; array of 3 ints int \*var[3]; array of 3 ptrs to ints int \*(var[3]); array of 3 ptrs to ints int (\*var)[3]; ptr to array of 3 ints function returns int int f(void); int \*f(void); function returns ptr to int int (\*p)(void); p is ptr to function that returns int void f(int \*a); arg to f is ptr to int void f(int \*); same - "a" is a comment int x[2][3][4]; /\*definition\*/ x[1][1][1] is int x[1][1] is array of 4 ints x[1] is a 3x4 array of ints is a 2x3x4 array if ints x[1][1], x[1], and x are pointers

Type names - declare an object and omit the object name. Used in casts, function declarations, and sizeof

```
Signed type conversions
char -> short -> long-> float -> dou
_____
Integer constants (suffix u, U, 1, I
 123 32-bit int
 123UL 32-bit unsigned long
 0100 100 octal is 64 decimal
 0x100 100 hexadecimal is 256 deci
Floating constants (suffix f, F, 1,
12.3 64-bit double
 12.3F 32-bit float
 12.3L 80-bit long double
 123e-01 == 12.3
Character and String Constants
 'x' 8-bit character constant
  'a' == '\101' == 'x41' == 61
 "string" array of 7 char ('\0' ac
Enumeration Constants
 enum day {sun=1, mon, tues, wed}
_____
C Operators in decreasing precedence
      () (grp) [] (sub)
struct -> (mbr) . (mbr)
unary ! (not) ~ (1's) ++ (inc)
unary -- (dec) + (pos) - (neg)
unary * (ind) & (adr)
unary (type) sizeof
arith / (div) * (mul) % (mod)
arith + (add) - (sub)
shift << (lft) >> (rgt)
relat < (ls) <= (le)
                       > (gt)
relat >= (ge) == (eg) != (ne)
bit & (and) ^ (exor) | (or)
logic && (and) || (or)
if ? (true) : (false)
assign = +=
assign *=
          r-1
&=
                       /=
assign %=
          <<=
assign |=
                    >>=
stmt ,
Operators
1. struct.member, union.member
2. ptr to struct->member
3. ptr to union->member
3. struct->member <==> (*struct).mem
4. *&var <==> var
5. exp1<<exp2 <==> exp1*(2**exp2)
 if no overflow
```

- 6. exp1>>exp2 <==> exp1/(2\*\*exp2)
   if positive and no overflow
- 7. for negative exp1, exp1>>exp2 may fill with 0 (unsigned) or 1 (sign

Typical Program Parts

```
#include ...;
#define ...;
function prototypes;
external declarations and definition
int main(argc, *argv[] {...}
ret-type funct-name(arg-decl) {...}
ret-type funct-name(arg-decl) {...}
```

```
-----page 2
                                       _____
                                       typedef - new names for old data type
Typical function parts (including main)
ret-type funct-name(arg-decl)
                                       typedef int Length; Length a, b, c;
                                       void (*signal(int signum,
   internal definitions and decl.;
                                                void (*func)(int)))(int);
   statement:
                                       typedef void Sigfun(int);
   statement:
                                       Sigfun *signal(int signum, Sigfun *fun);
   return expression;
                                       Structures and Unions
Composition of Declarations & Statements
                                       struct OptStructName {
                                         declaration1;
tokens: identifiers, keywords, constants
                                         declaration2;
                                       } OptVar1, OptVar2;
  string literals, operators, other
white space: sp, ht, vt, nl, ff, comment
                                       struct StructName Var3, Var4;
declaration: legal arrangement of tokens
statement: legal arrangement of tokens
                                       struct point {
_____
                                       int x;
Types of Statements
                                         int v;
                                       } xy, *pxy
labeled-stmt. label: stmt;
expression-stmt. expression;
                                       xy.x = 5; xy.y=10;
                                       pxy = &xy;
compound-statement. {stmt; stmt; }
                                     pxy->x = 20; pxy->y = 30;
selection-stmt. if, switch
iteration-stmt.
                while, do, for
                                       typedef struct Flt { /*little endian*/
                                       unsigned int fract:23;
                goto, continue,
jump-stmt.
                                       unsigned int exp:8;
                break, return,
                                        unsigned int sign:1;
while, for, do, if
                                       union Tstflt {
while (exp) {...}
                                       flt also3;
while (exp, 1..., for (exp; exp; exp) {...}
                                        float f;
do {...} while (exp);
                                       } tstflt;
if (expression) {...} else {...}
                                      flt a3 = \{0x400000, 128, 0\};
exp ? nonzerostmt : zerostmt;
                                      testflt.also3=a3;
                                       print("%12.10f\n", tstflt.f);
                                        -----
                                       Binary Trees
switch (intexp) {
 case int1: stmts;
                                       struct tnode {
 case int2: stmts; break;
                                         int data;
                                         struct tnode *left;
 default: stmts;
                                         struct tnode *right;
Jump statements and statement labels
                                       struct tnode *node = (struct tnode *)
if (exp) goto error;
                                        malloc(sizeof(struct tnode));
                                       node->data = 1;
error: stmts;
                                       node->left = node->right = null;
continue; /*end this iter; start next*/
                                       typedef struct tnode Treenode;
break; /*end iteration or switch */
                                       typedef struct tnode *Treeptr;
return exp;/*return to stmt after call*/
                                       Treeptr node = (Treeptr)
                                               malloc(sizeof(Treenode));
C Preprocessor
                                       Linked Lists
#include "filename" /* local dir */
#include <filename> /* sys defined */
                                       struct llist {
#define name replacement text
                                          struct llist *next;
#define forever for (;;) {}
                                          int value;
#define max(A,B) ((A) > (B) ? (A) : (B))
     max(i++, j++); /* bad idea*/
                                       for (p = head; p != NULL; p = p->next)
#if #ifdef #ifndef #elif #else #endif
                                       -----
                                       Printf format specifiers (after %)
#if !defined(ABC)
#define ABC
                                       i,d ints x hex
                                                              e,f,g double
                                       u uint c char p pointer
#else
 #undef ABC
                                       o octal s string
#endif
```

```
Examples
for (i=1, j=n; i>n; ++i, --j) {...}
struct tnode *node = (struct tnode *)
 malloc(sizeof(struct tnode));
for (p = head; p != NULL; p = p->next)
 int factorial(int i) {
   return i<=1 ? 1 : i*factorial(i-1);
ANSI C Standard Library
_____
Input and Output <stdio.h>
FILE *f, char *s, *t, int c, i, j
f=fopen(s, t)
                 i=fclose(f)
i=fprintf(f, s, ...) i=printf(s, ...)
i=sprintf(s, t, ...) i=fscanf(f, s, ...)
i=spfint(s, t, ...) i=iscanf(r, s, ...)
i=scanf(s, t, ...)
i=scanf(s, t, ...)
i=fgetc(f)
i=fgets(s, i, f)
i=fputs(s, f)
i=getc(f)
i=getchar(void)
t=gets(s)
i=putc(j, f)
i=nutchar(j)
i=ungetc(j, f)
_____
Character Class Tests <ctype.h>
int i, char c
i=isalnum(c) i=isalpha(c) i=iscntrl(c)
i=isdigit(c) i=isgraph(c) i=islower(c)
i=isprint(c) i=isspace(c) i=isupper(c)
string functions (incomplete) <string.h>
char *s, *t, const char *cs, *ct
size t n, int c, i
s=strcpy(s,ct)
                  s=strncpy(s,ct,n)
s=strcat(s,ct)
                  s=strncat(s,ct,n)
i=strcmp(cs,ct) i=strncmp(cs,ct,n)
                s=strrchr(ct,c)
s=strchr(ct,c)
s=strstr(cs,ct) n=strlen(cs)
s=strerror(n)
Mathematical Functions (math.h>
double x, y, z, int n
z=sin(x) z=cos(x) z=tan(x)
z=asin(x) z=acos(x) z=atan(x)
z=atan2(y,x) z=sinh(x) z=cosh(x)
z=tanh(x) z=exp(x) z=log(x)
z=log10(x) z=pow(x,y) z=sqrt(x)
 z=ciel(x) z=floor(x) z=fabx(x)
 -----
Utility Functions <stdlib.h>
double atof(const char *s)
int atoi(const char *s)
long atol(const char *s)
int rand(void)
void stand(unsigned int seed)
void *calloc(size t nobj, size t size)
void *malloc(size-t size)
void free(void *p)
void exit(int status)
int abs(int n)
int labs(long n)
```

```
_____
  as Assembler (info as)
  _____
  AT&T vs. Intel Syntax
   .intel syntax noprefix
  .att syntax prefix
  ТЗТА
                 Intel
  push $4
                 push 4
  clr %eax
                 clr eax
  jump *%eax
                 jump eax
  addl $4, %eax add eax, 4
  movb movw
                 mov (and examine
                 operands)
  movb $10, achar mov byte ptr acha
  AT&T addr. SEC:DISP(BASE,INDEX,SCAI
  Intel addr. SEC: (BASE+INDEX*SCALE+D]
  _____
  Assembler (as) Syntax
  1. /* this is a "multiline" comment
  2. # this is a "line" comment
  3. Statements end at '\n' or ';'
  4. Symbols (A-Z), (a-z), (0-9), (.5)
  5. symbol: - define symbolic address
  6. .symbol - assembly directive
  7. symbol - op code or address
  -----
  Assembler Operands
  zero, one, and two operand instructi
setc # set the carry bi
   incl wage # increment long v
    movb $5,x(\%eax) # x[\%eax] = 5;
  operands
    1000 - mem. addr. 1000 contains (
    beta - addr. beta contains oper.
    $55 - immediate operand 55
    %eax - 32-bit eax reg. contains (
    (%eax) - eax contains addr. of open
   beta(%eax,%ebx,4) - address
    beta + %eax + 4*ebx contains or
  section - block of addresses at 000(
  as predefined sections text, data, k
  .text contains code
            initialized data (int i
  .bss uninitialized data (int
named sections with .section directi
.section .rodata
   .section .note.GNU-stack, "", @progk
  other sections
   undefined value is 0, 1d will det
   .comment .ident text goes here
  as address=(SECTION)+(OFFSET INTO SE
  .comm defines symbol in .bss secti
  -----
  symbols
  symbol=expression #same as .se
  .set symbol expression #same as =
  .Lsymbol is local symbol (ld never :
  1:, 2:, 3: are local labels, 1f, 2b
  "." is location counter. myadr: .lor
```

```
-----page 3
symbol attributes (plus name)
                                    AMD and Pentium 4 64-bit Registers
                                                                         Stack Frame
                                                                                                             Convert (sign extend) instructions
                                    8 64-bit regs: %rax %rbx %rcx %rdx
                 # usually 32 bits
                                                                                                             cbtw %al to %ax
type includes section # .text, etc
                                                  %rdi %rsi %rbp %rsp
                                                                                  1 ...
                                                                                                             cwtl %ax to %eax
                                    8 64-bit exten: %r8 %r9 %r10 %r11
                                                                                  +----+
.type name, @function # for functions
                                                                                                             cltd %eax to %edx, %eax
                                                  %r12 %r13 %r14 %r15
                                                                                                             _____
.type name,@object # for data
                                                                                  |callers storage |
                                    8 32-bit exten: %r8d %r9d %r10d %r11d
.size name,exp
                 # .size myint, 4
                                                                                  +----+
                                                                                                             Classic Two Operand (source+dest->de
.size main, .-main # length of main
                                                  %r12d %r13d %r14d %r15d
                                                                         %ebp-12 -->|my arg 2 |<--+
                                                                                                             (add, adc, and, xor, or, sbb, sub, c
_____
                                    8 16-bit exten: %r8w %r9w %r10w %r11w
                                                                                  +-----
                                                                               -->|my arg 1 |
Reserving and defining storage
                                                  %r12w %r13w %r14w %r15w
                                                                         %ebp-8
                                                                                                             addX %reg,%reg
                                                                                                                             adcX %reg,%reg
                                    8 8-bit exten: %r8b %r9b %r10b %r11b
                                                                                  +-----
                                                                                                             addX $imm, %req
                                                                                                                             adcX $imm, %req
 .bvte 74, 0B01001010 # 74 2 times
                                                 %r12b %r13b %r14b %r15b
                                                                         %ebp-4 -->|return to caller| my
                                                                                                             addX mem,%req
                                                                                                                             adcX mem, %req
                                                                                  +----+ stack
 .bvte 0x4A, 0x4a, 'J # 74 3 times
                                    8 64-bit SSE
                                                %xmm0 %xmm1 %xmm2 %xmm3
                                                                                                             addX %reg.mem
                                                                                                                             adcX %reg.mem
                                                %xmm4 %xmm5 %xmm6 %xmm7
a: .byte 1 # char a = 1;
                                                                         my %ebp -->|callers %ebp | frame <--+
                                                                                                             addX $imm, mem
                                                                                                                             adcX $imm, mem
             # short b = 2;
                                    _____
                                                                                  +----+ |
b: .value 2
           # int c = 4;
                                                                         %ebp+4 -->|my storage | |
c: .long 4
                                    24 General Registers
                                                                                                             subX %req,%req
                                                                                                                             sbbX %req,%req
d: .long 4
             # long d = 4;
                                                                                  +-----
                                                                                                       h
                                                                                                             subX $imm, %rea
                                                                                                                             sbbX $imm, %req
e: .float 4.0  # float e = 4.0;
                                    bit 31 16 15 8 7 0
                                                                         %ebp+8 -->|my storage |<--+-%esp o
                                                                                                                             sbbX mem,%rea
                                                                                                             subX mem,%rea
f: .double 8.0  # double f = 8.0;
                                       +-----
                                                                                  +-----
                                                                                                             subX %rea.mem
                                                                                                                             sbbX %req,mem
g: .ascii "abc" # char g[3]={97,98,99}
                                    %eax| %ax->| %ah | %al |
                                                                         push arg2 |callees arg 2 | <--- %esp k
                                                                                                                             sbbX $imm.mem
                                                                                                             subX $imm.mem
h: .asciz "abc" # char h[3] = "abc";
                                       +-----
                                                                                  +----+
                                     %ebx| %bx->| %bh | %bl |
                                                                         push arg1 |callees arg 1 |<--- %esp |</pre>
i: .=.+100
             # char i[100];
                                                                                                             andX %req, %req
                                                                                                                             orX %req, %req
                                                                                  +----+
j: .comm symbol,length,align #j in .bss
                                       +----+
                                                                                                             andX $imm, %req
                                                                                                                             orX $imm, %req
                                                                                  |return addr | <--- %esp-+
                                     %ecx| %cx->| %ch | %cl |
______
                                                                                                             andX mem, %req
                                                                                                                             orX mem, %req
More Assembler Directives
                                       +----+
                                                                                  +----+
                                                                                                             andX %req,mem
                                                                                                                             orX %req,mem
                                                                         push %ebp |my saved %ebp |<--- %esp
                                    %edx| %dx->| %dh | %dl |
                                                                                                             andX $imm, mem
                                                                                                                             orX $imm, mem
             # addr%4==0
                                                                                  +----+ %ebp
align 4
                                            | %di |
                                                                         movl %esp, |
.align 4,,15
            # addr%4==0, max 15
                                     %edi|
                                                                                                             xorX %req, %req
                                                                                                                             cmpX %req,%req
.align 4,0,15
           # same, but fill with 0
                                                                             %ebp +----+
                                                                                                             xorX $imm, %req
                                                                                                                             cmpX $imm, %req
.p2align 4,,7
            # addr%2^4==0, max 7
                                     %esi| | %si |
                                                                                                             xorX mem, %req
                                                                                                                             cmpX mem, %req
.file file.c
           # may disappear
                                                                                                             xorX %req,mem
                                                                                                                             cmpX %req,mem
.fill repeat, size, value
                                          | %bp
                                                                                                             xorX $imm, mem
                                                                                                                             cmpX $imm, mem
.globl symbol # visible to ld
                                       +----+
                                                                         Section override prefixes
.ident "comment" # to comment section
                                          | %sp |
                                                                           `cs', `ds', `ss', `es', `fs', `qs'
                                                                                                             testX %reg,%reg
                                                                                                                             testX $imm, %rec
                                       +----+
                                                                         Operand/Address size prefixes
                                                                                                             _____
                                                                           `data16', `addr16', data32, addr32
expressions
                                                                                                             Classic 1 Operand (op dest->dest)
                                    Flag Register (low order 16 bits)
                                                                         lock and wait prefixes
result is abs. number or offset into
                                                                         rep, repe, repne for string instructions
                                                                                                             incX %req
                                                                                                                             decX %req
                                    Carry flag ----+
                                                                                                             incX mem
section (text, data, bss, absolute)
                                                                             (%ecx times)
                                                                                                                             decX mem
                                    Parity flag -----+ |
operators -, ~, *, /, %, <<, >>,
                                                                         rex family of prefixes (extended regs)
                                    Aux carry flag -----+ |
       |, &, ^, !, +, -,
                                                                         ______
                                                                                                             notX %reg
                                                                                                                             negX %reg
                                    Zero flag -----+ | |
       ==, <>, !=, <, >=, <=,
                                                                         Instruction Abbreviations
                                                                                                             notX mem
                                                                                                                             neaX mem
                                    sign flag -----+ | | |
       &&, || (true = -1, false = 0)
                                    trap flag -----+ | | | |
_____
                                                                         X = b, w, l, or q
                                                                                                             Shift and Rotate Instructions
                                                    mem = label(%base, %index, 1|2|4|8)
                                                                                                             (rol, ror, rcl, rcr, shl=sal, shr, s
_____
                                                     v v v v v
                                                                         %reg = one of 24 general registers
                                                                                                             If no $imm, shift amount in %cl
386-486-Pentium-Core 2-Athlon
                                                                         XX = bw, bl, bq, wl, wq, lq
                                                                         $imm = immediate operand
_____
                                          salX $imm.%reg
                                                                                                                             sarX $imm, %req
386-486-Pentium "General" Registers
                                          +----+
                                                                         _____
                                                                                                             salX $imm, mem
                                                                                                                             sarX $imm, mem
                                            ^ ^ ^ ^ ^ 7
                                                                         Data Movement (source -> dest)
8 32-bit regs:
             %eax, %ebx, %ecx, %edx,
                                            shlX $imm, %req
                                                                                                                             shrX $imm, %req
                                            | | | | +-- interrupt enable
             %edi, %esi, %ebp, %esp
                                                                         movX %reg, %reg
                                                                                          movX %reg, mem
                                                                                                             shlX $imm, mem
                                                                                                                             shrX $imm, mem
8 16-bit regs:
             %ax, %bx, %cx, %dx,
                                            | | | | +---- Direction flag
                                                                                          movX $imm, mem
                                                                         movX
                                                                              mem, %req
                                            | | | +---- Overflow flag
             %di, %si, %bp, %sp
                                                                         movX $imm, %req
                                                                                                                             rorX Simm.%rea
                                                                                                             rolX $imm.%rea
                                            | +-+---- priv level
8 8-bit regs:
             %ah, %al, %bh, %bl,
                                                                                                             rolX $imm,mem
                                                                                                                             rorX $imm,mem
             %ch, %cl, %dh, %dl
                                            +---- nested task
                                                                         movsXX %req, %req
                                                                                          movsXX mem, %req
1 32-bit eflags:
                                                                         movzXX %req, %req
                                                                                          movzXX mem, %rea
                                                                                                             rclX $imm, %rea
                                                                                                                             rcrX $imm, %req
                                    _____
1 32-bit eip:
                                                                                                             rclX $imm, mem
                                                                                                                            rcrX $imm, mem
                                    Calling Conventions
                                                                         pushl mem
                                                                                          popl
                                                                                                             _____
Additional 386-486-Pentium Registers
                                                                                                             Unsigned Multiply and Divide
                                                                         pushl %rea
                                                                                          popl %req
                                                                                                             oper = %reg or mem
                                    caller saves %eax, %ecx, %edx if wanted
                                                                         pushl imm
                                    callee saves %ebx, %esi, %edi if used
                                                                                                             for divide, result is remainer, quoti
6 16-bit segment: %cs, %ds, %ss, %es,
             %fs, %as
                                                                                          exch mem, %rea
                                                                         exch %req, %req
4 32-bit ctrl: %cr0, %cr1, %cr2, %cr3
                                                                                                                              = %al * oper
                                    fn: pushl %ebp
                                                    #save callers %ebp
                                                                                                             mulb oper %ax
6 32-bit debug: %db0, %db1, %db2, %db3,
                                       movl %esp,%ebp #create my %ebp
                                                                         leal mem, %req
                                                                                                             mulw oper %dx, %ax = %ax * oper
            %db6, %db7
                                                                                                             mull oper %edx, %eax = %eax * oper
2 32-bit test: %tr6, %tr7
                                       movl %ebp, %esp #restore %esp
                                                                                                             divb oper
                                                                                                                      %ah,%al = %ax / oper
8 80-bit flt pt: %st=%st(0), %st(1),
                                       popl %ebp #rest. callers %ebp
                                                                                                             divw oper
                                                                                                                      %dx, %ax = %dx, %ax / or
           %st(2), ..., %st(7)
                                                                                                             divl oper %edx, %eax = %edx, %eax /
                                       ret
```

\_\_\_\_\_

```
_____
-----page 4
Signed Multiply and Divide
                                     Floating Point (8 80-bit registers)
                                                                           ASCIT
                                                                                                                Decimal-Binary-Hexadecimal (Hex)
oper = %reg or mem
for divide, result is remainder, quotient
                                     float a, b, c;
                                                     double d, e, f;
                                                                           10 16 ch 10 16 ch 10 16 ch
                                                                                                                     2 16
                                                                                                                                           2
                                     a = b + c;
                                                     d = e + f;
                                                                            0 00 \0 32 20 SP 64 40 @ 96 60 `
                                                                                                                0 0000 0
imulb oper %ax = %al * oper
                                                                                                                                    8 1000
         %dx, %ax = %ax * oper
                                     flds b #s=short fldl e #1 = long
                                                                           1 01 SOH 33 21 ! 65 41 A 97 61 a
                                                                                                                1 0001 1
                                                                                                                                    9 1001
imulw oper
                                                                           2 02 STX 34 22 " 66 42 B 98 62 b
imull oper %edx,%eax = %eax * oper
                                     flds c #p=pop
                                                      fldl f #p = pop
                                                                                                                2 0010 2
                                                                                                                                  10 1010
                                     faddp %st, %st(1)
                                                    faddp %st,%st(1)
                                                                          3 03 ETX 35 23 # 67 43 C 99 63 c
                                                                                                               3 0011 3
                                                                                                                                  11 1011
                                                    fstpl d
                                                                          4 04 EOT 36 24 $ 68 44 D 100 64 d
                                                                                                              4 0100 4
                                                                                                                                  12 1100
imulX %req,%req
                                     fstps a
imulX &mem,%rea
                                                                           5 05 ENO 37 25 % 69 45 E 101 65 e
                                                                                                              5 0101 5
                                                                                                                                   13 1101
                                                                          6 06 ACK 38 26 & 70 46 F 102 66 f
imulX $imm, %req
                                     Floating Point Condition Codes. After
                                                                                                                6 0110 6
                                                                                                                                   14 1110
                                     "fnstsw %ax; andb $69, %ah", %ah = ($0,
                                                                          7 07 BEL 39 27 '
                                                                                             71 47 G 103 67 g
                                                                                                              7 0111 7
                                                                                                                                   15 1111
                                                                          8 08 BS 40 28 ( 72 48 H 104 68 h
idivb oper %ah, %al = %ax / oper
                                     $1, $64 or $69) for (>, <, =, or NAN)
                                     _____
                                                                          9 09 HT 41 29 ) 73 49 I 105 69 i
idivw oper %dx, %ax = %dx, %ax / oper
                                                                                                                Floating Point Numbers
                                                                           10 0A \n 42 2A * 74 4A J 106 6A j
idivl oper %edx, %eax = %edx, %eax / oper
                                     String Instructions
                                     len sign exp hide fi
Jump, Call, and Ret Instructions
                                                                                                                            32 1 8 1
                                                                                                                float
           #uses %eip relative addr.
jmp label
jmp *%req
           #jump to addr in reg
                                                                                                                double
                                                                                                                            64 1 11
                                                                        16 10 DLE 48 30 0 60 50 1 112 11
17 11 DC1 49 31 1 81 51 Q 113 71 q
18 12 DC2 50 32 2 82 52 R 114 72 r
                                                                           16 10 DLE 48 30 0 80 50 P 112 70 p
           #jump to addr in mem loc
                                     movs move string, movX (%esi),(%edi)
imp *mem
                                                                                                                long double 80 1 15
                                     cmps compare string, cmpX (%esi), (%edi)
                                     stos store string, movX %a?,(%edi)
call pushes %eip (return addr) on stack
                                     lods load string, movX (%edi),%a?
call label #uses %eip relative addr.
                                                                                                                exponent bias is 127, 1023, or 16383
                                     scas scan string, cmpX (%edi), %a?
call *%reg #function addr in reg
                                                                         20 14 DC4 52 34 4 84 54 T 116 74 t
call *mem #function addr in mem loc
                                                                           21 15 NAK 53 35 5 85 55 U 117 75 u
                                                                                                                     31 30 23 22
                                                                        22 16 SYN 54 36 6 86 56 V 118 76 V
         #pop stack into %eip
                                     put count in %ecx and repeat instruction
                                                                                                                    +-+----
                                     using rep, repe, and repne prefix 23 17 ETB 55 37 7 87 57 W 119 77 w
                                                                                                                float |s| exp |1. + 23-bit fracti
                                                                        24 18 CAN 56 38 8 88 58 X 120 78 x
Condition Codes
                                     _____
                                                                                                                     +-+----
                                     Read Time Stamp Counter 25 19 EM 57 39 9 89 59 Y 121 79 y
                                     rdtsc - cycles since boot in %edx, %ecx 26 1A SUB 58 3A : 90 5A Z 122 7A Z
CF: Carry flag (unsigned overflow if 1)
ZF: Zero flag, set to 1 if result is 0
                                     _____
                                                                        27 1B ESC 59 3B; 91 5B [ 123 7B {
                                                                                                                Recognizing Floating Point Numbers
                                     SF: Sign flag, equals result high bit
                                                                        28 1C FS 60 3C < 92 5C \ 124 7C |
OF: Overflow flag (unsigned overflow)
                                     Counting and Number Representation
                                                                         29 1D GS 61 3D = 93 5D ] 125 7D }
                                                                                                                if e==0.
                                     _____
                                                                           30 1E RS 62 3E > 94 5E ^ 126 7E ~
                                                                                                                denormalized
                                     Signed and unsigned 8-bit bytes
Conditional Jump instructions, jCC label
                                                                           31 1F US 63 3F ? 95 5F _ 127 7F DE
                                                                                                                   f=(-1)^S * 2^(1-bias)*0.M
Use after cmp, add, sub, etc. (not mov)
                                                                           _____
                                                                                                                else if e == all 1s:
cmpl a,b; jq label; jumps if b-a>0 (b>a)
                                     hex binary unsigned 2's comp
                                                                           Escape sequences in ASCII
                                                                                                                  if M == 0:
                                                                                                                     if S == 0:
Simple Condition Code Tests
                                      00 00000000
                                                                           null or zero NUL 0 = 000 = x00 = 0
                                                                                                                          +infinity
                                                                           audible alert BEL \a = \007 = \xx07 = 7
                                      01 00000001
                                                       1
                                                               1
                                                                                                                      else:
                                                                           backspace BS \b = \010 = \x08 = 8
jo label
               jno label
                                      02 00000010
                                                       2.
                                                               2.
                                                                                                                          -infinity
jz label
               jnz label
                                     03 00000011
                                                      3
                                                              3
                                                                           horizontal tab HT \t = \011 = \x09 = 9
                                                                                                                   else.
               jns label
                                                                           newline (LF) NL \n = \012 = \x0a = 10
js label
                                                                                                                     NaN
                                                                           vertical tab VT \setminus v = \setminus 013 = \setminus x0b = 11
jo label
               jn0 label
                                      7E 01111110
                                                     126
                                                             126
                                                                                                                else:
                                                                           formfeed FF \f = \014 = \x0c = 12
                                      7F 01111111
                                                     127
                                                             127
                                                                                                                   normalized
                                                                         carriage ret CR \r = \015 = \x0d = 13
signed
               unsigned
                                      80 10000000
                                                    128
                                                             -128
                                                                                                                    (-1)^S * 2^(e-bias)*1.M
                                                                         double quote " \" = \042 = \x22 = 34
                                     81 10000001
                                                  129
                                                                           single quote ' \' = \047 = \x27 = 39
jg (jnle) label
               ja (jnbe) label
                                                                                                                ______
                                                                           question mark ? ? = \sqrt{077} = \sqrt{x3f} = 63
jge (jnl) label jae (jnb) label
                                    FE 11111110
                                                  254
                                                             -2
                                                                                                                #define bit get(p,m) ((p) & (m))
                                                 255
jl (jnge) label jb (jnae) label
                                     FF 11111111
                                                             - 1
                                                                           \#define bit set(p,m) ((p) |= (m))
                                     ______
                                                                                                                #define bit clr(p,m) ((p) &= ~(m))
jle (jng) label jbe (jna) label
                                                                           octal number ooo \ooo
                                     Signed and unsigned 32-bit integers
                                                                                                                #define bit flip(p,m) ((p) ^= (m))
_____
                                                                           hex number hh \xhh
Set Instructions (setCC reg8)
                                                                                                                #define bit write(c,p,m)
setCC rea8; movzbl rea8, rea32;
                                                  unsianed
                                                            2's comp
                                                                           Powers of 2 and 16
                                                                                                                 (c ? bit set(p,m) : bit clear(p,r
                                        hex
                                                                                                                #define BIT(x) (0x01 \ll (x))
Set Individual Condition Code Bits
                                     00000000
                                                        Ω
                                                                     0
                                                                                   2^n
                                                                                                  16^n
                                                                                                                #define LONGBIT(x)
                                     00000001
                                                       1
                                                                     1
                                                                                                                ((unsigned long) 0x00000001 << (x))
seto label
               setno label
                                     00000002
                                                                            0
                                                                                                                ______
setz label
               setnz label
                                     00000003
                                                                                    2
                                                                                                  1.6
                                                                                                                struct one32bitlong {
                                                       3
                                                                     3
                                                                            1
sets label
               setns label
                                                                                                  256
                                      . . .
                                                                            2
                                                                                    4
                                                                                                                unsigned bit:1;
                                     7FFFFFFE 2,147,483,646 2,147,483,646
seto label
               setno label
                                                                            3
                                                                                   8
                                                                                         4,096
65,536
1,048,576
16,777,216
268,435,456
                                                                                                 4,096
                                                                                                                    unsigned nibble:4;
                                     7FFFFFFF 2,147,483,647 2,147,483,647
                                                                                   16
                                                                                                                  unsigned dozen:12;
                                                                          4
                                     80000000 2,147,483,648 -2,147,483,648
                                                                                 32
                                                                                                                   unsigned fifteen:15;
signed
               unsigned
                                                                          .5
                                     80000001 2,147,483,649 -2,147,483,647
                                                                                 64
                                                                                 128
setg (setnle) reg8 seta (setnbe) reg8
                                      . . .
                                                                  . . .
                                     FFFFFFFD 4,294,967,294
                                                                                 256 4,294,967,296
setge (setnl) reg8 setae (setnb) reg8
                                                                    -3
                                                                           8
set1 (setnge) reg8 setb (setnae) reg8
                                     FFFFFFFE 4,294,967,294
                                                                   -2
                                                                           9
                                                                                 512 68,719,476,736
setle (setng) reg8 setbe (setna) reg8
                                     FFFFFFFF 4,294,967,295
                                                                  -1
                                                                           10 1,024 1,099,511,627,776
```

1. Please place the letter for each in the answer line.

|           | A L1                     | H BSS                           | O flash                  | V preprocessor         |
|-----------|--------------------------|---------------------------------|--------------------------|------------------------|
|           | B loader                 | I SRAM                          | P sector                 | W symbol table         |
|           | C nanoseconds            | J compiler<br>K track           | Q heap                   |                        |
|           | D cylinder               | L EAX                           | R magnetic disk          | X CPU                  |
|           | E L2                     | M seconds                       | S assembler              | Y EBP                  |
|           | F data                   | N instruction pointer           | T stack                  | 1 EBI                  |
|           | G DRAM                   | or program counter              | U locality               | Z milliseconds         |
| (1 point) | (a) The auto storage of  | class (used for local variables | in a function) uses this | area of memory.        |
|           |                          |                                 |                          | (a)                    |
| (1 point) | (b) Translates a high    | level language program into a   | ssembly language.        |                        |
|           |                          |                                 |                          | (b) assembler          |
| (1 point) | (c) Data structure cre   | eated by the assembler during   | pass 1.                  | ( )                    |
| ` - /     | . ,                      | ·                               |                          | (c)                    |
| (1 point) | (d) Danahlu ana milli    | on times slawer shaarer and     | langer than main mana    | · /                    |
| (1 point) | (a) Roughly one million  | on times slower, cheaper, and   | larger man mam memo      |                        |
|           |                          |                                 |                          | $(d)$ $\frac{flash}{}$ |
| (1 point) | (e) Technology used for  | or main memory.                 |                          | dram                   |
|           |                          |                                 |                          | (e)                    |
| (1 point) | (f) Tracks are partition | oned into these (typically 512  | bytes each).             | ( )                    |
|           |                          |                                 |                          | (t)                    |
| (1 point) | (a) Mallag and free re   | anage this area of memory.      |                          | (f)                    |
| (1 point) | (g) Manoc and free in    | anage this area of memory.      |                          |                        |
|           |                          |                                 |                          | (g)                    |
| (1 point) | (h) Stores each bit in   | a cell composed of six transis  | tors.                    |                        |
|           |                          |                                 |                          | (h)                    |
| (1 point) | (i) One of the concern   | tric rings on the surface of a  | disk.                    | ` /                    |
| ,         |                          |                                 |                          | (;)                    |
| (1 point) | (i) Toohnology year t    | o create solid state disks.     |                          | (i)                    |
| (1 bount) | (J) recimology used t    | o create some state disks.      |                          |                        |
|           |                          |                                 |                          | (j)                    |
|           |                          |                                 |                          |                        |

## Computer Systems and Low-Level Programming

Final December 15, 2011

(The answers are repeated at the top of this page for your convenience. They are the same as the answers on the previous page.)

|              | A L1                   | H BSS                           | or program counter          | U locality      |
|--------------|------------------------|---------------------------------|-----------------------------|-----------------|
|              | B loader               | I SRAM                          | O flash                     | V preprocessor  |
|              | C nanoseconds          | J compiler                      | P sector                    | W symbol table  |
|              | D cylinder             | K track                         | Q heap                      | X CPU           |
|              | $\to L2$               | L EAX                           | R magnetic disk             |                 |
|              | F data                 | M seconds                       | S assembler                 | Y EBP           |
|              | G DRAM                 | N instruction pointer           | T stack                     | Z milliseconds  |
| (1 point)    | (k) Area of memory     | used for uninitialized global   | variables.                  |                 |
|              |                        |                                 |                             | (k)             |
| (1 point)    | (l) Tendency for pro   | grams to access multiple obje   | ects in a block.            |                 |
|              |                        |                                 |                             | (1)             |
| (1 point)    | (m) Translates assem   | bly language programs into n    | machine language.           |                 |
|              |                        |                                 |                             | (m)             |
| (1 point)    | (n) Area of momory     | used for initialized global var | ria blog                    | (III)           |
| (1 point)    | (ii) Area of memory    | used for initialized global var | lables.                     |                 |
|              |                        |                                 |                             | (n)             |
| (1 point)    | (o) In a collection of | disk platters, a set of tracks  | equidistant from the center | of the platter. |
|              |                        |                                 |                             | (o)             |
| (1:+)        | () D                   | 41 4 41 11                      | f +1 : +: + - 1 -           | · /             |
| (1 point)    | (p) Processor register | r that contains the address of  | the next instruction to be  | executed.       |
|              |                        |                                 |                             | (p)             |
| (1 point)    | (q) Contains the retu  | ırn value of functions which    | return ints.                |                 |
|              |                        |                                 |                             |                 |
| <i>(</i>     | / \                    |                                 |                             | (q)             |
| (1 point)    | (r) The larger but sle | ower cache. Still much faster   | than main memory.           |                 |
|              |                        |                                 |                             | (r)             |
| (1 point)    | (s) Time it takes to   | read from disk.                 |                             | ( )             |
| ( <b>1</b> ) | (-)                    |                                 |                             |                 |
|              |                        |                                 |                             | (s)             |
| (1 point)    | (t) Time to read from  | m on-CPU cache.                 |                             |                 |
|              |                        |                                 |                             | (t)             |
|              |                        |                                 |                             | (6)             |

(2 points) 2. What is  $111101011010_2 + 1011111011_2$  in base 2?

|   | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | $0_2$ |
|---|---|---|---|---|---|---|---|---|---|---|---|-------|
| + |   |   | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | $1_2$ |

(2 points) 3. What is  $5696C1B_{16} + DA778_{16}$  in base 16?

(5 points) 4. The hex value 0x411a0000 is stored in a 32-bit C float variable. What floating point number does this value represent? It's perfectly OK if your answer includes fractions. (For example, if the correct answer were 2.75, you could also write "2 and  $\frac{3}{4}$ ".)

| (10 points) | <ul> <li>5. Some bit operations. If we have char x = 0x5C, y = 0xA9;, operations? Your answer must be in the form of exactly two hex di</li> <li>(a) x y</li> </ul> | what is the result of the following $\operatorname{gits}^1$ . |
|-------------|---|---|
|             |   | (a)   |
|             | (b) x  y  | (b)   |
|             | (c) x<<2  | (c)   |
|             | (d) ~x  | (d)   |
|             | (e) ~~x   | (e)   |
|             | (f) x&0x0F  | (f)   |
|             | (g) x^y   | (g)   |

 $<sup>^{1}</sup>$ Ignore the possibility of promotion to 32-bit ints. Behave as though we're living in the land of 8-bit arithmetic.

| (h) | x&&1 |  |
|-----|------|--|
| (h) | x&&1 |  |

(h) \_\_\_\_\_

(i) \_\_\_\_\_

(j) \_\_\_\_\_

(k) \_\_\_\_\_

1) \_\_\_\_\_

## (m) x&y

(m) \_\_\_\_\_

## (n) $x^y$

(n) \_\_\_\_\_

(o) \_\_\_\_\_

| (5 points) | 6. If I have the following:               |   |      |
|------------|---|---|------|
|            | <pre>int main(void)</pre>                 |   |      |
|            | {    int a=10;                            |   |      |
|            | int b=20;                                 |   |      |
|            | <pre>int *p=&amp;a</pre>                  |   |      |
|            | <pre>int *q=p; char *cp = (char*)q;</pre> |   |      |
|            | char *cp = (char*)q;                      |   |      |
|            | (*p);                                     |   |      |
|            | q;<br>cp;                                 |   |      |
|            | and memory is laid out like this:         |   |      |
|            |   | cp 1000   |      |
|            |   | q = 1004  |      |
|            |   | $egin{array}{cccc} p & 1008 & & & & & & & \\ b & 1012 & & & & & & & & \\ \end{array}$ |      |
|            |   | $\begin{bmatrix} a & 1012 \\ a & 1016 \end{bmatrix}$                                  |      |
|            | what do you see if you print:             |   |      |
|            | (a) a                                     |   |      |
|            |   |   | (a)  |
|            | (b) &a                                    |   | · /  |
|            |   |   | (b)  |
|            | (c) b                                     |   | (6)  |
|            | ( )                                       |   | (-)  |
|            | (d) p                                     |   | (c)  |
|            | (d) p                                     |   |      |
|            | ( ) *                                     |   | (d)  |
|            | (e) *p                                    |   |      |
|            |   |   | (e)  |
|            | (f) &p                                    |   |      |
|            |   |   | (f)  |
|            | (g) q                                     |   |      |
|            |   |   | (g)  |
|            | (h) *q                                    |   |      |
|            |   |   | (h)  |
|            | (i) cp                                    |   | ( )  |
|            |   |   | (i)  |
|            | (j) &cp                                   |   | (1)  |
|            | w/ 1                                      |   | (:\) |
|            |   |   | (j)  |
|            |   |   |      |

(10 points) 7. Use the following code to answer the questions. Data sizes are specified on the cover of the exam.

```
struct Stuff {
     int x;
                                                 27
                                                       return 0;
      int *p;
                                                     }
3
                                                 28
     int A[10];
                                                 29
   };
                                                     void func01(int arr[]) {
5
                                                 30
                                                       arr[0]=3333;
6
                                                 31
   int main(void)
                                                 32
8
                                                 33
9
      struct Stuff s;
                                                 34
                                                     void func02(char *s) {
      int A[10];
                                                       strcpy(s, "yeah, winter break");
10
                                                 35
      int x, y;
11
                                                 36
      char str[24];
12
                                                 37
                                                     void func03(char *s) {
13
                                                 38
     x=10;
                                                       s = malloc(40);
14
                                                       strcpy(s, "how many more pages is this thing?");
      y=20;
15
      A[0]=30;
                                                     }
16
                                                 41
      strcpy(str, "almost quitting time");
17
                                                 42
      s.x=40:
                                                     void func04(struct Stuff s) {
18
                                                 43
      s.p=&y;
                                                       s.x=4444;
19
                                                 44
      s.A[0]=50;
                                                       *(s.p)=2222;
20
                                                 45
                                                       s.A[0]=5555;
                                                       s.p=malloc(sizeof(int));
22
      func01(A);
                                                 47
      func02(str);
                                                       *(s.p)=2020;
23
                                                 48
     func03(str);
24
                                                 49
     func04(s);
25
     (a) How many bytes are passed to the function func01()?
                                                                                  (a) _____
    (b) How many bytes are passed to the function func02()?
                                                                                  (b) _____
     (c) How many bytes are passed to the function func04()?
                                                                                  (c) _____
    What is the value of each of the following after func04() has been called?
    (d) x
                                                                                  (d) _____
     (e) y
                                                                                  (e) _____
     (f) A[0]
                                                                                  (f) _____
    (g) s.x
                                                                                  (g) _____
```

(j) str (What's the string?)

(j) \_\_\_\_\_

(h) s.A[0]

(i) \*(s.p)

(h) \_\_\_\_\_

(i) \_\_\_\_\_

(10 points) 8. Write a C function which is passed an unsigned int x. The function returns 1 if there are an odd number of 1s in x's binary representation or 0 otherwise.

points: \_\_\_\_\_ out of a possible 10

| Ω  | Cirron | +ha | C fu  | nction: |
|----|--------|-----|-------|---------|
| 9. | Criven | The | C 111 | nction: |

```
int func(int x, int y) {
  int t;
  ...
  return x+y-t;
}
```

Immediately before func() is called, *i.e.*, immediately before the instruction call func, %ebp contains the value  $1000_{10}$  and %esp contains the value  $960_{10}$ . Before func() exits (more precisely, just before the leave and return instuctions are executed), what is:

(1 point) (a) stored in %ebp?

(1 point) (b) stored in %esp?

(1 point) (c) the location of x?

(1 point) (d) the location of y?

(1 point) (e) the most likely location of t?

(1 point) (f) the location of the return value?

(a) \_\_\_\_\_

· /

(b) \_\_\_\_\_

(c) \_\_\_\_\_

(d) \_\_\_\_\_

(e) \_\_\_\_\_

(f) \_\_\_\_\_

(10 points) 10. Write a C function equivalent to the following assembly (no credit for an answer containing inline assembly).

```
.section .text
1
            .globl mystery
2
            .type mystery, @function
3
   mystery:
           pushl %ebp
5
           movl %esp, %ebp
6
           xorl %eax, %eax
           xorl %ecx, %ecx
           movl 8(%ebp), %edx
9
   begin:
10
           cmpl 12(%ebp), %ecx
11
            jge done
12
           addl (%edx, %ecx, 4), %eax
13
            incl %ecx
14
            jmp begin
   done:
16
           movl %ebp, %esp
17
           popl
                 %ebp
18
           ret
```

(10 points) 11. Implement the function void reverse(int A[], int len), which reverses the order of A[], an array of len items. Do not use the [] operator. No credit will be given for solutions which use the [] operator, or which declare len or more elements of temporary storage.

void reverse(int A[], int len) {

(10 points) 12. A common way of storing a spreadsheet is comma-separated text. For example, the following line in a spreadsheet:

could be stored as "apple, banana, cherry, some fruit beginning with d". Write the function char \*\*split(char \*s) which is passed s, which is a string of comma-separated values, and returns an array of string containing the values in the line terminated by a NULL pointer. Using our current example, we'd return:

| w[0] | apple                       |
|------|-----------------------------|
| w[1] | banana                      |
| w[2] | cherry                      |
| w[3] | some fruit beginning with d |
| w[4] | NULL                        |

 $\mathtt{split}(\ )$  should return NULL on failure. Hint: if there are n commas in  $\mathtt{s},$  there will be n+1 words. You may use any function in the Standard C Library.

(extra space)

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