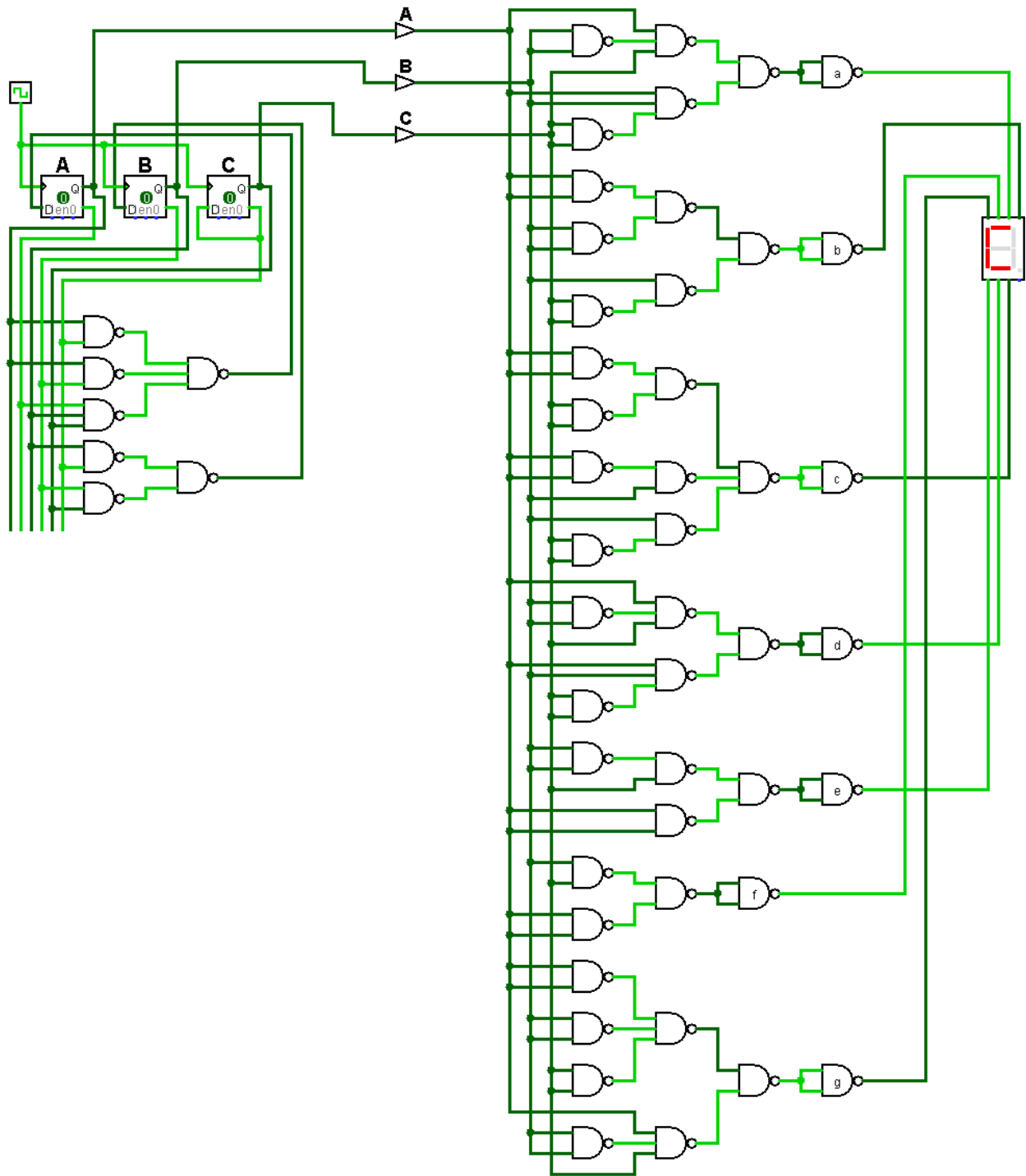
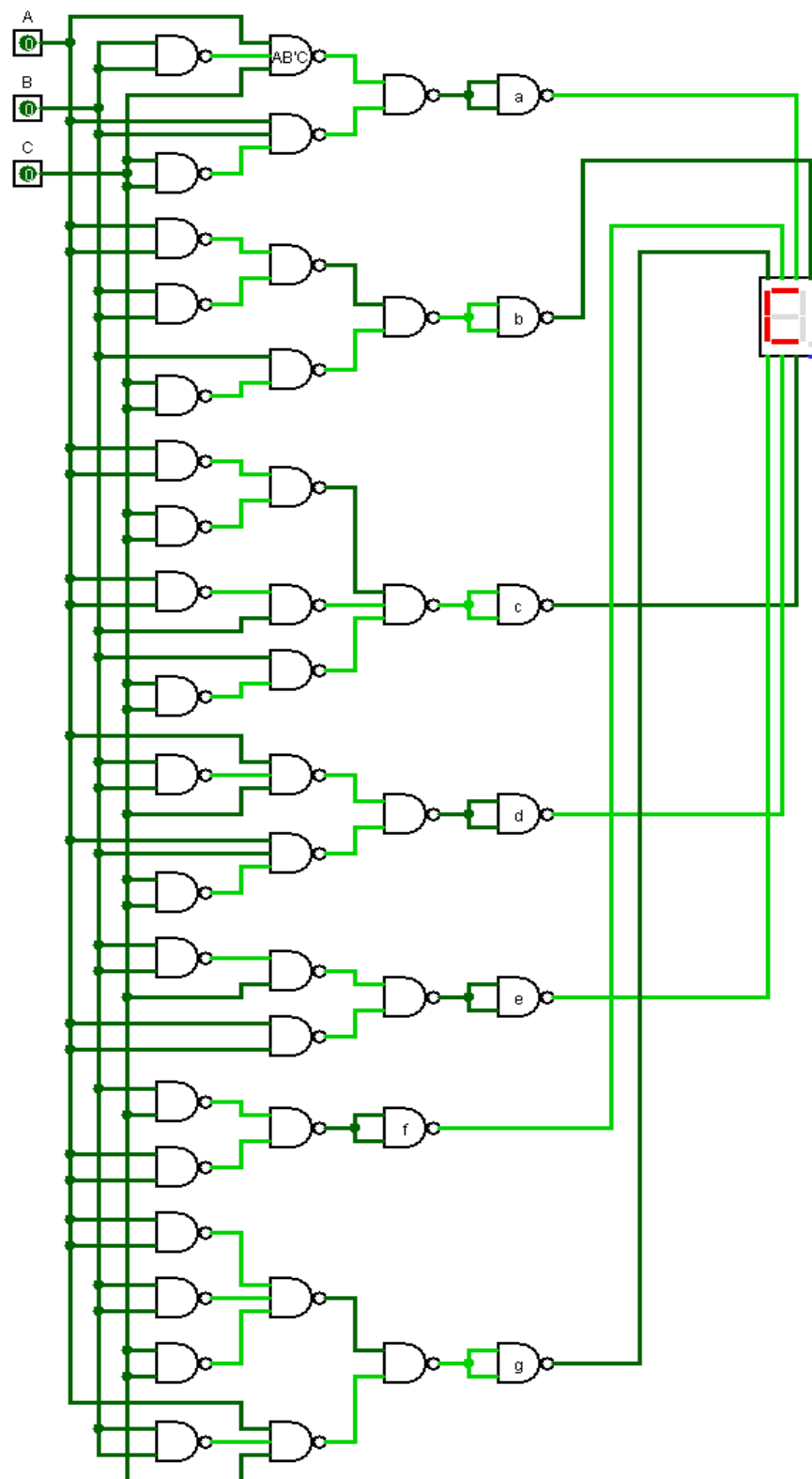


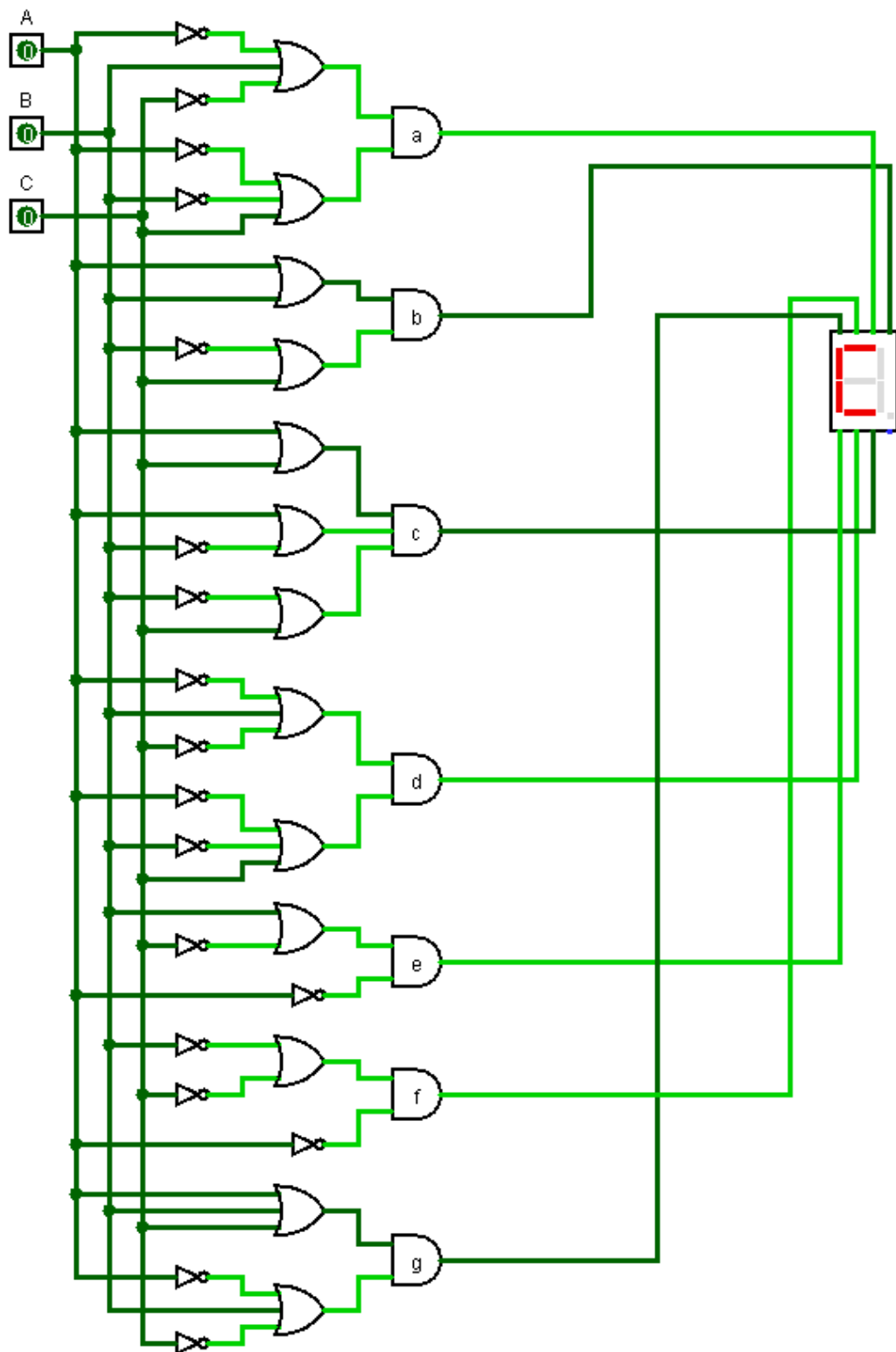
## Logisim Simulation of Sequential Circuit



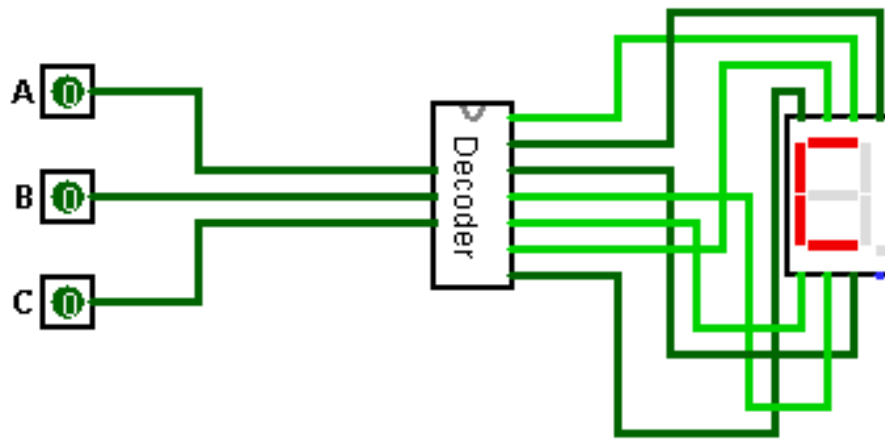
## Combinational Circuit – Using Universal Gate



## Combinational Circuit – Using Basic Gates



## Combinational Circuit – Using Decoder



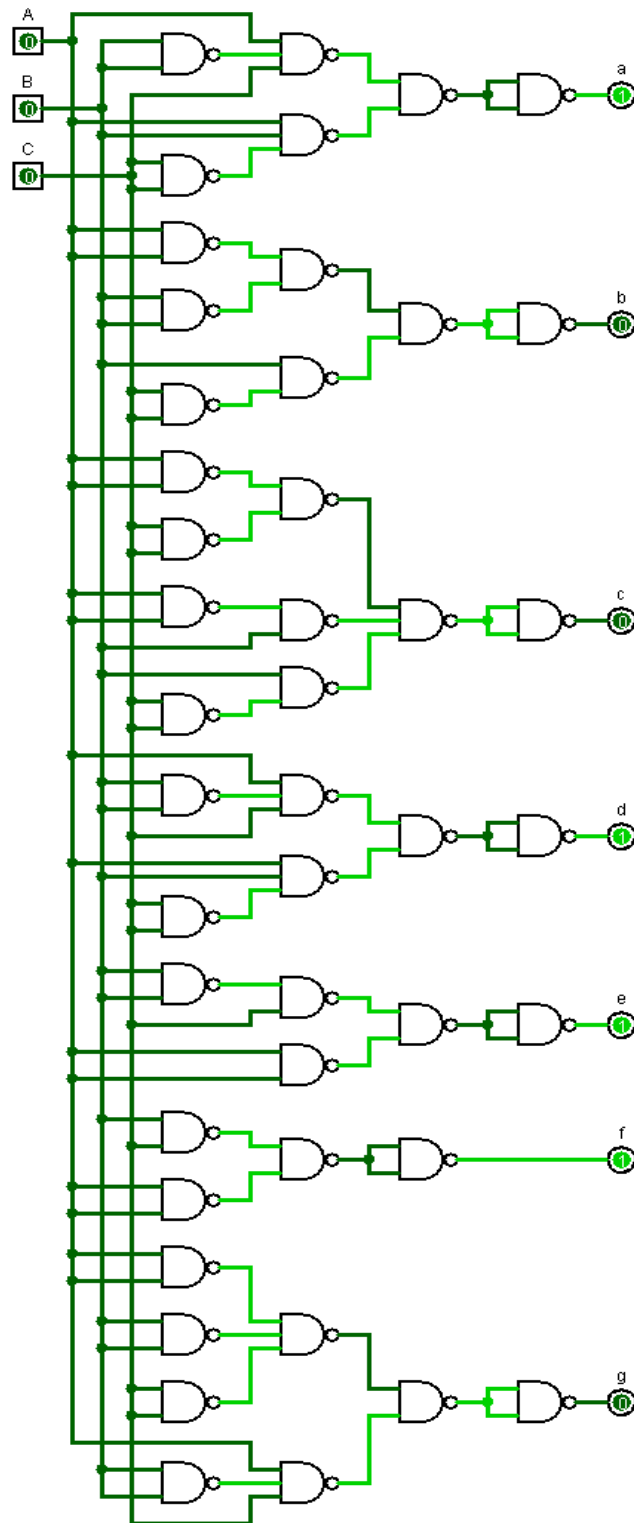


Fig: BCD to 7 Segment Decoder circuit

## Combinational Circuit – Using MUX

