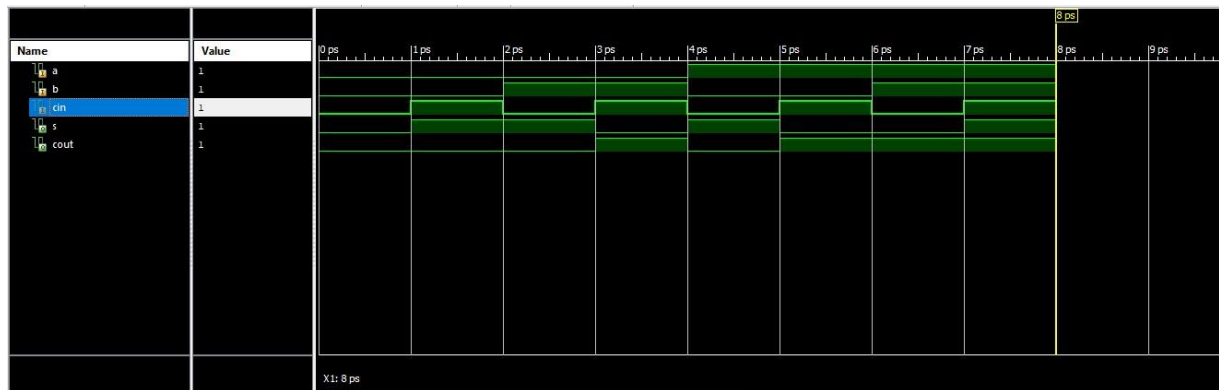
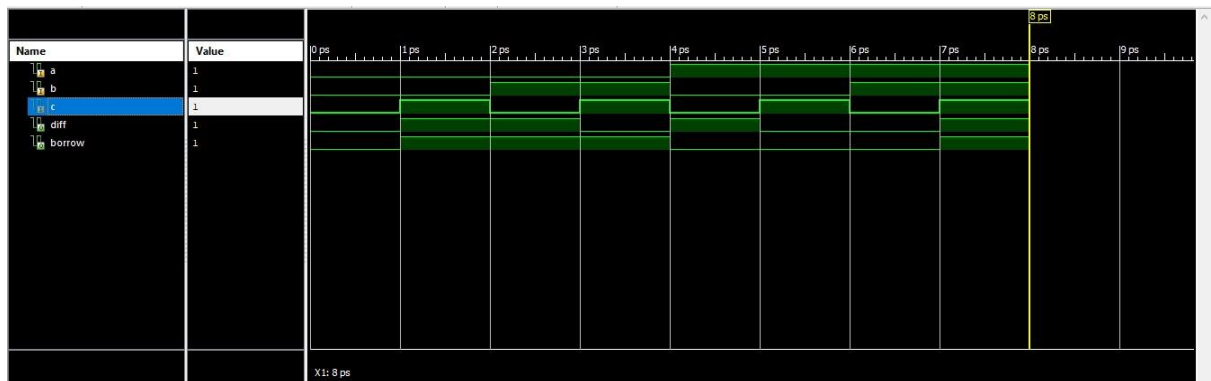


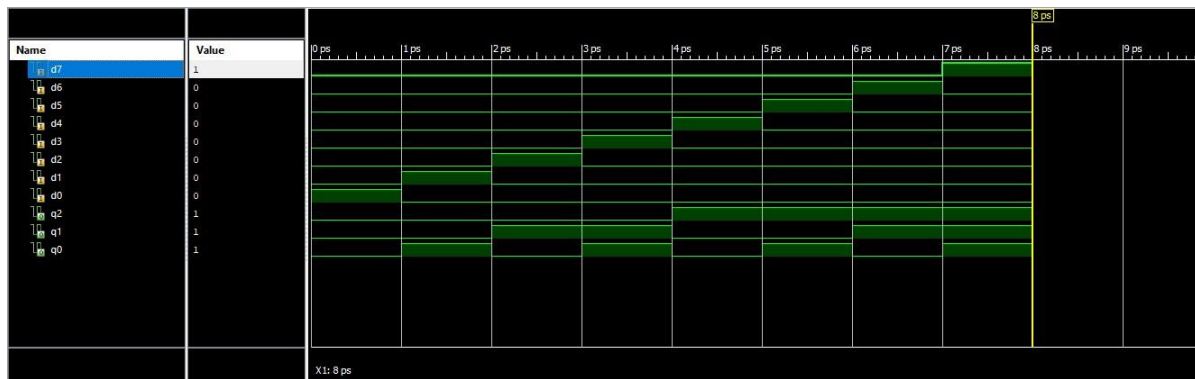
## Full Adder



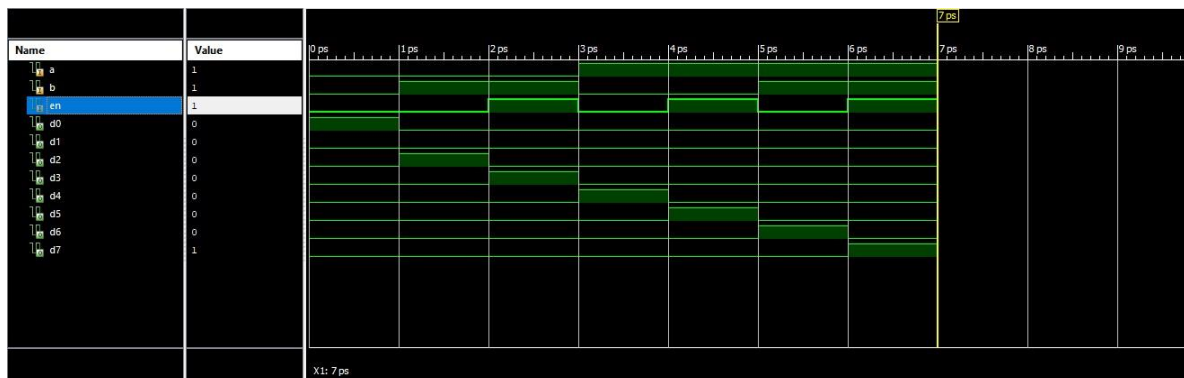
### Full Subtractor



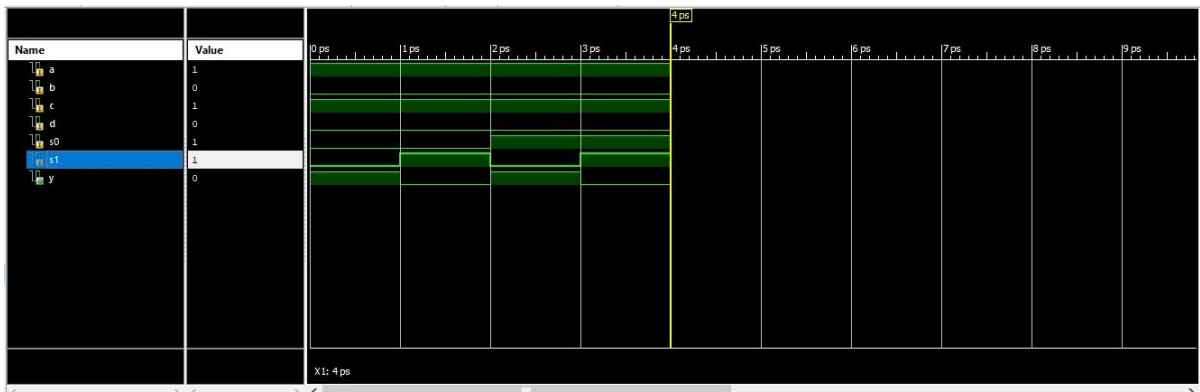
## Encoder



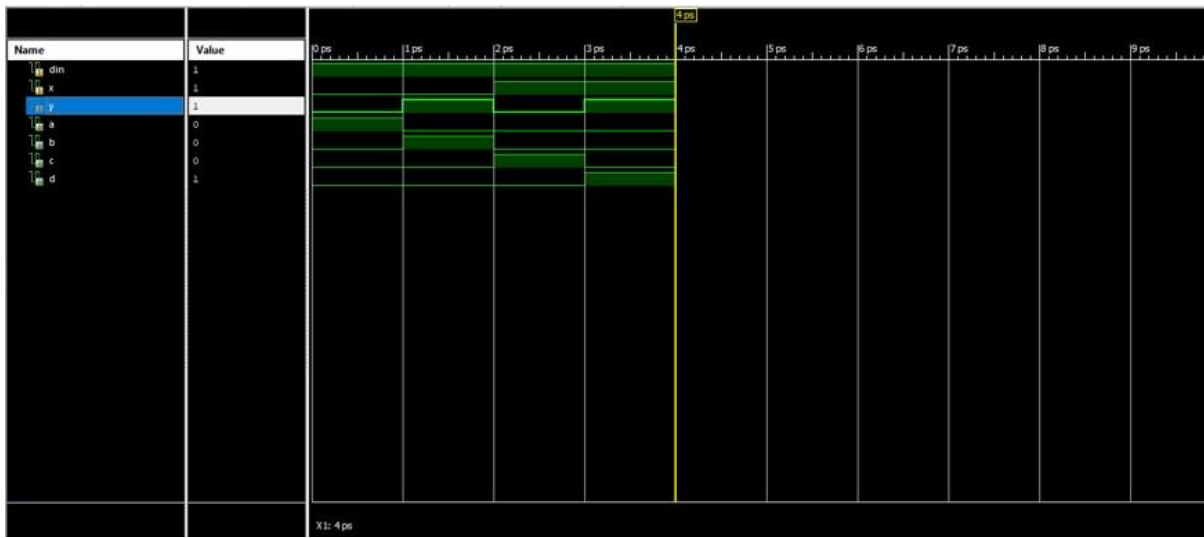
## Decoder



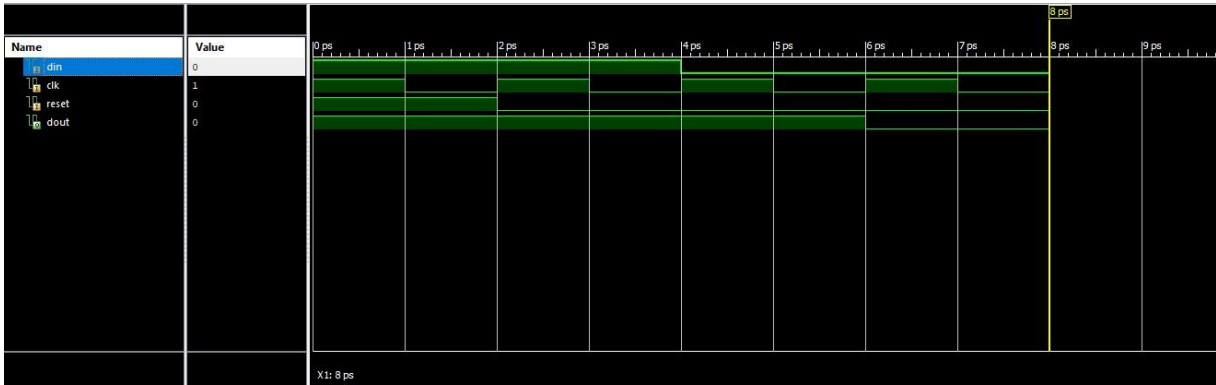
Multiplexer



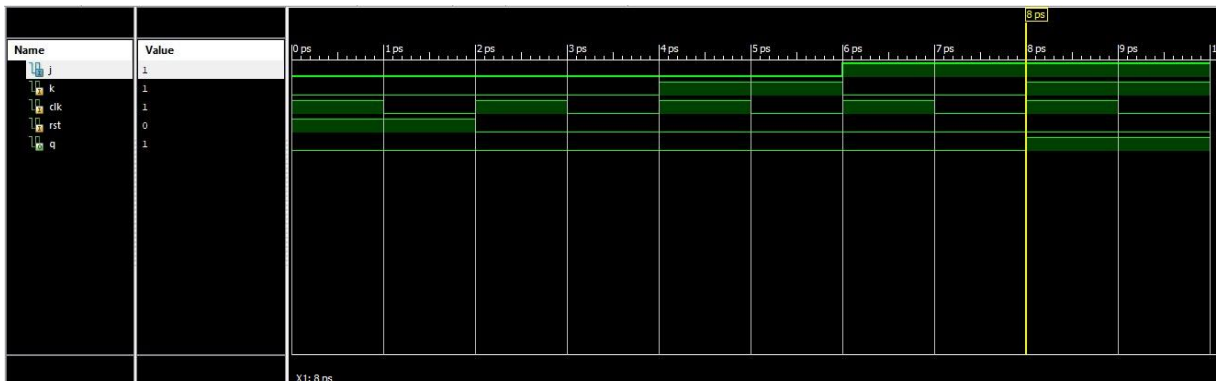
Demultiplexer



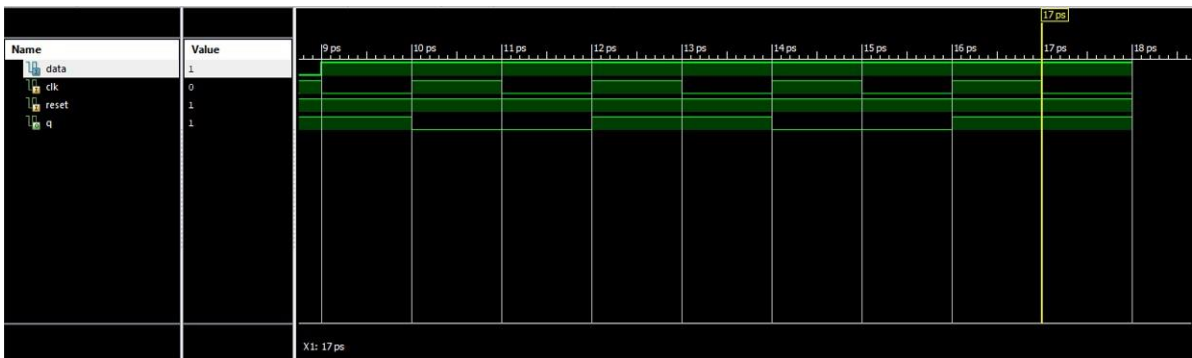
D Flip Flop



JK Flip Flop

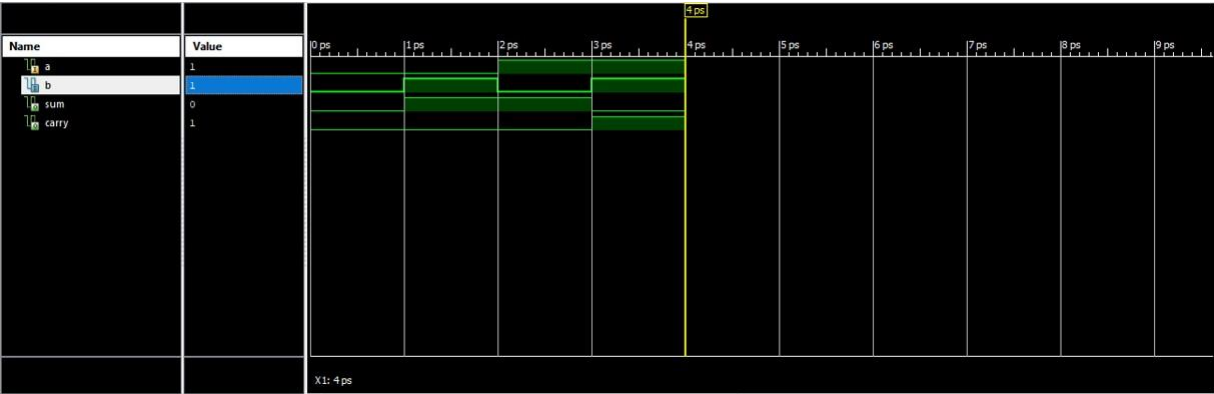


T Flip Flop



Half Adder

727721EUEC097



Half Subtractor

