TECHNICAL DATA SHEET

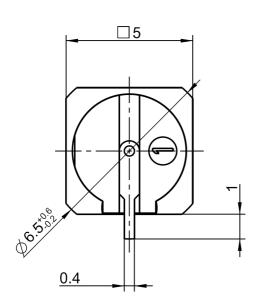
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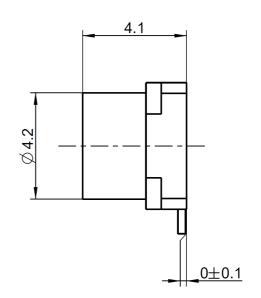
SMP

STRAIGHT PLUG PCB LIMITED DETENT

19S101-40ML5-NM







All dimensions are in mm; tolerances acc. ISO 2768 m-H

Interface

According to

MIL-STD-348

Documents

PCB layout

Tape & reel packaging

B 120 VG01.01M00

Material and plating

Non-magnetic version Connector parts

Center contact Outer contact

Dielectric

Material

Spring bronze Spring bronze

LCP

Plating

AuroDur, gold plated AuroDur, gold plated

RF_35/11.05/3.1

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Page

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1/2

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Electrical data

Impedance 50 Ω

Frequency DC to 26.5 GHz
Return loss \geq 26 dB, DC to 6 GHz \geq 20 dB, 6 to 12 GHz

Insertion loss $\leq 0.05 \text{ x } \sqrt{\text{f(GHz)}} \text{ dB}$

 $\begin{array}{lll} \text{Insulation resistance} & \geq 5 \text{ } G\Omega \\ \text{Center contact resistance} & \leq 6.0 \text{ } m\Omega \\ \text{Outer contact resistance} & \leq 2.0 \text{ } m\Omega \\ \text{Test voltage} & 500 \text{ } V \text{ } rms \\ \text{Working voltage} & 335 \text{ } V \text{ } rms \\ \text{Contact Current} & 1.2 \text{A DC max.} \end{array}$

Mechanical data

Mating cycles ≥ 500 Center contact captivation $\geq 7 \text{ N}$

Engagement force

- limited detent 45 N max.

Disengagement force

- limited detent 9 N min.

Environmental data

Temperature range -65°C to +155°C

Thermal shock MIL-STD-202, Method 107, Condition B
Vibration MIL-STD-202, Method 204, Condition B
Shock MIL-STD-202, Method 213, Condition A

Moisture resistance MIL-STD-202, Method 106
Max. soldering temperature IEC 61760-1, +260°C for 10 sec.

RoHS compliant

Tooling

N/A

Suitable cables

N/A

Weight

Weight 0.3 g/pc

While the information has been carefully compiled to the best of our knowledge, nothing is intended as representation or warranty on our part and no statement herein shall be construed as recommendation to infringe existing patents. In the effort to improve our products, we reserve the right to make changes judged to be necessary.

Draft	Date	Approved	Date		Rev.	Engineering change number	Name	Date
F.Michelmann	15/07/08	J_Krautenbacher	12.07.16		e00	15-1629	I_Wallner	12.07.16
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⁻ VSWR in application depends decisive on PCB layout -