This is an electronic schematic diagram titled "Arudino shiled" containing numerous components, connectors, and net labels.

Title block:

Title	Arudino shiled	
Size	Number	Revision
B	01.	01
Date:	4/17/2025	Sheet of 1
File:	M:\thesis\..\arduino_sh.SchDoc	Drawn By: Mulugeta.T Tadesse

Comment	Description	Designator	Footprint	LibRef	Quantity
EEEFT1V101AP	CAP ALUM 100UF 20% 35V SMD	C1, C2, C3, C4	FP-FTD-MFG	CMP-05427-000027-1	4
1N4148W-7-F	Fast Switching Diode, 100 V, 0.15 A, -65 to 150 degC, 2-Pin?SOD123, RoHS, Tape and Reel	D1, D2	DIOD-SOD123-2_V	CMP-2000-04932-2	2
Header 8	Header, 8-Pin	E1, E2, P7, P8, P8_2, P9, P9_2, P10, P10_2, P20, P20_2, P21, P21_2, X1, X2, Y1, Y2, Z1, Z2	HDR1X8	Header 8	19
Header 3X2	Header, 3-Pin, Dual row	E44, X11, Y22, Z33	HDR2X3	Header 3X2	4
TR/3216FF10-R	TR/3216FF10-R 10 A AC 24 V DC Fuse Board Mount (Cartridge Style Excluded) Surface Mount 1206 (3216 Metric)	F1	FP-3216FF-MFG	CMP-03047-09453137-1	1
Header 8X2	Header, 8-Pin, Dual row	mux_isc	HDR2X8	Header 8X2	1
Header 2	Header, 2-Pin	P2	HDR1X2	Header 2	1
Header 10	Header, 10-Pin	P3	HDR1X10	Header 10	1
Header 4	Header, 4-Pin	P4, P6, S_E, S_X, S_Y, S_Z	HDR1X4	Header 4	6
I2C	Header, 4-Pin	P5	HDR1X4	Header 4	1
Header 6	Header, 6-Pin	P11, P11_2	HDR1X6	Header 6	2
LIMIT_SWITCH	Header, 6-Pin, Dual row	P16	HDR2X6	Header 6X2	1
Header 18	Header, 18-Pin	P18	HDR1X18	Header 18	1
Header 18X2	Header, 18-Pin, Dual row	P19, P19_2	HDR2X18	Header 18X2	2
691214110002		power	691214110002	CMP-1502-02464-2	1
4.7k		R1, R2	RESC2013X70X40ML20T20	CMP-2001-04376-1	2
ERJ-6ENF1002V		R3, R4, R5, R6	RESC2013X70X40ML20T20	CMP-2001-04376-1	4
1825910-6	Tact Switch, SPST-NO, 0.05 A, -35 to 85 degC, 4-Pin THD, RoHS, Bulk	SW1	TECO-1825910-6_V	CMP-1684-00021-1	1
2717	Module: multiplexer; I2C interface multiplexer; TCA9548A; 3Ã·5VDC	TCA9548A	MODULE_2717	2717	1