

Assembly Language

- Its a low-level language (Not machine code) (Human understandable)
- Its a type of programming language which we can use to communicate with the hardware directly

Q- What are addressing modes?

- Methods of accessing memory are known as addressing mode.

Addressing Modes

- Immediate addressing
- Direct addressing
- Indirect addressing
- Indexed addressing
- Symbolic addressing
- Relative addressing

Concept of accumulator: It is a place where the results are stored.

Immediate Addressing



LDM = Immediate addressing

- Load the value next to LDM instruction into accumulator

Direct Addressing

- Memory will be involved

• `LDD <Address>`

e.g: `LDD 301` → 301
302

Location

300

301

302

Memory

20

30

100

- Step 1: Go to the given address

- Step 2: Load the value in that address into ACC

30

ACC

Indirect Addressing

Indirect Addressing = Lambi

• 2 addresses involved

• LDI (301) First address

Location

300

301

302

Memory

20

2nd

(302)

address

100

★ If question comes about what is an addressing ^{mode}, then explain the procedure of that addressing mode.

100

ACC

★ No double jump

ADDRESS

ADDRESS

Value

Index Addressing

- Calculation is required
- There will be a register known as index register (temporary memory location)

· **L D X** **300** → initial address

↳ $300 + [IX]$

$300 + [1]$

301 → New address

Location Memory

300

20

301

30

302

100

30

ACC

1

IX

Instructions

STO <address> store the content of accumulator at the given

can be a value as address.

ADD <address> ^{well} Add the content of the given address to ACC. (Added value stored in Acc)
_{with #}

INC <register> Add 1 to the content of ACC or IX

DEC <register> Subtract 1 to the content of ACC or IX.

CMP <address> Compare content of ACC with content of address

JPE <address> Following a compare instruction, jump to address if comparison was true.

JPN <address> Following a compare instruction, jump to address if comparison was False

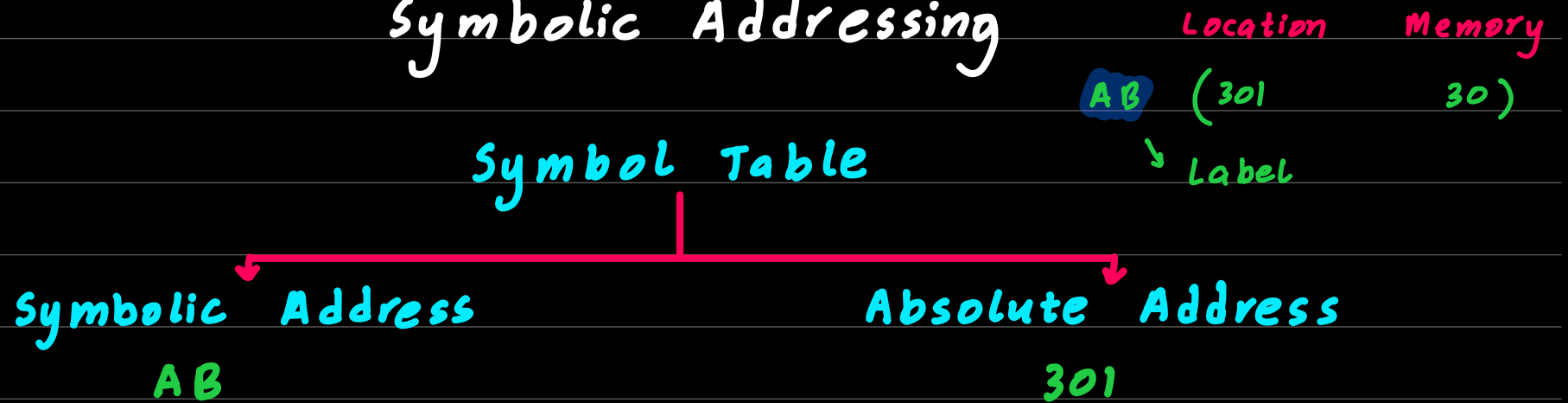
JMP <address> Jump to the given address



OUT output the character represented by the ASCII code in ACC.

END End of assembly code

Symbolic Addressing



Q- Explain how Assembler makes entries to the symbol table?

- The assembler scans the assembly language instructions in sequence.
- When it meets a symbolic address, it checks if that symbol is in the symbol table
- If not, it adds it to the symbol table in the symbolic address column.
- If it is already in the symbol table, it checks if absolute address is known
- If the absolute address is known, it is entered in the appropriate cell.
- If the absolute address is unknown, leave it as unknown

One - Pass Assembler

- It puts the machine code instructions into the computer memory
- ★ One-pass assembler converts source-code into machine code and loads it into the memory.

Two - Pass Assembler

- Needs to scan the source program twice, so they can replace labels in the assembly program with memory addresses in the machine code program.
- ★ Machine code is produced in the 3rd Pass.

Absolute Addressing: The operand is a numeric address

Symbolic Addressing: The operand is a symbol/label and that represents memory location

Relative Addressing: Form the address by adding the given number to the base address and then load the value on that address.