

Mean Position: 5.612 Theoretical Mean: 2.400 Variance: 1.643 Standard Deviation: 1.282 Theoretical Std Dev: 1.296

Bias Parameter: 0.3 Number of Levels: 8

0.10

0.05

Position

Number of Qubits: 8 Circuit Depth: 8 Bias Parameter: 0.3

- Circuit Operations:
 RY gates for bias
 CX gates for entanglement
 RZ gates for phase
 Measurement