

REPORT

on

“VLSI Circuits Design Laboratory”

**Experiment-III: “Implementing CMOS-NAND
& CMOS-NOR using Microwind 2.”**

Course Code: CSE-406

Submitted by

Md. Tazel Hossan

Roll No. 1921 of 2017-18

4th Year 1st Semester

Submitted to

Anup Majumder

Lecturer



**Department of Computer Science & Engineering
Jahangirnagar University
Savar-1342**

Date Of Submission: 25 Jun, 2022