

# REPORT

on

*“VLSI Circuits Design Laboratory”*

Experiment-II: “Implementing CMOS-NOR  
using DHCH2/3 & Microwind 2.”

Course Code: CSE-406

*Submitted by*

**Md. Tazel Hossan**

Roll No. 1921 of 2017-18

4<sup>th</sup> Year 1<sup>st</sup> Semester

*Submitted to*

**Anup Majumder**

Lecturer



Department of Computer Science & Engineering  
Jahangirnagar University  
Savar-1342

*Date Of Submission: 19 Jun, 2022*