REPORT

on

"VLSI Circuits Design Laboratory"

Experiment-VI: "Design & implementation of XOR & XNOR on DHCH2 & Microwind2."
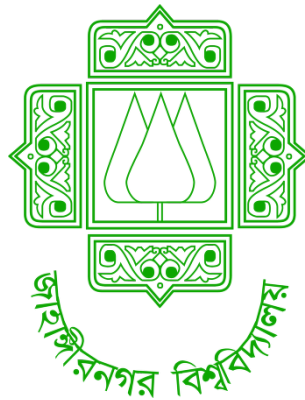**Course Code: CSE-406**

Submitted by

Md. Tazel Hossan

Roll No. 1921 of 2017-18

4th Year 1st Semester

Submitted to

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