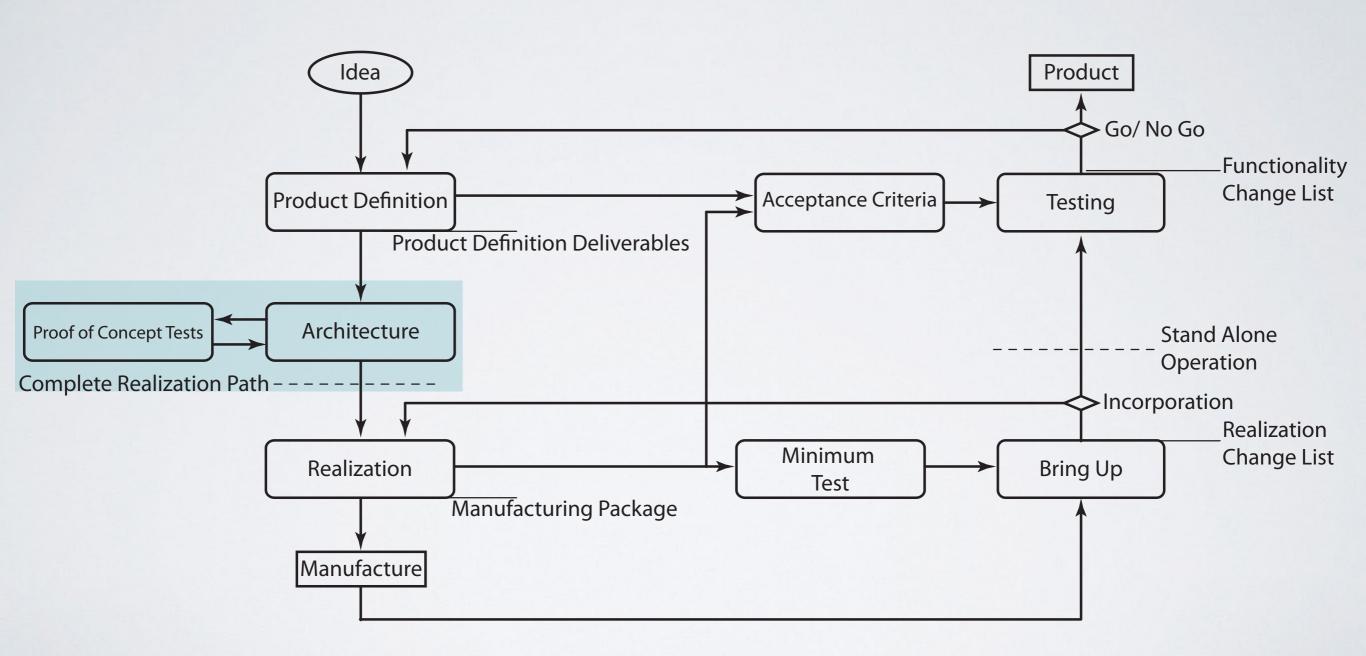
#### ARCHITECTURE PHASE

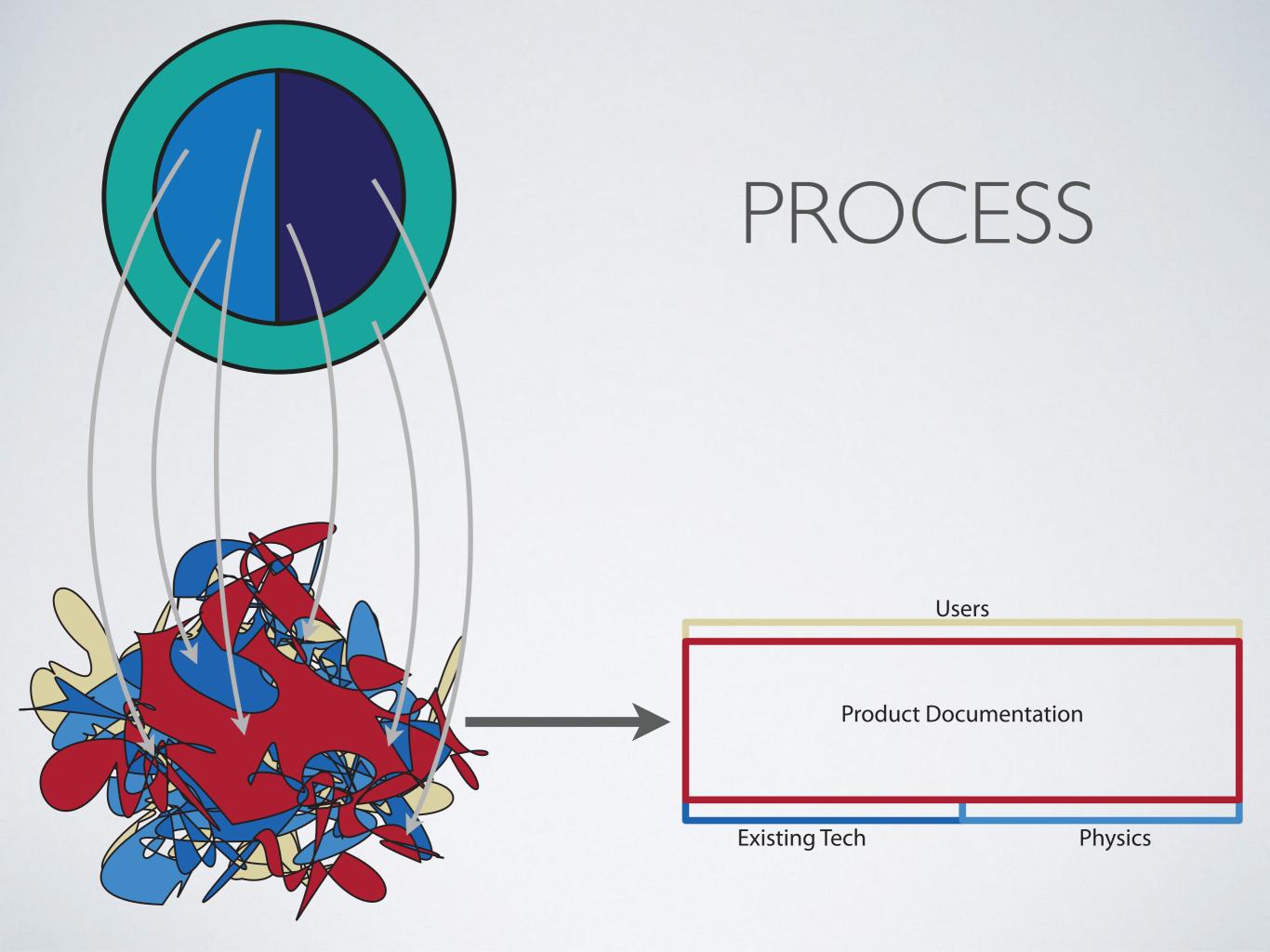
The Engineer Accelerator Malcolm Knapp

### DEVELOPMENT PHASE



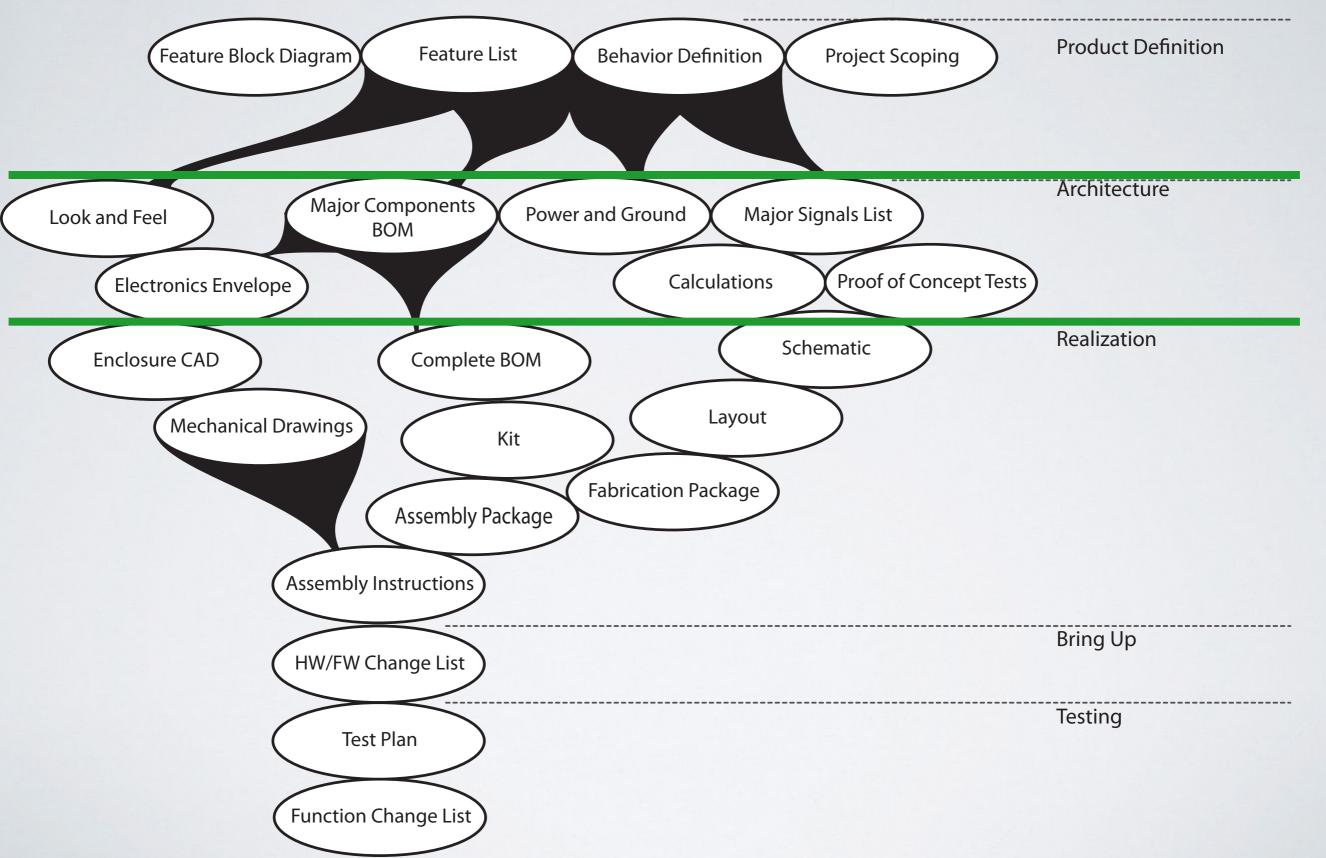
#### GOAL

### TO DETERMINE THE REALIZATION PATH FOR ALL FUNCTIONALITY

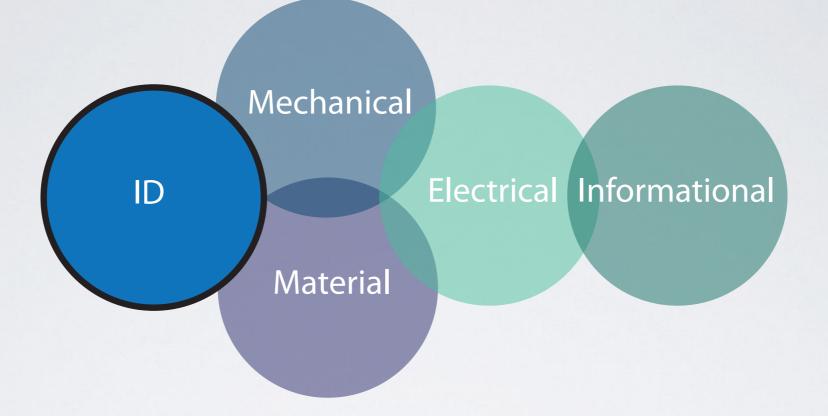


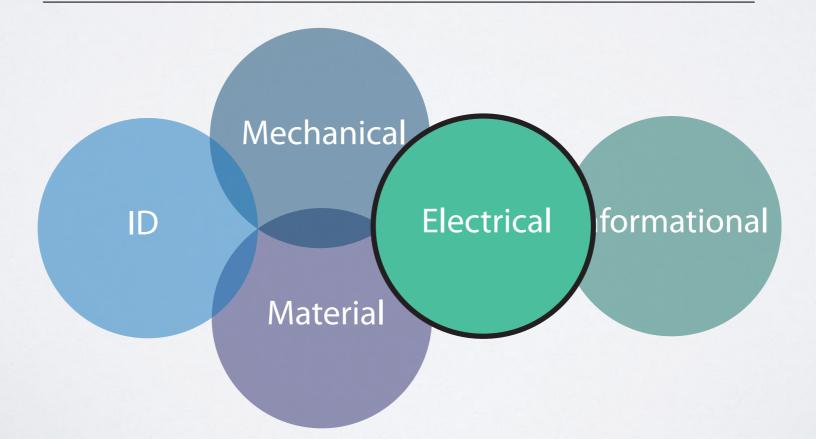
# SOME ONE HAS SOLVED THE PROBLEM BEFORE

### DESIGN DEPENDENCY



#### DESIGN DRIVER





#### ARCHITECTURAL INFORMATION

- Power and Ground Distribution
- Major Components
- Major Signals
- Pinouts
- Design Calculations
- Board Outline Drawing

#### ENGINEERING OVERLAP



Component Size
Connector Pinouts
Connector Locations
Board Outline

Microcontroller Pinout Current Draw

Electrical

formati

Manufacturing Process

laterial

#### PROOF OF CONCEPTIESTS

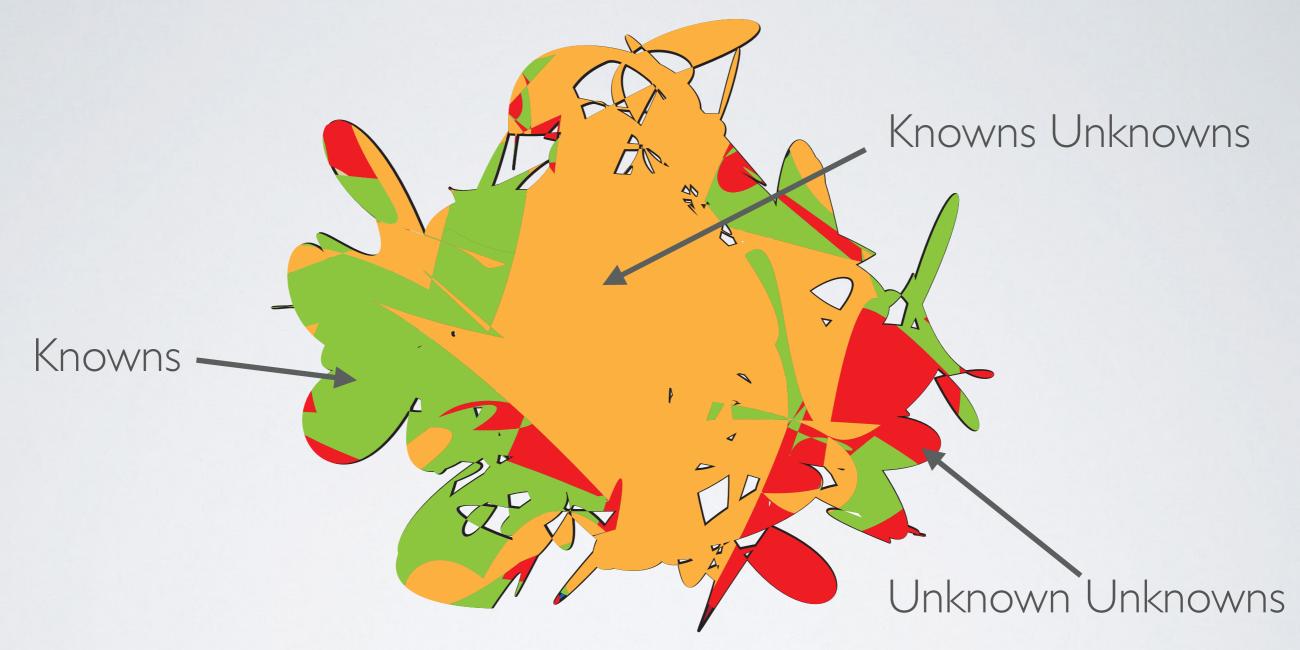
- Validates Realization Path for each Feature,
   Behavior or Interface.
- Development Process in its own right

#### CONSTRAINTS

- Component Sourcing and BOM Costing
- Regulations
- Standards
- Environment
- Human Factors

## WHAT DO YOU WORK ON FIRST?

#### KNOWNS AND UNKNOWNS



# START WITH WHATYOU KNOW LEAST ABOUT

#### DELIVERABLES

- Major Components BOM
  - Manufacturer and Vendor information for IC, Connector,s etc
- Product Architecture
  - Major Signals Characteristics
  - Current Budget
  - Pinouts
  - Calculations
- Engineering Block Diagram
  - Connections between major components
- (Board Outline Drawing)

#### WHAT IS COMPLETE

- Major Components are determined
- Overlaps are enumerated
- When you are confident that all functionality can be made