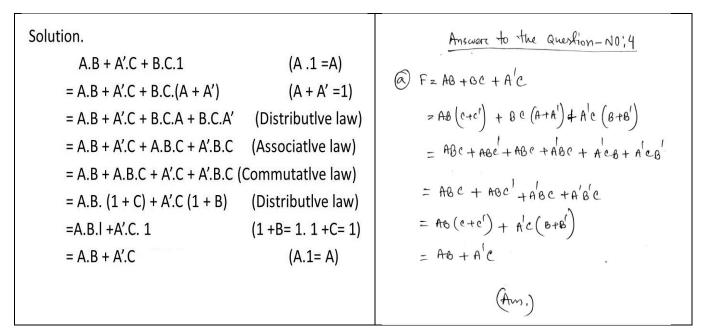
#### **Tabulation Method:**

#### Boolean algebra (Any one solution)



$$F = AB + Bc + A'c$$

$$= AB + A'c + BC$$

$$= AB + A'c + BC \cdot 1$$

$$= AB + A'c + BC \cdot (A + A')$$

$$= AB + A'c + ABC + A'BC$$

$$= AB + ABC + A'C + A'BC$$

$$= AB(1+C) + A'C(1+B)$$

$$= AB(C+D) + A'C(B+D)$$

$$= AB \cdot 1 + A'C \cdot 1$$

$$= AB + A'C \cdot 1$$

# Ans. to the Q. no-1

The range of an 8 bit 2's complement numbers system is from  $-2^{(8-1)} = -128$  to  $2^{(8-1)} - 1 = 127$  in decimal number system.

$$\begin{array}{rcl}
 & + (1073)_{10} & = & + (01001001)_{2} \\
 & - (35)_{10} & = & - (00100011)_{2} \\
 & \cdot \cdot \cdot \cdot + (73)_{10} & = (01001001)_{1/3}
\end{array}$$

And, 
$$+(73)_{10} = (01001001)_{2,3}$$
  
 $-(35)_{10} = (11011101)_{2,3}$ 

$$+(73)_{10} = (01001001)_{\text{sign-mag}}$$
  
 $-(35)_{10} = (10100011)_{\text{sign-mag}}$ 

2's complement

$$73 = 01001001$$

$$36 = 00100011$$

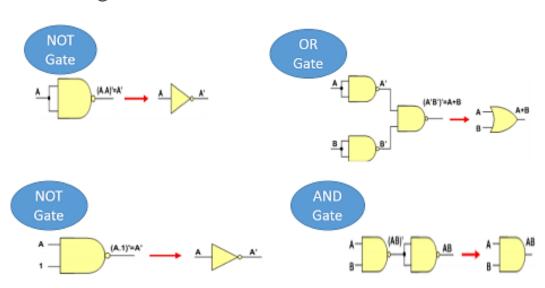
$$(08) = 01101100$$

It is not an overflow a because the some sign bits gives the same sign bit.

1 335 - Cr 1 COOM 4 255 - Cr 1 DOC

#### **Construct Gates**

## Using NAND



#### **Kmap Solution**

### Design example: 2-bit multiplier (SOLUTION)

