



MURALIDHARAN P

*Electronics and Communication
Engineering Student*

CONTACT ME



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<https://www.hackerrank.com/muralidharanpas1>



<https://github.com/Muralidharan2003>

AREA OF INTEREST

- UI/UX design
- Embedded Programming
- PCB design

TECHNICAL SKILLS

- C, C++
- Basics of Java and Python
- SQL
- Basics of HTML, CSS and Javascript

TOOLS

- Unity (Game development)
- Arduino IDE
- Proteus
- KiCAD
- Adobe Photoshop

CAREER OBJECTIVE

Looking forward to secure a responsible career opportunity to fully utilize my skills for the growth of the organisation and to enhance my knowledge.

EDUCATION

BANNARI AMMAN INSTITUTE OF TECHNOLOGY

B.E Electronics and Communication Engineering
CGPA: 8.91 (Till 5th semester)

SRI VIJAY VIDYALAYA MATRIC HR SECONDARY SCHOOL

Secondary Schooling.

- SSLC Percentage: 95.8%
- HSC Percentage: 88%

SRI BALAJI HI-TECH SCHOOL

Primary Schooling.

INTERNSHIP

TESSOLVE SEMICONDUCTORS

VLSI design and Embedded IOT Programming

ROLE: Trainee

Learnt Digital Electronics, Verilog Programming and worked on mini projects using embedded microcontrollers

PATENT

Suicide Prevention Using Load Cell
(Application Number: 202341001727)
Prevention of suicide using NodeMCU and Load Cell

ACHIEVEMENTS (INTERNATIONAL)

- Medallion for Excellence Winner at BRICS World Skills Competition, China(Oct-Nov 2022)
- Top 20 Finalist at Euro circuits PCB design contest, Belgium. I have been awarded 500 Euros to complete the prototype of my model.
- First Runner-Up at Cisco IOT Hackathon at BITS PILANI.(May 2022) with Rs.3000 cash prize

ACHIEVEMENTS (NATIONAL)

- Second Runner-Up Project Presentation in Dr.MGR University(April 2022)
- Second Prize Paper Presentation at IETE Students Forum,BIT(Oct 2022)
- Second Prize Project Presentation at IETE Students Forum,BIT(Nov 2022)
- Second Prize Project Presentation IEEE,BIT(Nov 2022)

HOBBIES

- Star gazing
- Reading books

PERSONAL SKILLS

- Confident speaker with good communication skills
- Team management
- Art and craft
- Writing stories and quotes

PERSONAL INFO

- Date of Birth : 31 July 2003
- Nationality : Indian
- Father's Name : Pasupathi N
- Mother's Name : Vimala P
- Languages Known: Tamil(R,W,S)
English(R,W,S)

PROJECTS

- **ENERGY MONITORING USING ESP8266**
ROLE: Hardware,PCB design,HTML webpage design
TEAM SIZE:3
- **REMOTE PATIENT MONITORING SYSTEM USING ESP32**
ROLE:Hardware,PCB design
TEAM SIZE:3

CERTIFICATIONS

- NI Certified Labview Associate Developer
87.5%
(Valid till May 2024)
- Introduction to Networks: CCNA
(CISCO certified)

PAPER PRESENTATION

- NCAMMT 2022
(Karpagam Academy of Higher Education)

PROJECT PRESENTATION

- KRISHNECS 2022
(Sri Krishna College Of Engineering and Technology)
- EW Project Challenge 2022

TECHNICAL COMPETITION

- Asia Pacific Microwave Conference 2022(Bandpass Filter Design)
- L and T Techgium 2022

DECLARATION

- I MURALIDHARAN P, hereby declare that the above written particulars are true to the best of my knowledge

(MURALIDHARAN P)