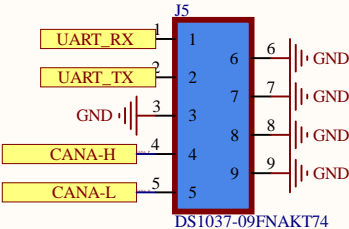
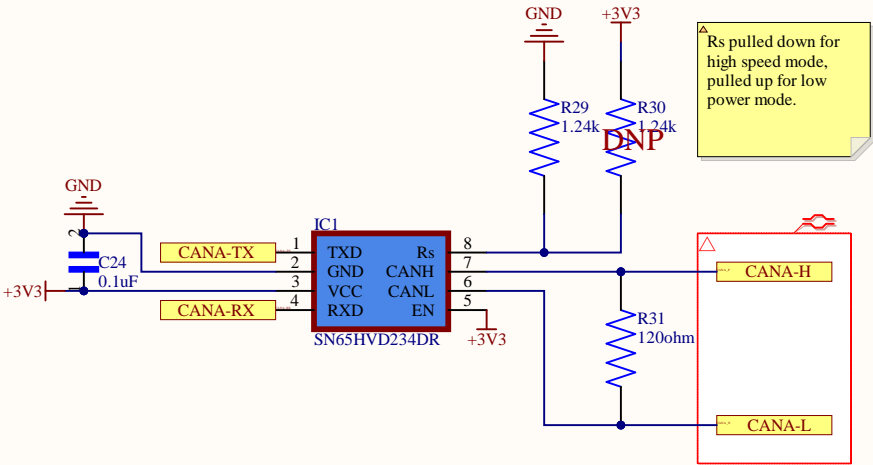


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Date:	8/14/2021	Sheet of
File:	C:\Users\...\Block Diagram.SchDoc	Drawn By:

CAN PORT PINOUTS

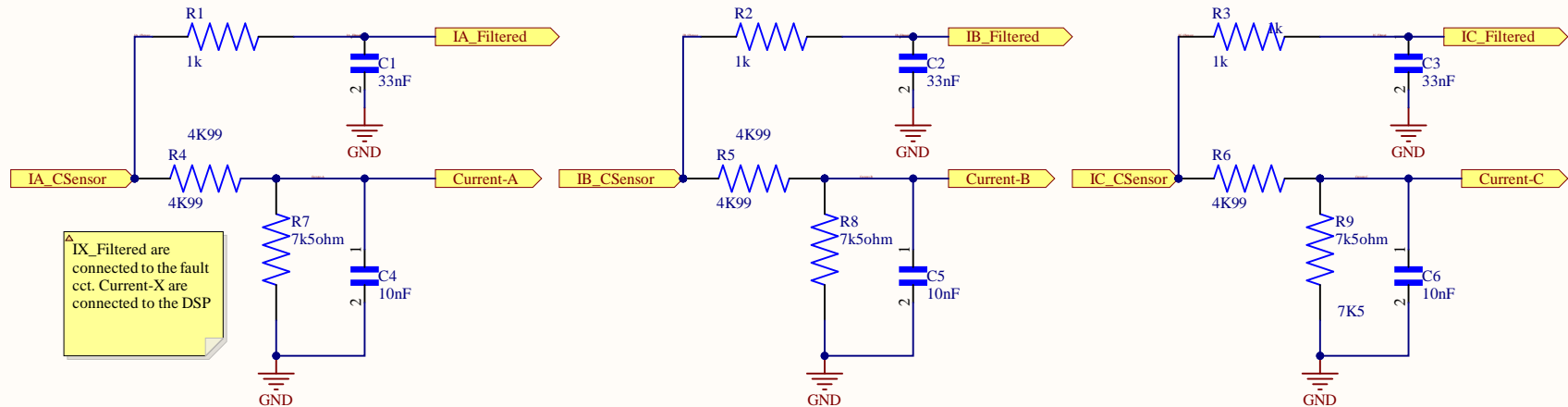


CAN TRANSCEIVER

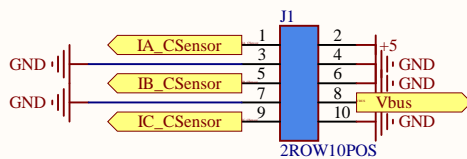


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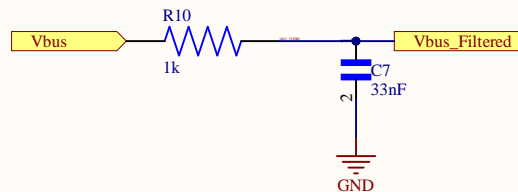
ANTI ALIASING FILTERS FOR DSP AND FAULT CCT.



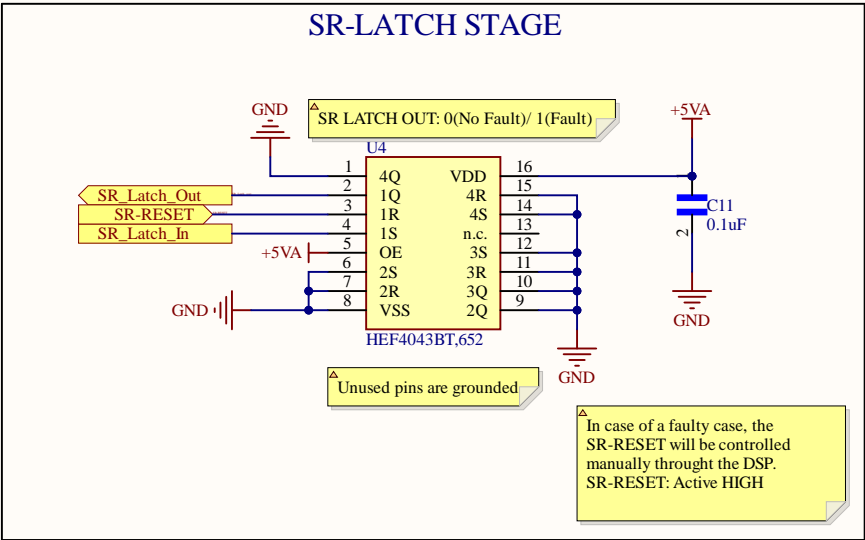
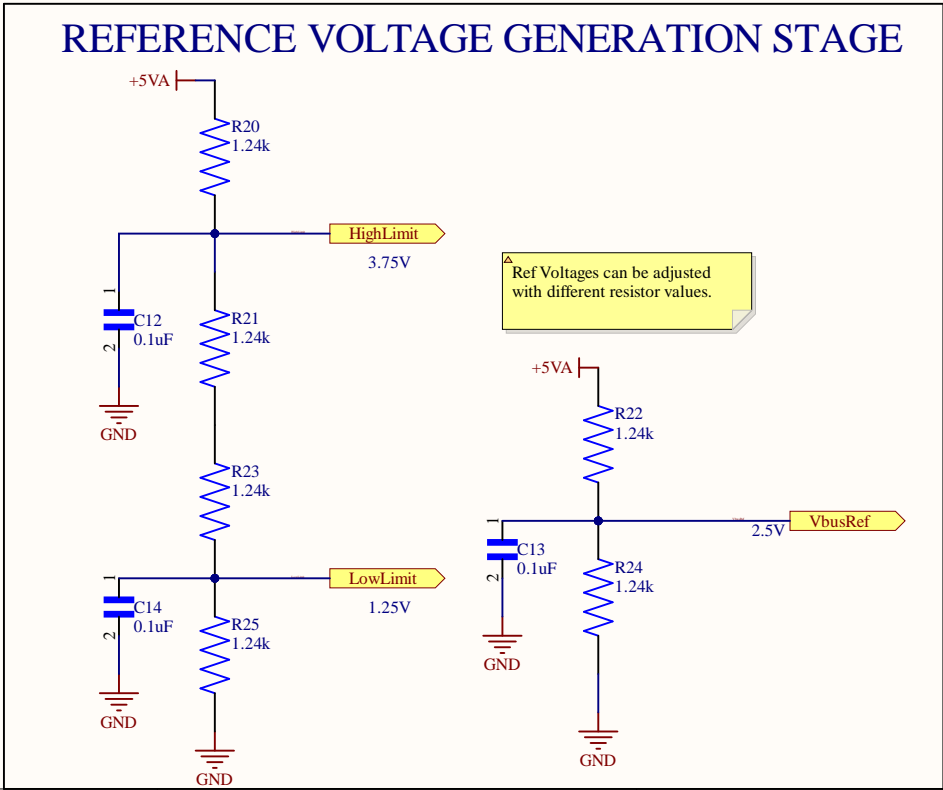
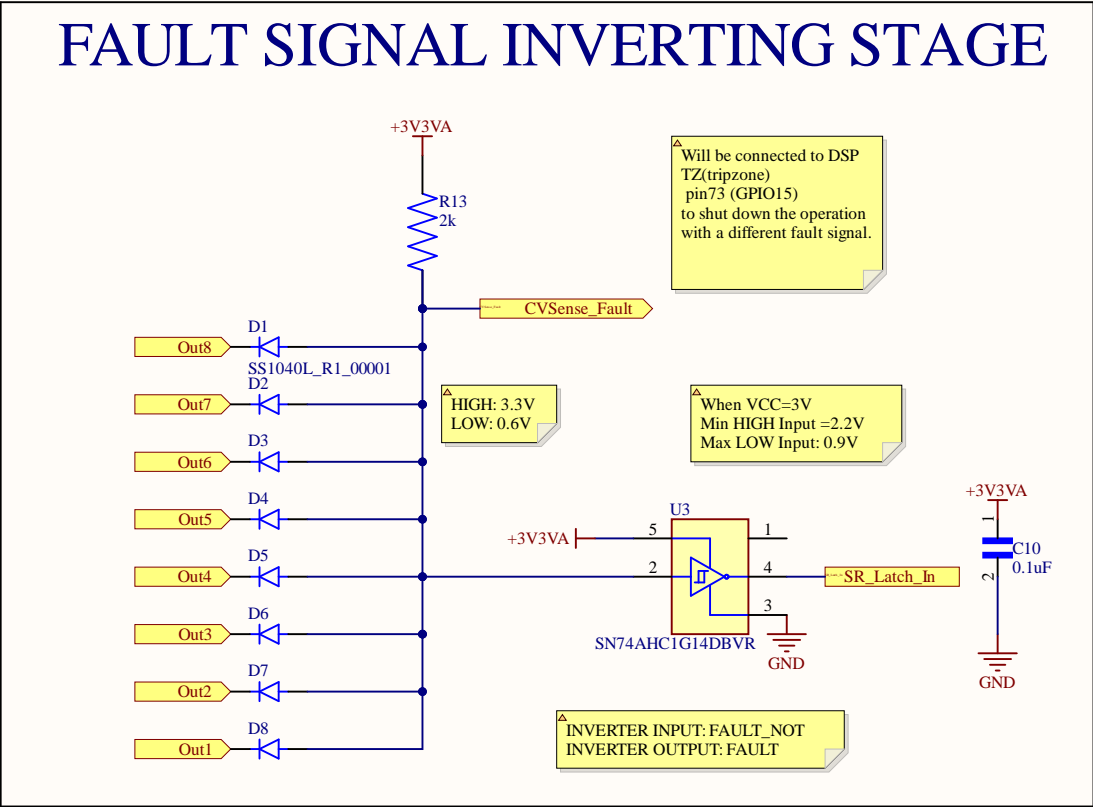
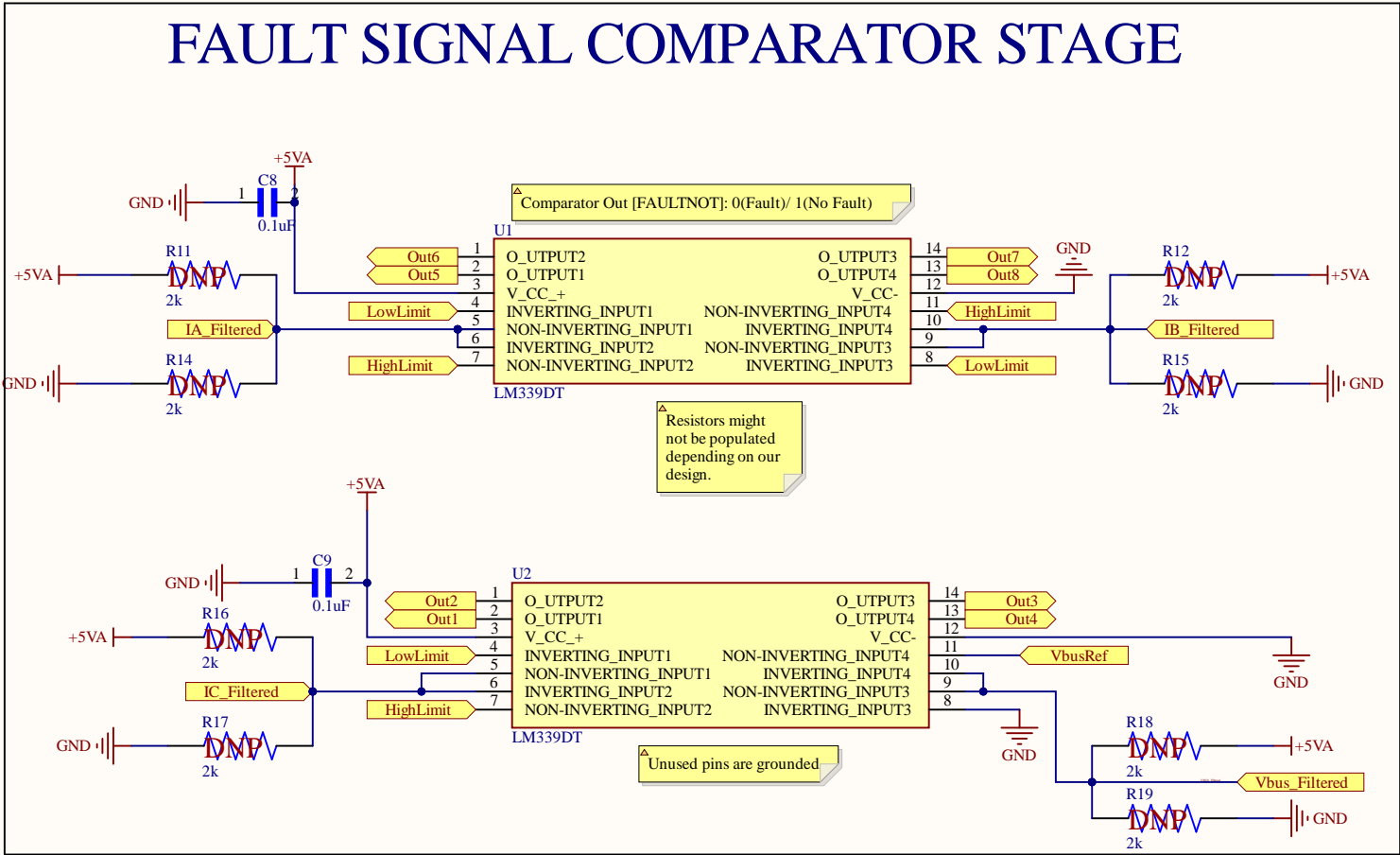
CURRENT SENSOR CONNECTOR/ VBUS



VBUS Signal Filtering

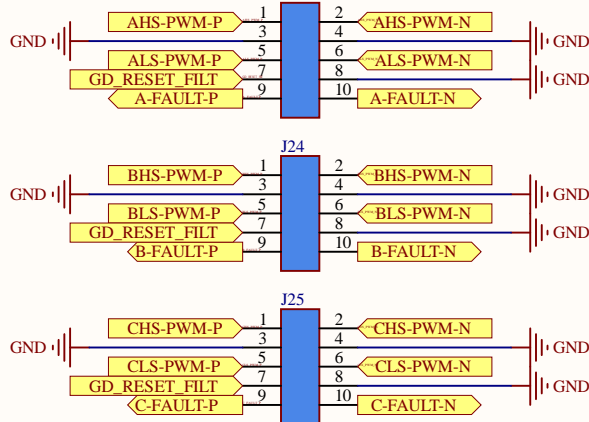


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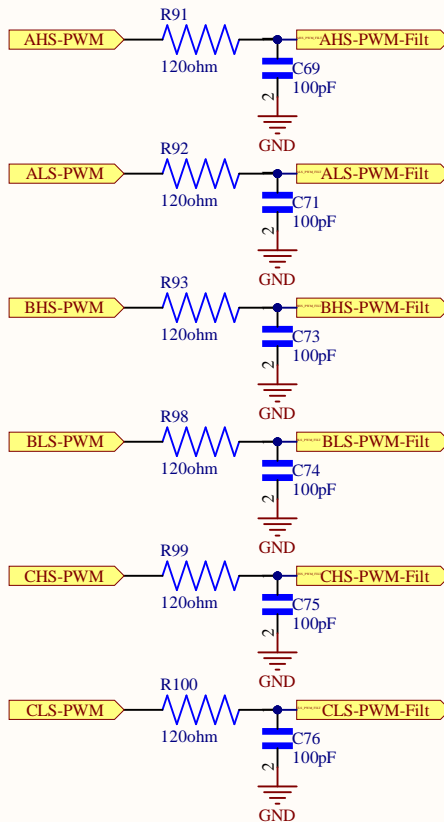


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B		
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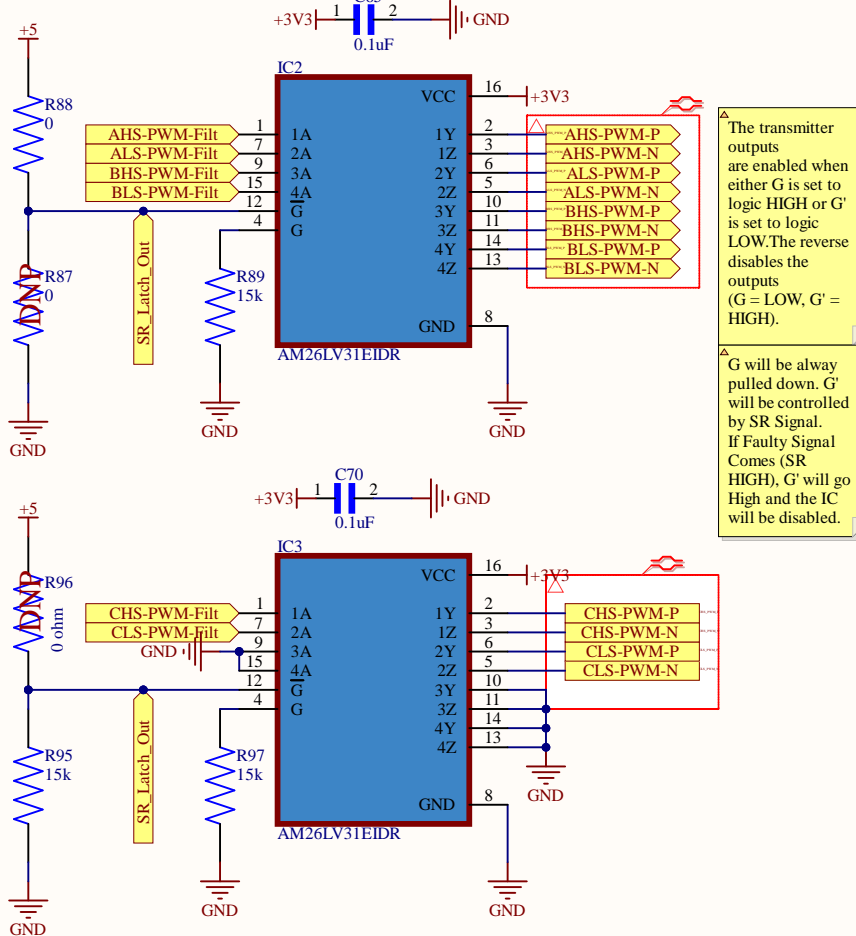
Connector



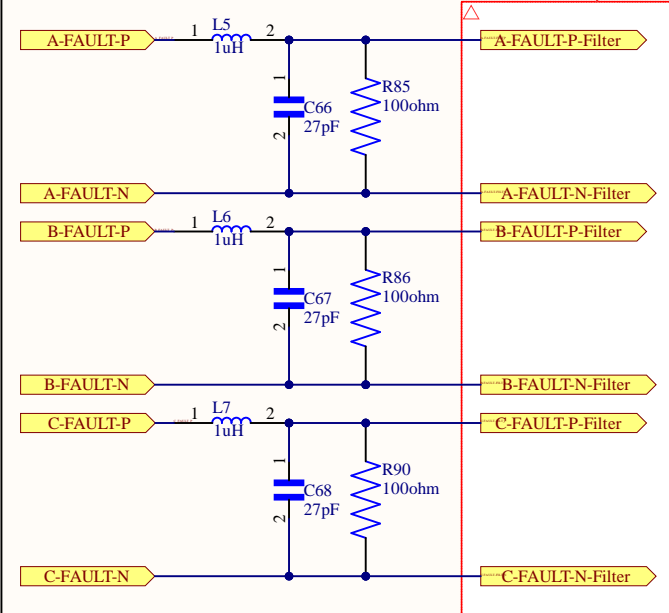
PWM Signal Anti-Aliasing Filtering



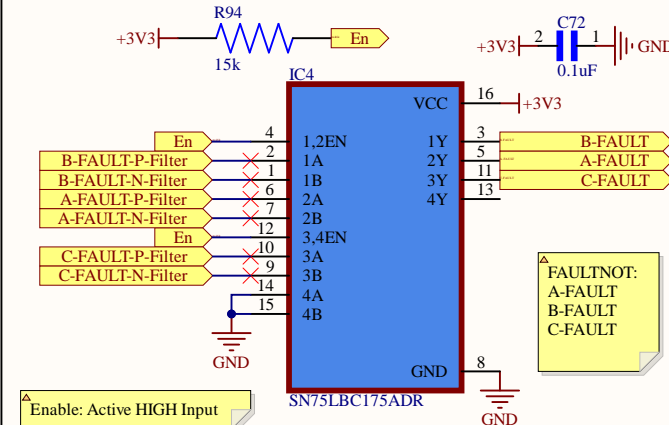
Diff. Line Driver for PWM Signals



FAULT Signal Filtering

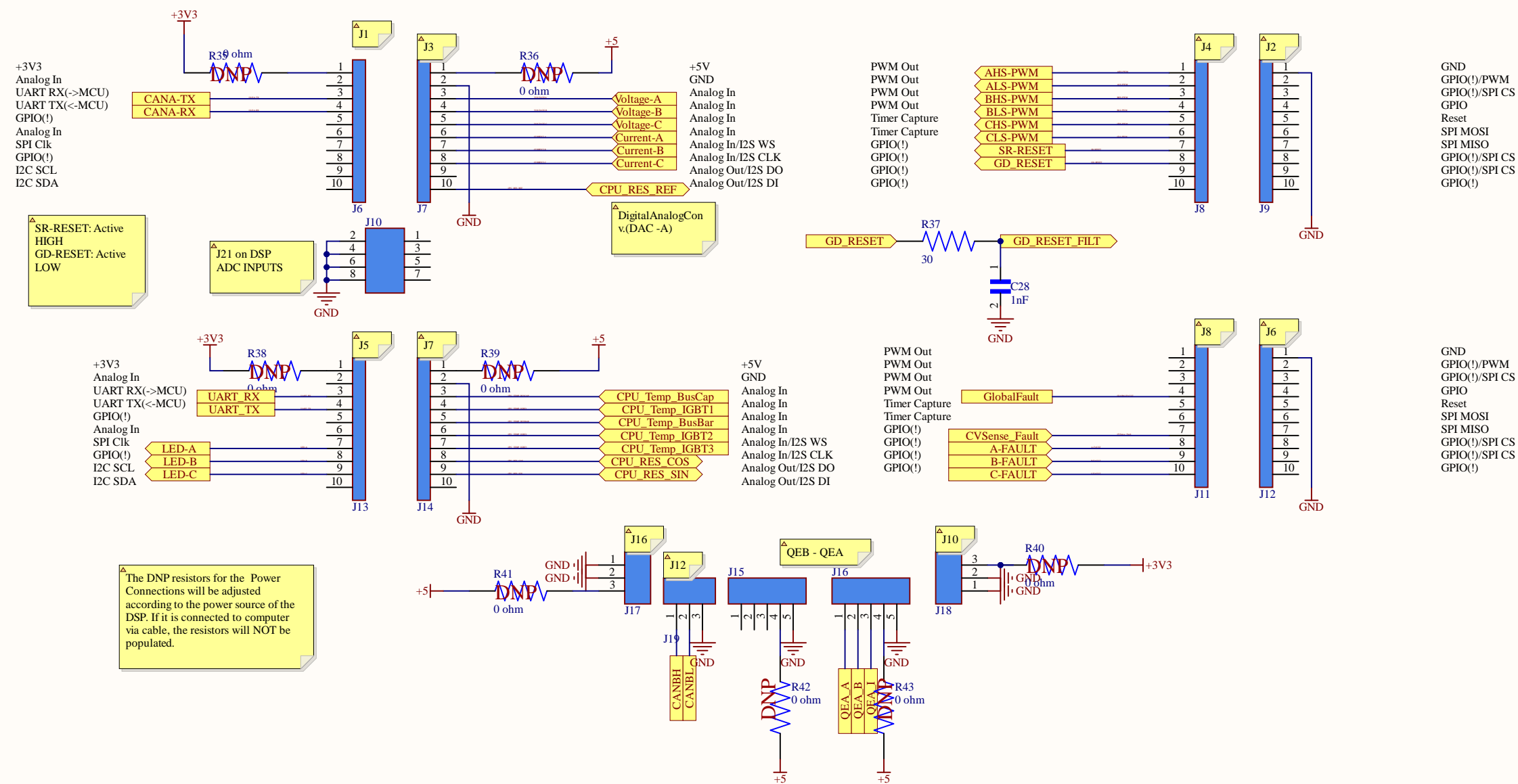


Diff Line Receiver for Fault Signals

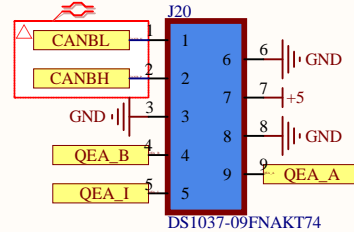


Title		
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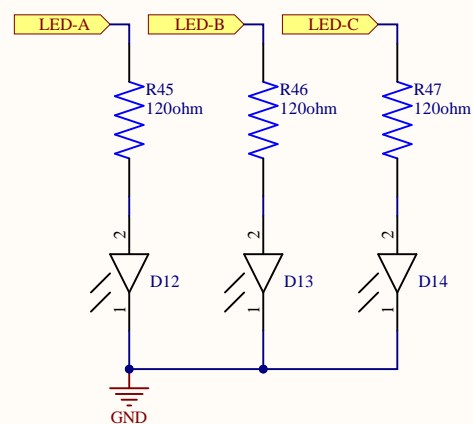
DSP CONNECTORS



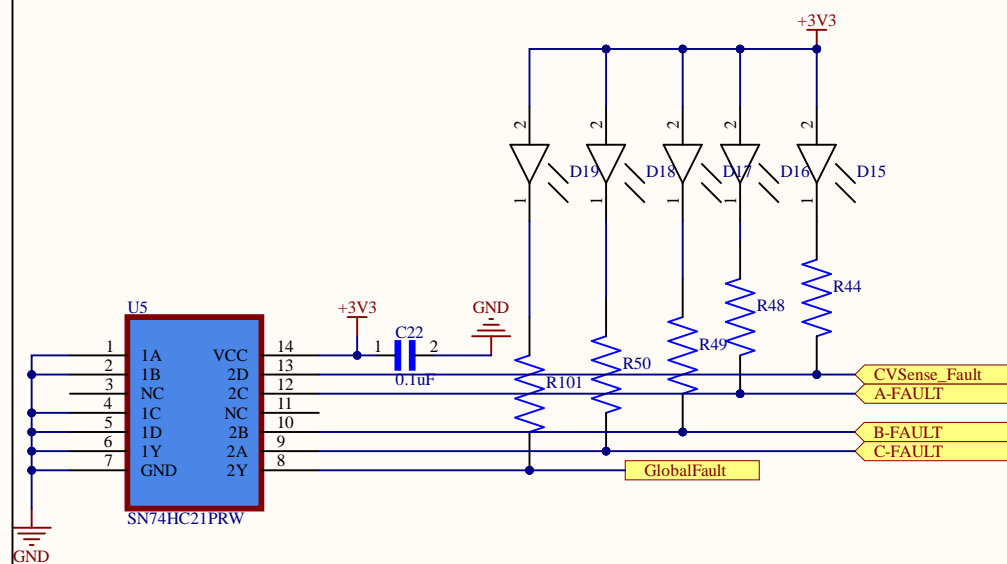
AUX CONNECTOR



INDICATOR LEDs



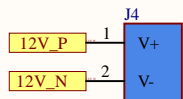
GLOBAL FAULT GENERATION



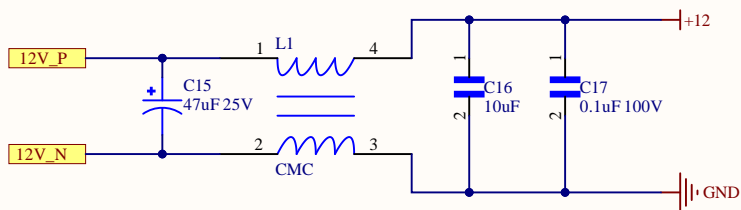
All Fault Inputs (VCsense, A, B, C) are FAULTNOT. --> HIGH is No Fault condition. --> LOW is FAULT condition. When there is a fault, AND gate outputs in a LOW voltage.

Title		
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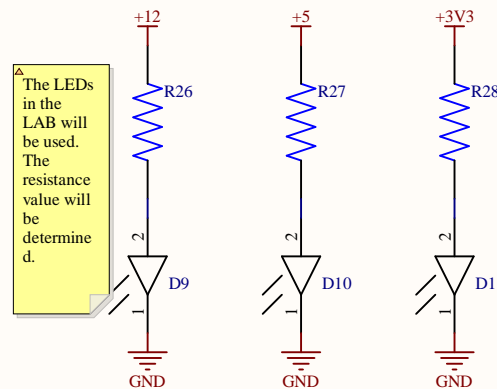
BATTERY CONN



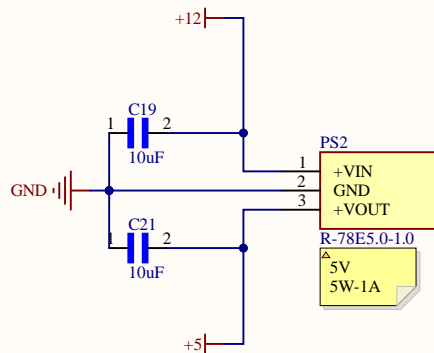
COMMON MODE CHOKE



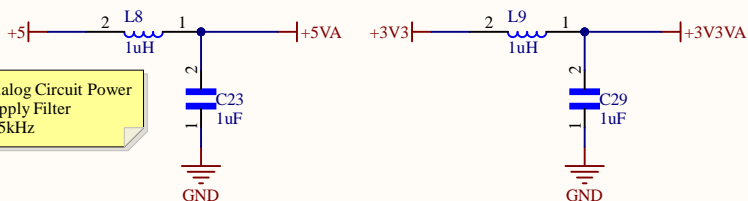
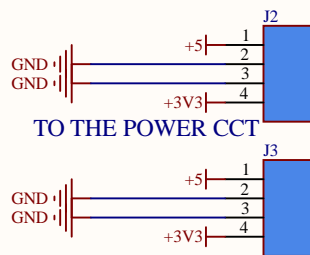
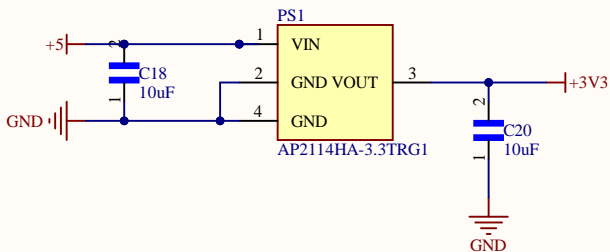
POWER ON INDICATORS



12V TO 5V



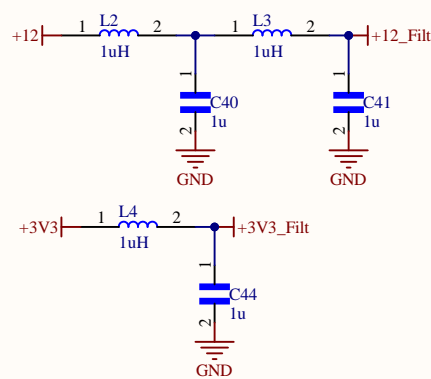
5V TO 3.3V



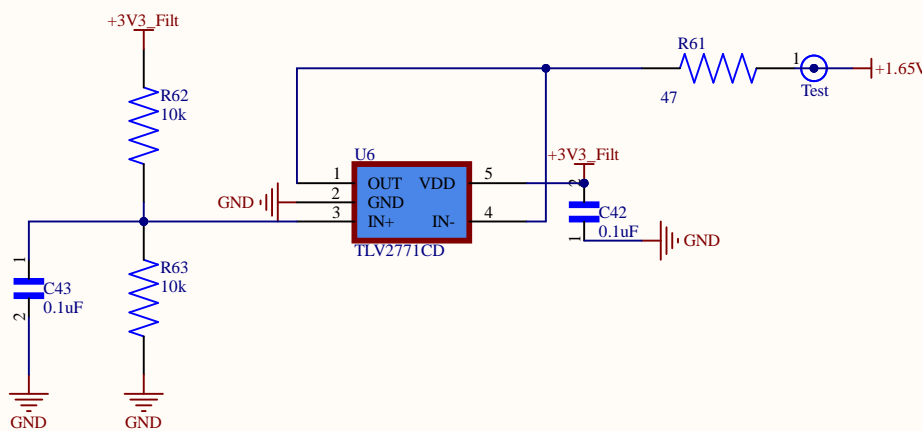
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RESOLVER INTERFACE

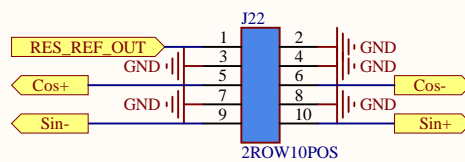
POWER FILTERS



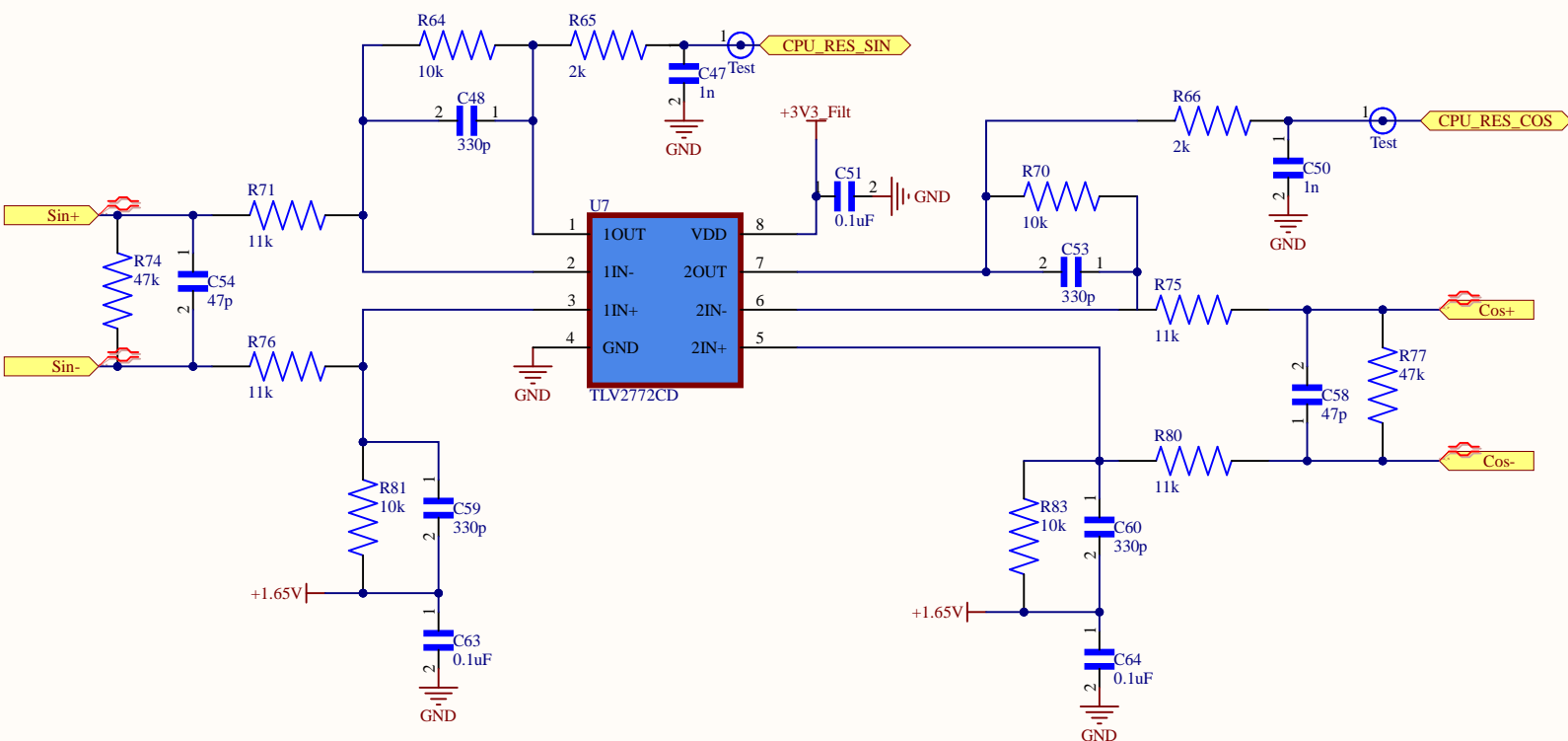
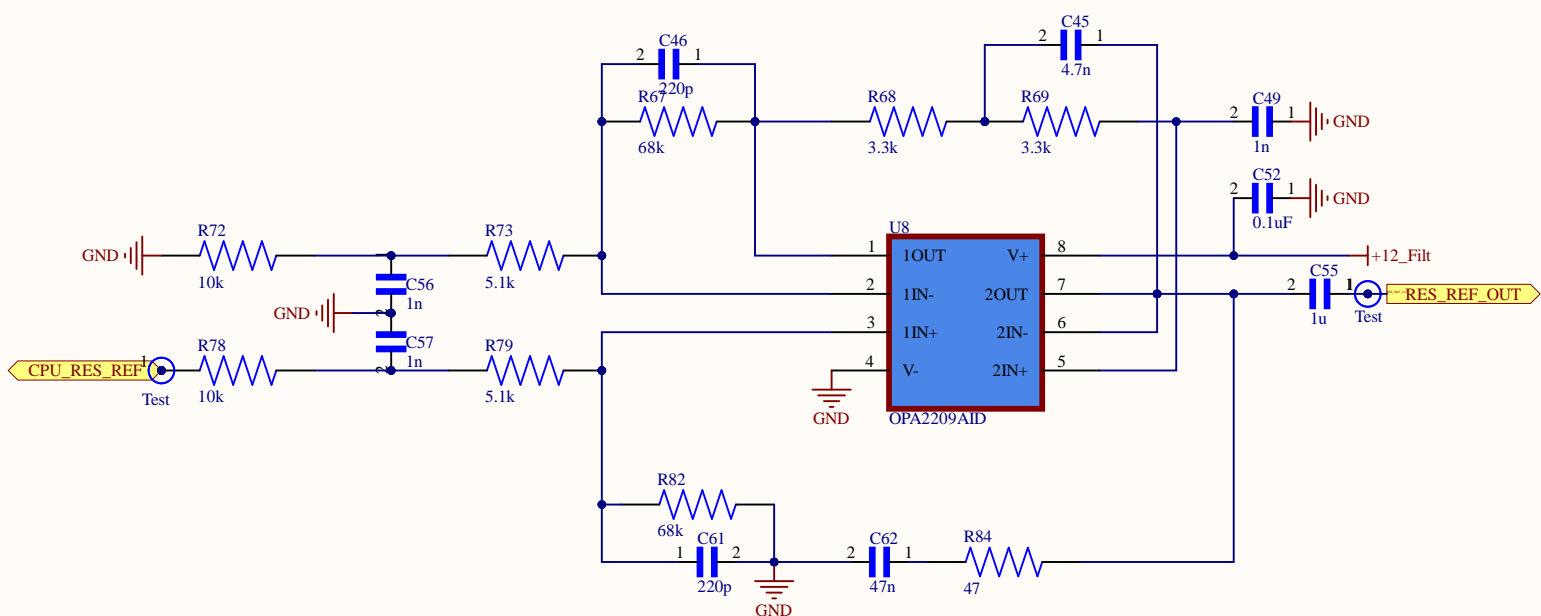
1.65V VOLTAGE FOLLOWER



RESOLVER CONN.



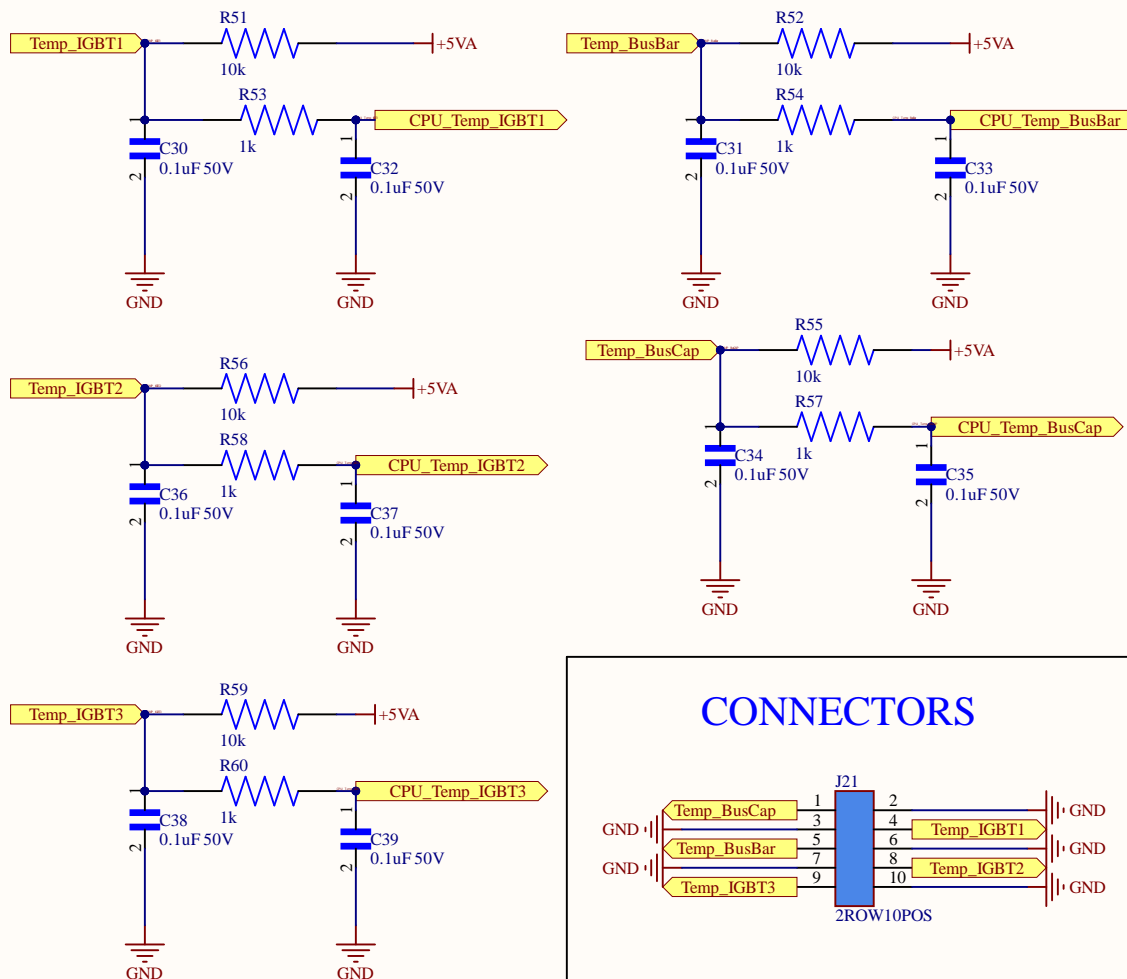
Resolver Connector:
-Cos and Sin will come from Resolver
-RES_REF_OUT will go to Resolver



REF: TI INDUSTRIAL DRIVE DEVELOPMENT KIT: RESOLVER INTERFACE

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INTERFACE CCT

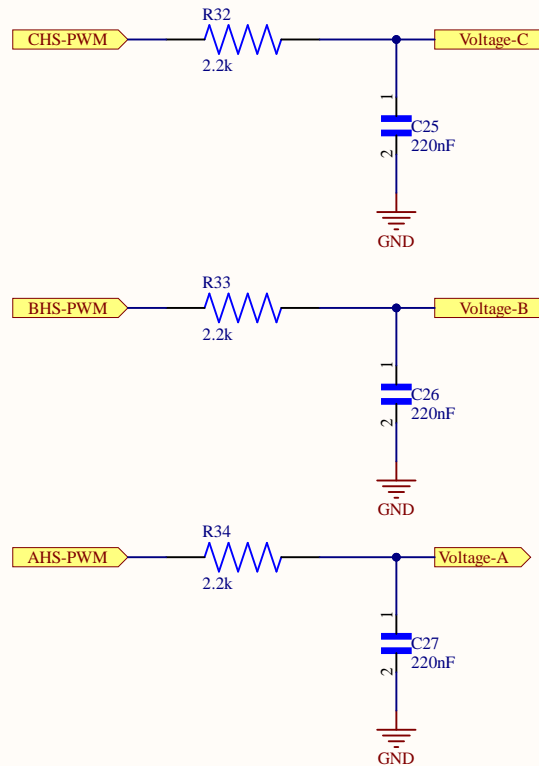


thermocouple

https://urun.n11.com/arduino-urunleri-ve-setleri/k-tipi-termokupl-0-800c-p426199755?utm_source=comp_cimri&utm_medium=cpc&utm_campaign=cimri_genel

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Date:	8/14/2021	Sheet of
File:	C:\Users\...\Temp Sensor Interface.SchDoc	Drawn By:

FILTERS



Title		
Size	Number	Revision
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Date:	8/14/2021	Sheet of
File:	C:\Users\...\Voltage Sensor Interface.SchDoc	Drawn By: