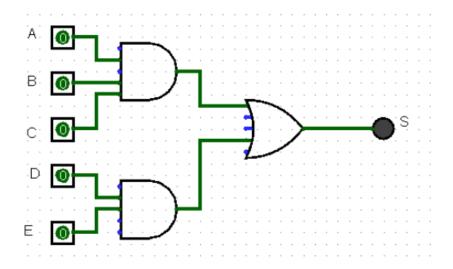
Exercícios Portas Lógicas

1) Dado o circuito abaixo faça a tabela verdade e a expressão booleana



Expressão booleana:

$$S = (A B C) + (D E)$$

Tabela Verdade:

Α	В	С	D	Е	S
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 1 1 1 1 1 1 1 0 0 0 0	0 0 0 1 1 1 1 0 0 0 0 1 1 1 1 0 0 0 0 0	0 0 1 1 0 0 1 1 0 0 1 1 0 0 1 1 0 0 0 1 1 0	0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
0	0	0	0	1	0
0	0	0	1	0	0
0	0	0	1	1	1
0	0	1	0	0	0
0	0	1	0	1	0
0	0	1	1	0	0
0	0	1	1	1	1
0	1	0	0	0	0
0	1	0	0	1	0
0	1	0	1	0	0
0	1	0	1	1	1
0	1	1	0	0	0
0	1	1	0	1	0
0	1	1	1	0	0
0	1	1	1	1	1
1	0	0	0	0	0
1	0	0	0	1	0
1	0	0	1	0	0
1	0	0	1	1	1
1	0	1	0	0	0
1	0	1	0	1	0

1	0	1	1	0	0
1	0	1	1	1	1
1	1	0	0	0	0
1	1	0	0	1	0
1	1	0	1	0	0
1	1	0	1	1	1
1	1	1	0	0	1
1	1	1	0	1	1
1	1	1	1	0	1
1	1	1	1	1	1

2) Dada a expressão boolena apresente o circuito e a tabela verdade:

$$S = (AB) + (CD) + (EF)$$

Circuito:

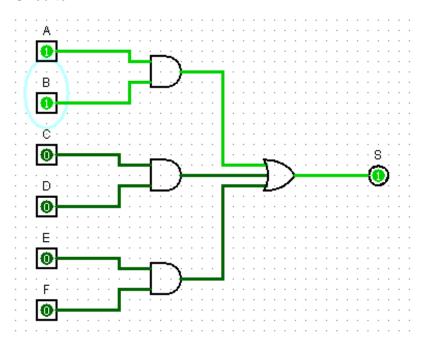


Tabela verdade:

Α	В	С	D	Ε	F	S
0	0	0	0	0	0	0
0	0	0	0	0	1	0
0	0	0	0	1	0	0
0	0	0	0	1	1	1
0	0	0	1	0	0	0
0	0	0	1	0	1	0
0	0	0	1	1	0	0
0	0	0	1	1	1	1
0	0	1	0	0	0	0

0	Λ	1	Λ	0	1	Λ
0	0	1	0	1	n	0
0	0	1	0	1	1	1
0	0	1	1	0	n	1
0	0	1	1	0	1	1
0	0	1	1	1	0	1
0	0	1	1	1	1	1
0	1	_	-	0	-	-
0	1	0	0	0	1	0
0	1	0	0	1		0
0	1	0	0	1	1	1
0	1	0	1	١	0	1
0	1	0	1	0	1	0
0	1	0	1	1		0
0	1	0	1	1	1	1
0	1	1	1	1	1	1
0	1	1	0	0	1	0
0	1	1	0	0	-	0
0	1	1	0	1	0	0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 0	1 1 1 1 1 1 1 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 1 1 1 0 0 0 0 1 1 1 1 0 0 0 0 0 0 1 1 1 1 1 0	0 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 1 1 1 0 0 0 1	1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0	0 0 1 1 1 1 0 0 0 0 1 1 0 0 0 0 1 1 1 1
0	1	1	1	0	0	1
0	1	1	1	0	1	1
0	1	1	1	1	0	1
0	1	1	1	1	1	1
1	0	0	0	0	0	0
1	0	0	0	0	1	0
1	0	0	0	1	0	0
1	0	0	0	1	1	1
1	0	0	1	0	0	0
1	0	0	1	0	1	0
1	0	0	1	1	0	0
1	0	1	0	0	0	0
1	0	1	0	0	1	0
1	0	1	0	1	0	0
1	0	1	0	1	1	1
1	0	1	1	0	0	1
1	0	1	1	0	1	1
1	0	1	1	1	0	1
1	0	1	1	1	1	1
1	1	0	0	0	0	1
1	1	0	0	0	1	1
1	1	0	0	1	0	1
1	1	0	0	1	1	1
1	1	0	1	0	0	1
1	1	0	1	0	1	1
1 1 1 1 1 1 1 1 1 1 1 1	0 0 0 0 0 0 0 1 1 1 1 1 1 1	1 1 1 1 1 1 0 0 0 0 0 0 0	1 1 1 0 0 0 0 1 1 1 1 0	0 1 1 0 0 1 1 0 0 1 1 0 0	1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 0 1 0 0 1 0	0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
1	1	0	1	1	1	1
1	1	1	0	0	0	1

1	1	1	0	0	1	1
1	1	1	0	1	0	1
1	1	1	0	1	1	1
1	1	1	1	0	0	1
1	1	1	1	0	1	1
1	1	1	1	1	0	1
1	1	1	1	1	1	1

3) Dada a tabela verdade a seguir, desenhe o seu circuito lógico e a expressão booleana

Α	В	С	D	S
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

Expressão booleana:

$$\left(\mathsf{A}+\mathsf{B}\right)\left(\mathsf{A}+\mathsf{D}\right)\left(\mathsf{B}+\mathsf{C}+\overline{\mathsf{D}}\right)\left(\mathsf{A}+\overline{\mathsf{C}}\right)\left(\overline{\mathsf{C}}+\mathsf{D}\right)\left(\overline{\mathsf{A}}+\overline{\mathsf{B}}\right)$$

Circuito Lógico:

