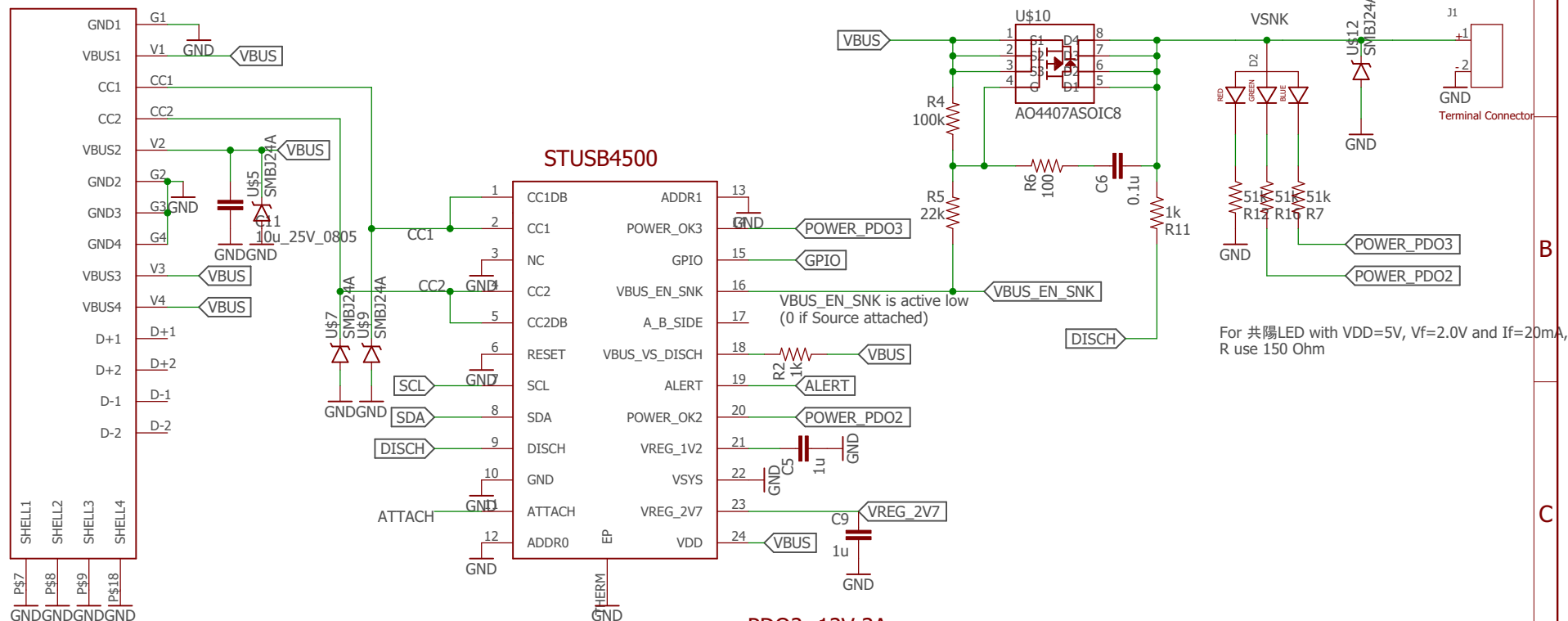


# Muro Box Type-C Module

Type-C 90 Degree 14P Female



For 共陽LED with VDD=5V, Vf=2.0V and If=20mA, R use 150 Ohm

PDO2: 12V 3A  
PDO3: 15V 3A

## Testing Heads

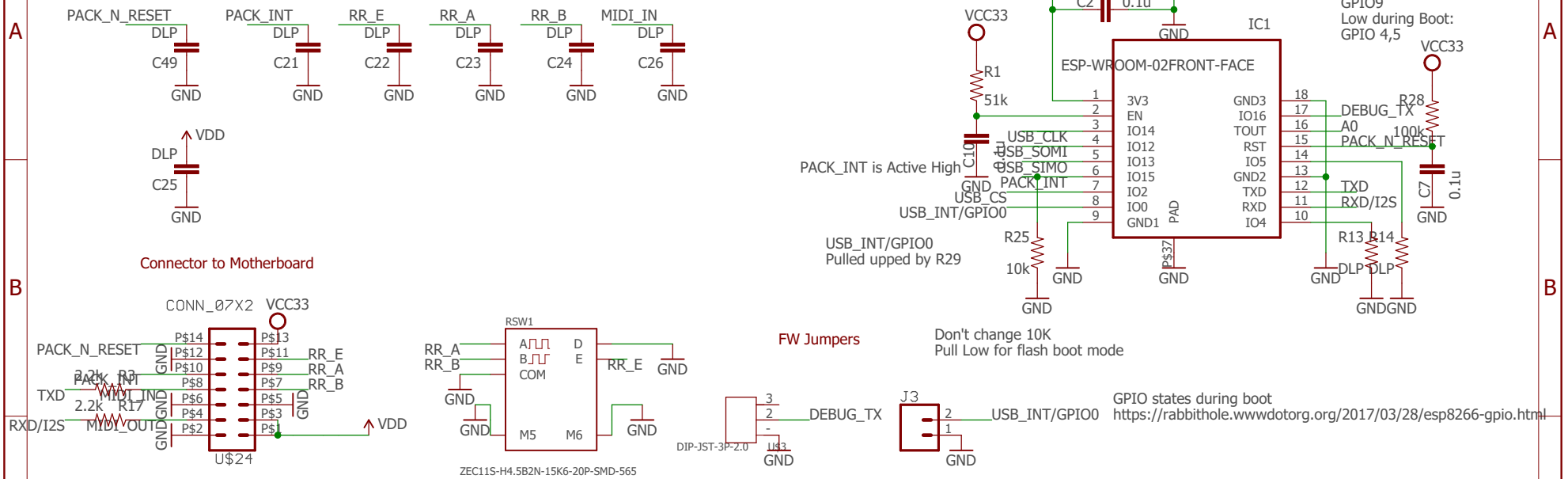


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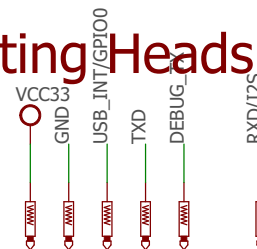
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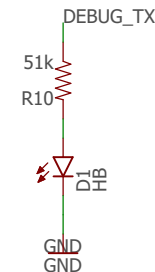
# Muro Box MEGA Pack



## Testing Heads



### HEART BEAT



GPIOs 4 and 5 are the only ones that are always high impedance. All others do have internal pull-ups or are even driven low/high during boot.

GPIOs 3, 12, 13 and 14 pulled HIGH during boot. Their actual state does not influence the boot process.

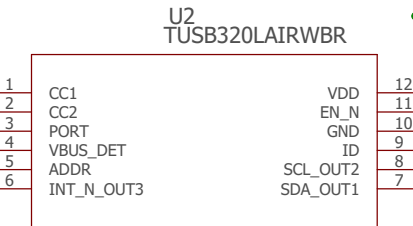
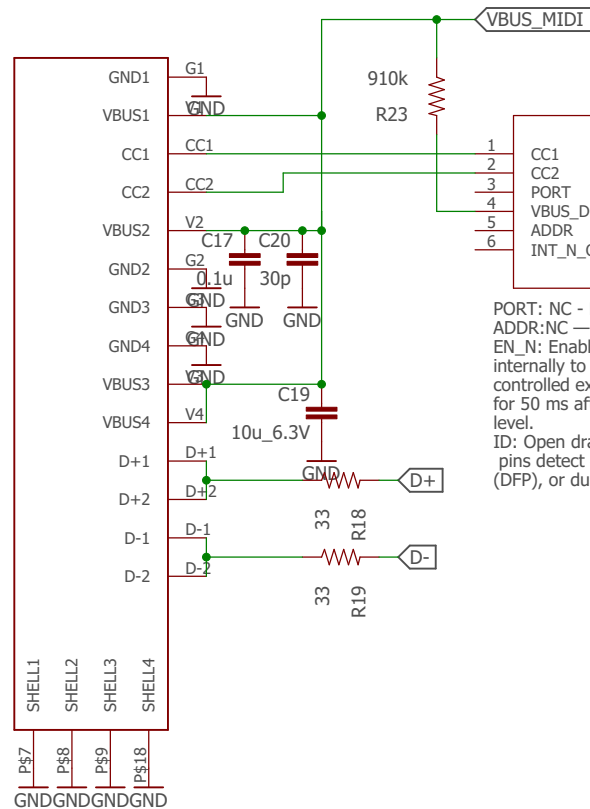
GPIOs 0, 1, 2 and 15 are pulled HIGH during boot and also driven LOW for short periods.  
The device will not boot if 0, 1 or 2 is driven LOW during start-up.

GPIO 16 is driven HIGH during boot, don't short to GND.



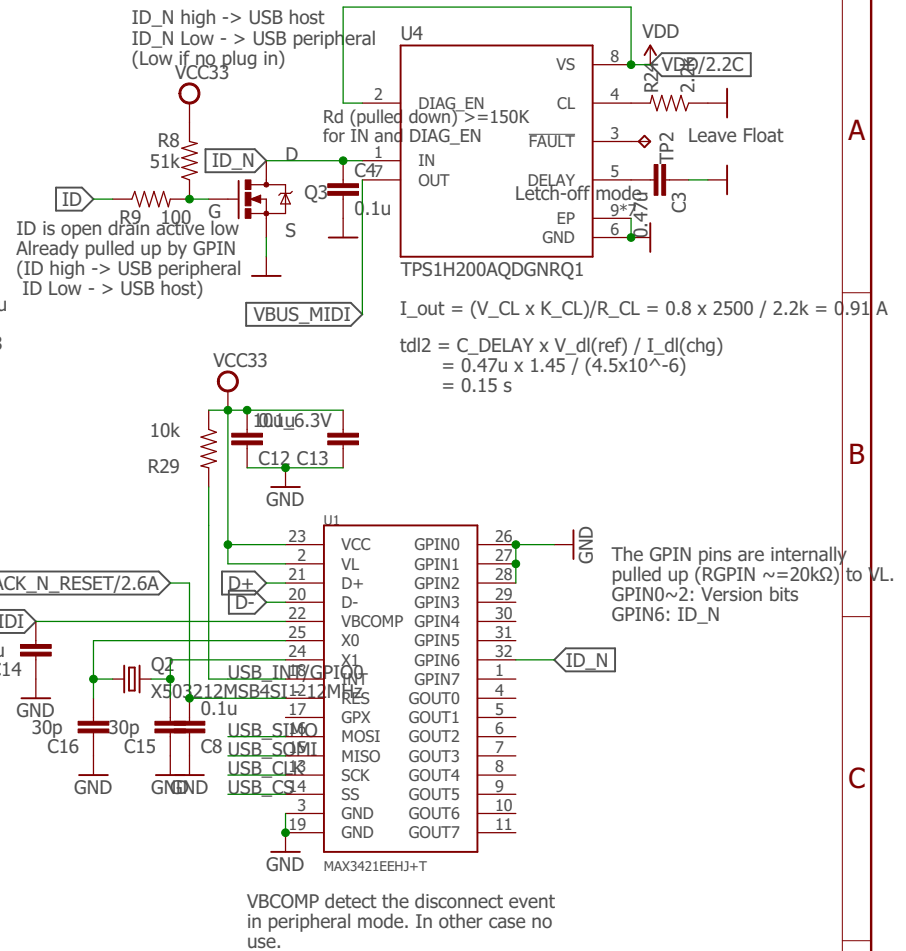
Chen-Hsiang Feng  
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# Muro Box USB SHIELD



PORT: NC - DRP (Leave unconnected if DRP mode is desired)  
 ADDR:NC — GPIO mode (I2C is disabled)  
 EN\_N: Enable signal; active low. Pulled up to VDD internally to disable the TUSB320L device. If controlled externally, must be held low at least for 50 ms after VDD has reached its valid voltage level.  
 ID: Open drain output; asserted low when the CC pins detect device attachment when port is a source (DFP), or dual-role (DRP) acting as source (DFP).

ID\_N high -> USB host  
 ID\_N Low -> USB peripheral  
 (Low if no plug in)



## USB in a NutShell

<https://www.beyondlogic.org/usbnutshell/usb1.shtml>



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