



# Introduction to Trusted Systems

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# Introduction





# Trust and Attestation of me....



Professor of Practice @ Oulu,  
Lead Scientist @ Nokia Standards  
Startup founder (medical stuff)  
Works with dogs, horses and occasionally humans too

25+ years telecoms and critical system experience  
200+ papers & other publications  
100+ patents  
1 book (some more in progress)  
10<sup>th</sup> or 11<sup>th</sup> CRIM ... lost count, can't remember



★ Best Seller



# How the World ends





# How the World ends...





# Examples



Photos of an NSA “upgrade” factory show Cisco router getting implant

Servers, routers get “beacons” implanted at secret locations by NSA’s TAO team.

SEAN GALLAGHER - 5/14/2014, 10:30 PM



(TS//SI//NF) Left: Intercepted packages are opened carefully; Right: A “load station” implants a beacon



# Philosophy





# Trust

**How do we trust systems?**

- a) how to ensure they are WHO they claim to be?
- b) how to ensure they are WHAT they claim to be?

**Trust is a POSITIVE EXPECTATION regarding the BEHAVIOUR of a system**



# A bit of philosophy...

**The trust relationship between things:**

**2-place trust:** A trusts B

**3-place trust:** A trusts B to do X

**Doxastic:** *trust = believes*, A trusts B to do X => A believes B will/can/does do X

**Non-doxastic:** trust = A is optimistic that B will do X

**Modality:** deontic (obligation, permission), epistemic (knowledge)

O(A trusts B to do X) => not K(A trusts B to do X)

**Proof:** for A trusts B to be valid, B must prove their trust to A



# A bit of philosophy...

## **Trustworthy systems:**

- A system is trustworthy if it can provide evidence that it can be trusted.

## **Question: what does “trustworthy AI” mean?**

- What are the criteria for trust?

## **Trust $\propto$ Risk<sup>-1</sup>**

- Who takes the risk?



# Establishing Trust



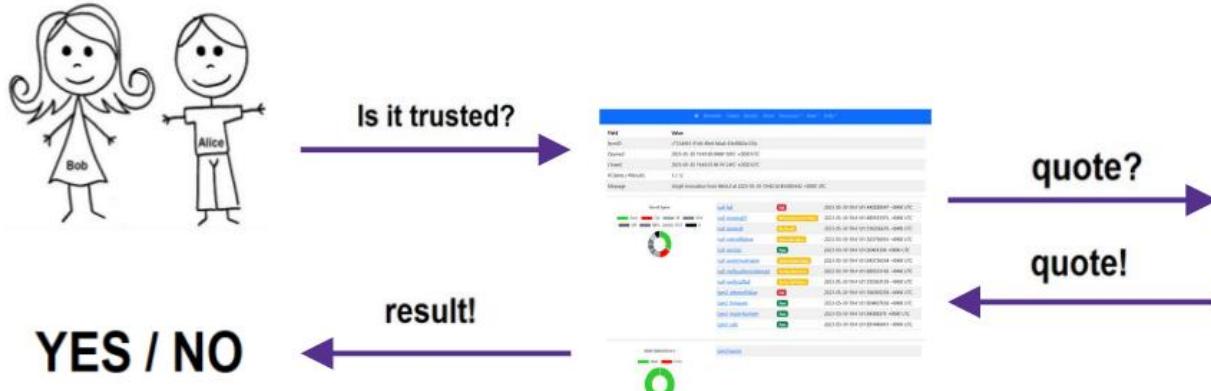


# Attestation



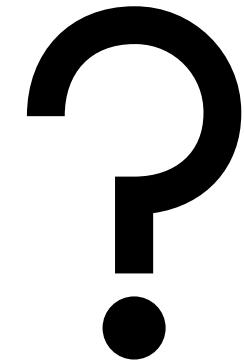
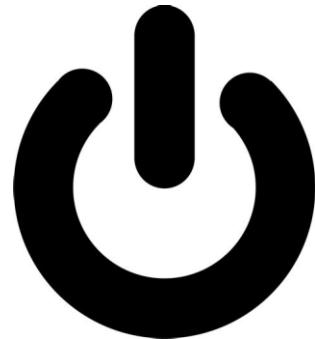


# Attestation



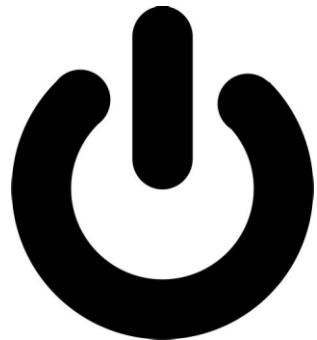


# Power On





# x86 -Power On



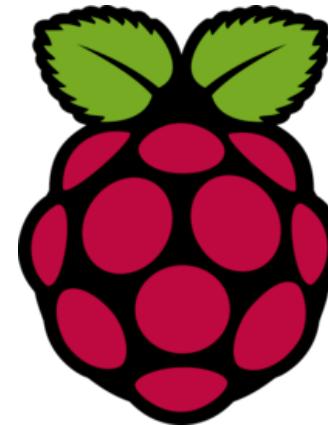
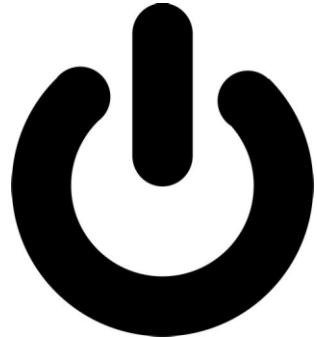
Mac OS X



Secure Boot  
Trusted Boot  
Measured Boot



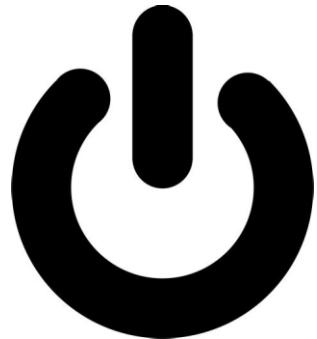
# arm/pi - Power On



Secure Boot?  
Trusted Boot?  
Measured Boot?



# arduino - Power On



Secure Boot?  
Trusted Boot?  
Measured Boot?

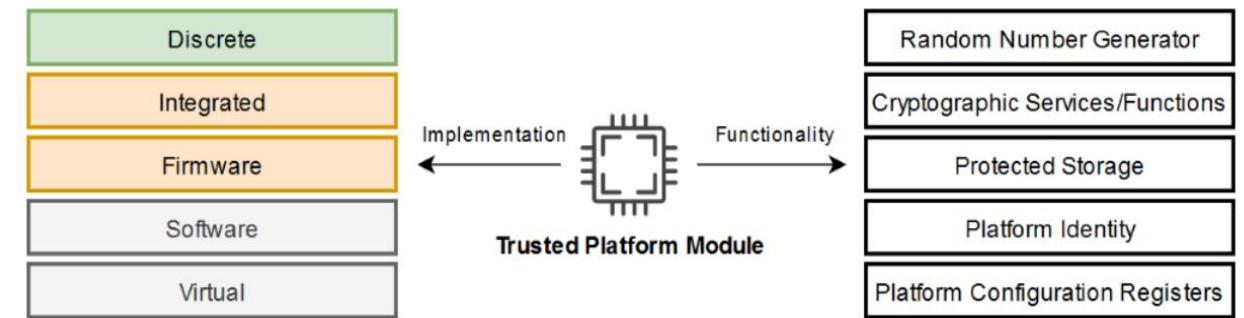
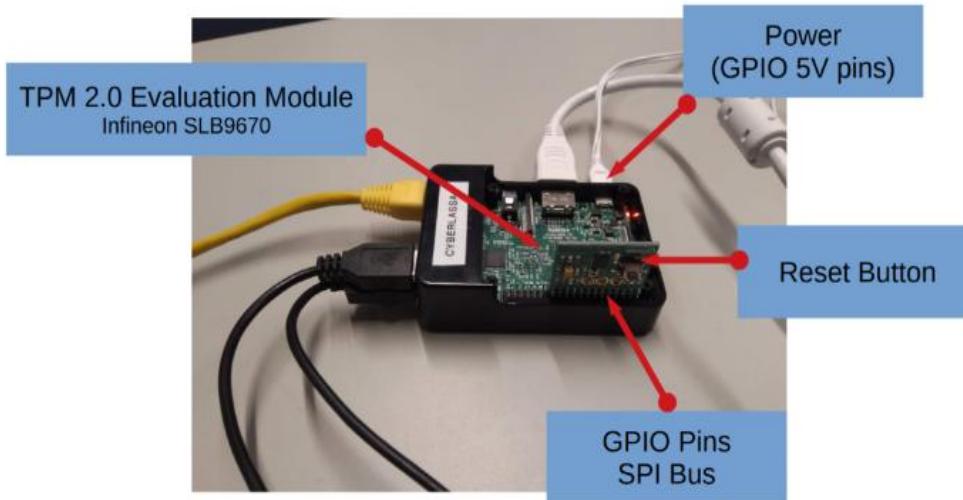


# Trusted Platform Module



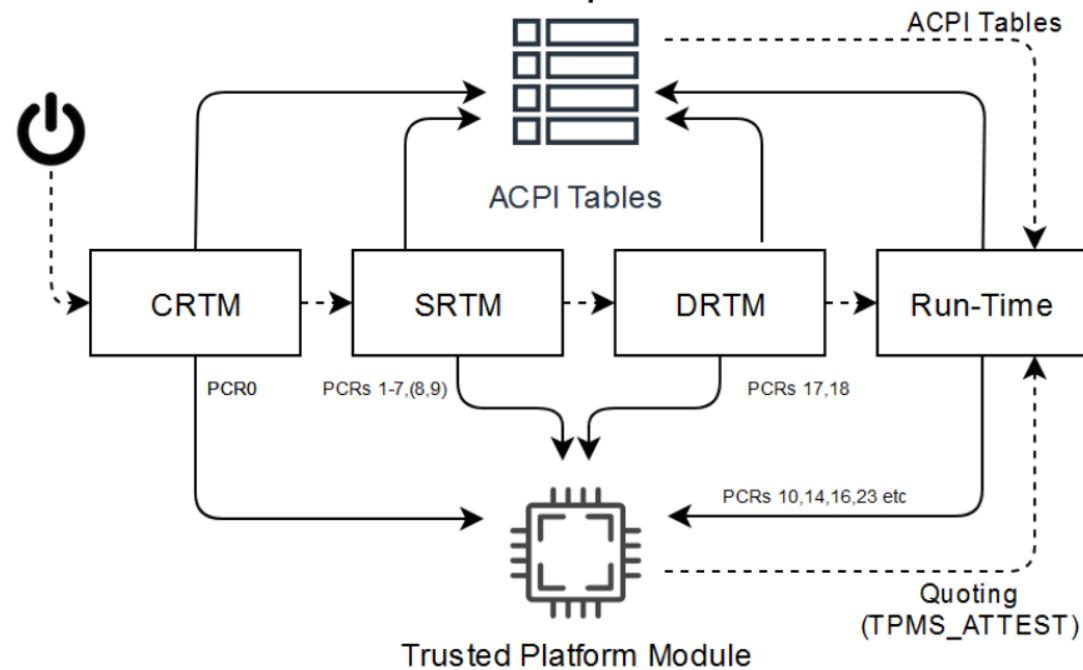


# TPM



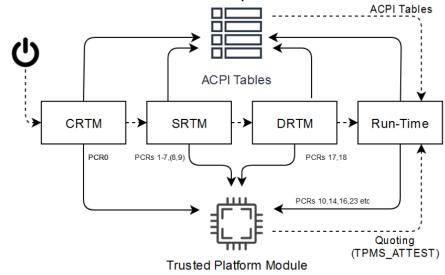


# TPM – PC Boot Sequence



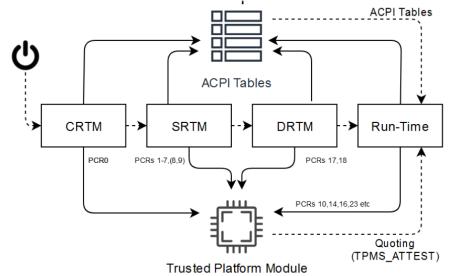


# **TPM - PCRs**





# UEFI Eventlog (location)

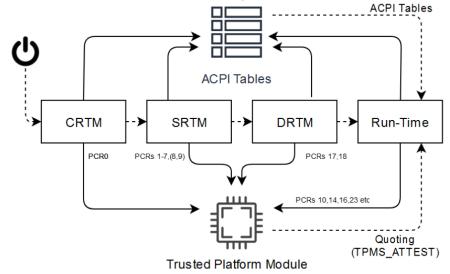


```
tpm0 : bash — Konsole
New Tab  Split View  Copy  Paste  Find...  ⌂

ian@debianwork:/sys/kernel/security/tpm0$ ls -l
total 0
-r----- 1 root tss 0 Sep 28 18:08 binary_bios_measurements
ian@debianwork:/sys/kernel/security/tpm0$ tpm2_eventlog ./binary_bios_measurements
```



# UEFI Eventlog (contents)

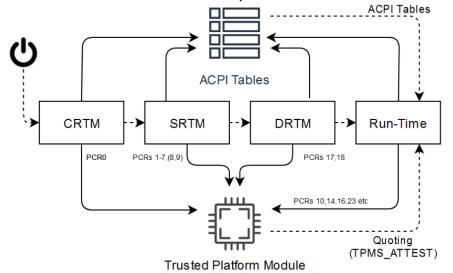


```
tpm0 : vi — Konsole
```

```
version: 1
events:
- EventNum: 0
  PCRIndex: 0
  EventType: EV_NO_ACTION
  Digest: "0000000000000000000000000000000000000000000000000000000000000000"
  EventSize: 45
  SpecID:
    - Signature: Spec ID Event03
    platformClass: 0
    specVersionMinor: 0
    specVersionMajor: 2
    specErrata: 0
    uint0Size: 2
    numberofAlgorithms: 4
    Algorithms:
      - Algorithm[0]:
        algorithmId: sha1
        digestSize: 20
      - Algorithm[1]:
        algorithmId: sha256
        digestSize: 32
      - Algorithm[2]:
        algorithmId: sha384
        digestSize: 48
      - Algorithm[3]:
        algorithmId: sha512
        digestSize: 64
        vendorInfoSize: 0
- EventNum: 1
  PCRIndex: 0
  EventType: EV_S_CRTM_VERSION
  DigestCount: 4
  Digests:
    - AlgorithmId: sha1
      Digest: "489f923c4dca72917b3e3233458550d0dddf29"
    - AlgorithmId: sha256
      Digest: "96a066224f285c67bee93c30f8a309157f0daa35dc5b87e410b78630a09cf07"
    - AlgorithmId: sha384
      Digest: "dd617b457ad880d840d41c961283bab688e94e4b59359ea45686581e90fecce3c624b1226113f824f315eb60ae0a7c"
    - AlgorithmId: sha512
      Digest: "sea71dc6d0b4f57bf39aadd07c208c35f06cd2bac5fd210397f70de11d439c62ec1cdf3183758865fd387fceab0ada2f6c37a4a17851dd1d78fefef6f204ee54"
  EventSize: 24
  Event: "0000"
- EventNum: 2
  PCRIndex: 0
  EventType: EV_EFI_PLATFORM_FIRMWARE_BLOB
  DigestCount: 4
  Digests:
    - AlgorithmId: sha1
      Digest: "77d1b5c305d8640b28cd54f945ebf4f0a157ec"
    - AlgorithmId: sha256
      Digest: "65da839e93b06966e2f91e6a21295c49d0653e11aa83ab175c6681f5575dd6e"
    - AlgorithmId: sha384
      Digest: "e3f6fc8df659b4de0f16f6acf6d12be106279e04b07cd5bf7b2ef476f286ec8f1106175bc6e0ec78a166dccc73c026e2"
    - AlgorithmId: sha512
      Digest: "a880b6828e9ef628b43c0a42ca334d1e2fcc484a9f9c707b845fba9f6ed23628dacb78cb5068057e394d8511ce18a4aaa7e288c7fa464022f476802fc5137e"
  Event:
    BlobBase: 0x2020000
    BlobLength: 0xe0000
- EventNum: 3
  PCRIndex: 0
  EventType: EV_EFI_PLATFORM_FIRMWARE_BLOB
  DigestCount: 4
  Digests:
    - AlgorithmId: sha1
```



# Linux IMA



```
GRUB_DISTRIBUTOR='( . /etc/os-release && echo ${NAME} )'  
GRUB_CMDLINE_LINUX_DEFAULT="quiet ima_audit=1 ima_policy=tcb ima_hash=sha256 ima_template=ima-ng"  
GRUB_CMDLINE_LINUX=""
```

```
ima : bash — Konsole  
ian@debianwork:/sys/kernel/security/ima$ ls -l  
total 0  
lr--r--r-- 1 root root 0 Sep 28 18:08 ascii_runtime_measurements -> ascii_runtime_measurements_sha1  
-r--r---- 1 root root 0 Sep 28 18:08 ascii_runtime_measurements_sha1  
-r--r---- 1 root root 0 Sep 28 18:08 ascii_runtime_measurements_sha256  
-r--r---- 1 root root 0 Sep 28 18:08 ascii_runtime_measurements_sha384  
-r--r---- 1 root root 0 Sep 28 18:08 ascii_runtime_measurements_sha512  
lr--r--r-- 1 root tss 0 Sep 28 18:08 binary_runtime_measurements -> binary_runtime_measurements_sha1  
-r--r---- 1 root root 0 Sep 28 18:08 binary_runtime_measurements_sha1  
-r--r---- 1 root root 0 Sep 28 18:08 binary_runtime_measurements_sha256  
-r--r---- 1 root root 0 Sep 28 18:08 binary_runtime_measurements_sha384  
-r--r---- 1 root root 0 Sep 28 18:08 binary_runtime_measurements_sha512
```

```
- : sudo — Konsole  
ian@debianwork:/sys/kernel/security/ima$ sudo more ascii_runtime_measurements  
10 c2282550a641b1c51242838af2941966a96917d5 ima-ng sha256:7605735f66018f97e360eb861c21a7359f9e555978c042e3cafe45c62f0c04d0 boot_aggregate  
10 e65210d8cd9b108369f02f71d57947b4f4076b1e ima-ng sha256:a775c12b9d71d9548654ff98ecc0e5e3378bdacc52ccb62fa80a5f41e849caf /usr/bin/kmod  
10 b5906a4ab0a94b3642c2b4f0889e0631ccda8a7b ima-ng sha256:daa5744b852336b1ced7ea80da7b555ec92f92ee5b2d36c29b1684f885b9b /usr/lib/x86_64-linux-gnu/ld-linux-x86-64.so.2  
10 47d8f15dc48def0fa760d1b21d7bf5b5129c53 ima-ng sha256:c8e1e997628468ff690cb4e3cb6745df0025be2f3a56a2d4472e2975d37e0 /etc/ld.so.cache  
10 1ab6b2653fe560385d02e24dd58b4d419ec693c ima-ng sha256:b3b4e4c6a6696e8e40803fd018061cfb28ac6a4b46729288c97fb8db5a740d2 /usr/lib/x86_64-linux-gnu/libcrypto.so.3  
10 5033f160e2ca6d0965192cce0a081916f09b4db2 ima-ng sha256:5e42210fbaae00535b622121f8c8b0c16ca80837edce3e3557075103da78 /usr/lib/x86_64-linux-gnu/libc.so.6  
10 d0490024df8ac8fe0a3e7f0e4264b8bbcb39556 ima-ng sha256:85590dd58edf5445e18bc7193e5ebc01ac5841f1ae187e97705a662e90c6421e /usr/lib/x86_64-linux-gnu/libz.so.1.3.1  
10 f2886fcf2974e83ac5e9e2275e1cbe81c8dceec8 ima-ng sha256:27f07c9a49c2c956bcfb64cd4712976586a66facbf15fc7f09bc37413b5f2b21 /usr/lib/x86_64-linux-gnu/libzstd.so.1.5.7  
10 342647041708180206c3c5eacb69e967af96b40 ima-ng sha256:0ffc97a5b69113438c1b7e162ef6871e197e24973f359bfbeef448b089bd67e5 /usr/lib/modprobe.d/aliases.conf  
10 d3cfb0093b9a5cea3fa0da9d0a1672e38b9828c ima-ng sha256:e99b6166cf309aa1ccb926d9fb9f3af567148c7316c8d5d7a057cf3e8513ea /usr/lib/modprobe.d/fb-dev-blacklist.conf  
10 8ff5103795a920548c6eec3b6e29396e1c8d3cd ima-ng sha256:f17b12aa7a202bb4f0ed09cbba45561417986fded2cf8f861364f84c8f3d42fb8 /etc/modprobe.d/intel-microcode-blacklist.conf  
10 33bc43b1558e588393a38ed3c2720b7c04355569 ima-ng sha256:d1327ebfd9ec030a9853804c017f76179c165b38fb5db0b1b7e3163518e3bd7b /usr/lib/modules/6.12.48+deb13-amd64/modules.softdep  
10 e3fac9a947b63663eb66b5804ad7b54d296a978e ima-ng sha256:a1ffffe1059d8150b5d402b3f284f507025a8d4b5881801cb17b3fda8b8ab9304 /usr/lib/modules/6.12.48+deb13-amd64/modules.weakdep  
10 a49880860b99d9644a3219bc9c8494d3ca80099d4 ima-ng sha256:e5a3958cbd3684b63f3cada6604469c56f727b106d5524daf5afea6935a48ce /usr/lib/modprobe.d/stemd.conf  
10 5f3bd2d2a0daff8268a3d369c600aaf2604369aeb6 ima-ng sha256:60fc237c548de6d236b8579080e2311e7e78156c4deeff944695833473b38cb /usr/lib/modules/6.12.48+deb13-amd64/modules.dep.bin  
10 bdd43e6b4dd0241d1db7c8a8df738e76831cba0a ima-ng sha256:03bc72742f1964a0dbf394cbbdcea9c0f5b0e1c7aaac4ca96e742e52a570307ff /usr/lib/modules/6.12.48+deb13-amd64/modules.altas.bin  
10 3c91c55a61b2c084a46fe4578f08ade9ee0500a6 ima-ng sha256:277572cc8e857cb72574d8800a5ce236e69e57e44b98a4b9cd3e6f041a6486b7 /usr/lib/modules/6.12.48+deb13-amd64/modules.symbols.bin  
10 930f30158e4fa5d4eb5357325998718c08c7fa3 ima-ng sha256:f45762f4a96a3e41da6f4410c655c67ce67cbf1ef1cc93117b9e851ba112cb05 /usr/lib/modules/6.12.48+deb13-amd64/modules.builtin.alias.bin  
10 108fb2256c6159adfec34fc26b0f70f62b189e9a6 ima-ng sha256:91f2413151b7b0451ce0bcedc7e5919931acf792d9a26645c0deb3bc230d9fe /conf/arch.conf  
10 108fb2256c6159adfec34fc26b0f70f62b189e9a6 ima-ng sha256:fc284437470427fece0993063cb46846a105bdf /conf/initramfs.conf  
10 533b3fb1e62f41c672b4e8c8db3668cc82de783 ima-ng sha256:d087b17b0875c351e46e2412525a6b71ed28131d7e272929e20f8005363f7789 /conf/conf.d/resume  
10 07f573704dbdea05bf9d7b5306232f6873cd0f0 ima-ng sha256:b328b49756e0ec7c692ddd28526831def5e9e0d7a0d0963087b450c2db4743 /scripts/functions  
10 cb34d52a83e5ccc1755ff868e81e7ad98c9fdbd8 ima-ng sha256:d50aef05c0c796756833fe9b39441f55e0b87939f7451d381deb8a93d53cb7a /scripts/init-top/ORDE
```



# TPM - Quotes

Field	Description
pcrDigest	hash of the selected PCR entries
pcrSelect	list of the selected PCR fields
magic, type	Fixed values denoting the type of the structure (TCG defined)
firmwareVersion	Firmware version of the TPM 2.0 device
clockInfo::clock	Value of the TPM's internal clock
clockInfo::resetCount	Counters showing the number of power cycles
clockInfo::restartCount	Counters showing the number of sleep (S3) cycles
clockInfo::safe	Flag showing whether the TPM was powered off explicitly
extraData	Any user supplied data, eg: a nonce for replay attack prevention
qualifiedSigner	a hash of the public parts of the signing key's hierarchy

**Table 1.** TPM 2.0 Quote (TPMS\_ATTEST) Fields

Name	Function	Description
Linux IMA ASCII Log	<a href="#">ima/asciilog</a>	Retrieves the ASCII Log generated by Linux Integrity Measurement Architecture
Linux IMA with PCR_0	<a href="#">tpm2-quote</a>	The Linux IMA measurements as stored in SHA256 PCRs
RATSD Chares call	<a href="#">ratsd/charcs</a>	Test call to the RATSD daemon
System Information	<a href="#">sys/info</a>	Collects basic system information
TPM 2.0 PCRs	<a href="#">tpm2/pcrs</a>	Returns the PCR values for all banks
UEFI Boot Configuration	<a href="#">uefi/bootconfig</a>	Retrieves the UEFI Boot Configuration
UEFI EFI Variables	<a href="#">uefi/efivars</a>	Retrieves the UEFI EFI Variables on Linux (and maybe Windows) systems
UEFI Eventlog	<a href="#">uefi/eventlog</a>	Retrieves the UEFI Eventlog on Linux systems
x86 Grub Full	<a href="#">tpm2-quote</a>	The Grub measurements as stored in SHA256 PCRs
x86 Intel TXT	<a href="#">tpm2-quote</a>	The Intel TXT measurements as stored in SHA256 PCRs
x86 UEFI Bootloader Only	<a href="#">tpm2-quote</a>	The bootloader only
x86 UEFI Bootloader and CRTM	<a href="#">tpm2-quote</a>	The bootloader and CRTM
x86 UEFI CRTM Firmware	<a href="#">tpm2-quote</a>	The initial CRTM Firmware
x86 UEFI CRTM only	<a href="#">tpm2-quote</a>	The initial CRTM
x86 UEFI SRTM	<a href="#">tpm2-quote</a>	The initial SRTM
x86 UEFI SRTM and Bootloader	<a href="#">tpm2-quote</a>	The SRTM and boot loader

PCR Digest	Sbp3eQgneJzDRRIuvQU8TgMYql0mOeY/Gi0fC1xMJwk=				
PCR Selection	sha256 : 0 1 2 3 4				
Firmware	1.970689910360832e+15				
Magic & Type	4.283712327e+09, 32792				
Extra Data					
Clock	6.883417337e+09				
Reset Count	24				
Restart Count	0				
Safe	Safe (1)				
Qualified Signer	sha256 n7MMXkPSXT6TWvKE5OjbUgnGTg3EwEOZnrXFRbuL8vk=				

Field	Value	Field
ItemID, BodyType	b8ff57d0-53f3-4eeb-a6e6-c04f26aa5c16 <b>tpm2/quote</b>	Hash
Element	Coreservices <b>tarzan</b> A10HTTPRESTv2 http://127.0.0.1:8530	Signature
Intent	x86-UEFI CRMT Firmware <b>tpm2/quote</b>	
Session	417a2a20-5e24-420c-b6bd-f788fb57dfc4	
Additional Parameters	map[]	
Call Parameters	map[bankssha256 pcrSelection:0,1,2,3 tpm2/akhandle:0x810100AA tpm2/device:/dev/tpmrm0 tpm2/nonce:Fh6NbBpPCV1unFKjaATNL3z-ePy3GbNzg]	
Requested, Received	2025-10-17 08:47:42.0320715 2025-10-17 08:47:42.5135695	

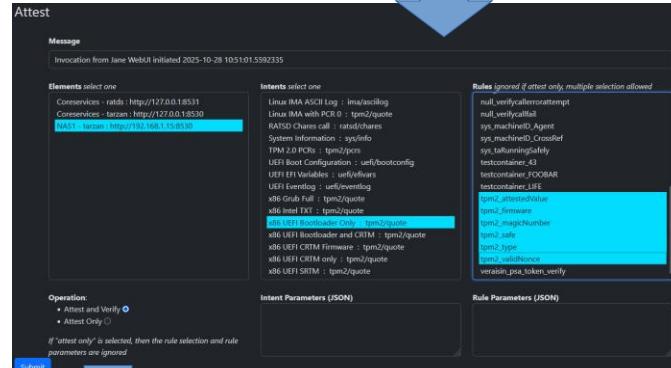
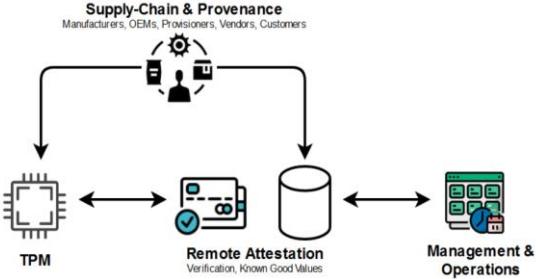
Body:	
<b>tpm2/quote</b>	

Quote									
Field	Reported value								
PCR Digest	OH16l6KfpSUNwAggmUTomPaac9EKI8g500HlpNf1co=								
PCR Selection	({}) [{} 11 [15 0 0]])								
Firmware	5000000044102								
Magic	ff544347 (default value is ff544347)								
Type	8018 (default value is 8018)								
ClockInfo	<table border="1"> <tr> <td>Clock</td><td>18561189102</td></tr> <tr> <td>Reset</td><td>286</td></tr> <tr> <td>Restart</td><td>0</td></tr> <tr> <td>Safe</td><td><b>Safe (false)</b></td></tr> </table>	Clock	18561189102	Reset	286	Restart	0	Safe	<b>Safe (false)</b>
Clock	18561189102								
Reset	286								
Restart	0								
Safe	<b>Safe (false)</b>								
Qualified Signer	4141736e543176714b514f75665168594462376aa461c54386a38564235577a6f55345aa4b74456b76364a6842673d3d								

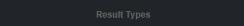


# Attestation



Session	
Field	Value
ItemID	2169e978-848c-4930-9675-5e4ab52c0f1f
Timing	2025-10-17 08:44:22.5380227 → <b>3.893849068s</b> → 2025-10-17 08:44:26.4318718
#Claims / #Results	9 / 28
Message	Provisioning Session

Result Types
 

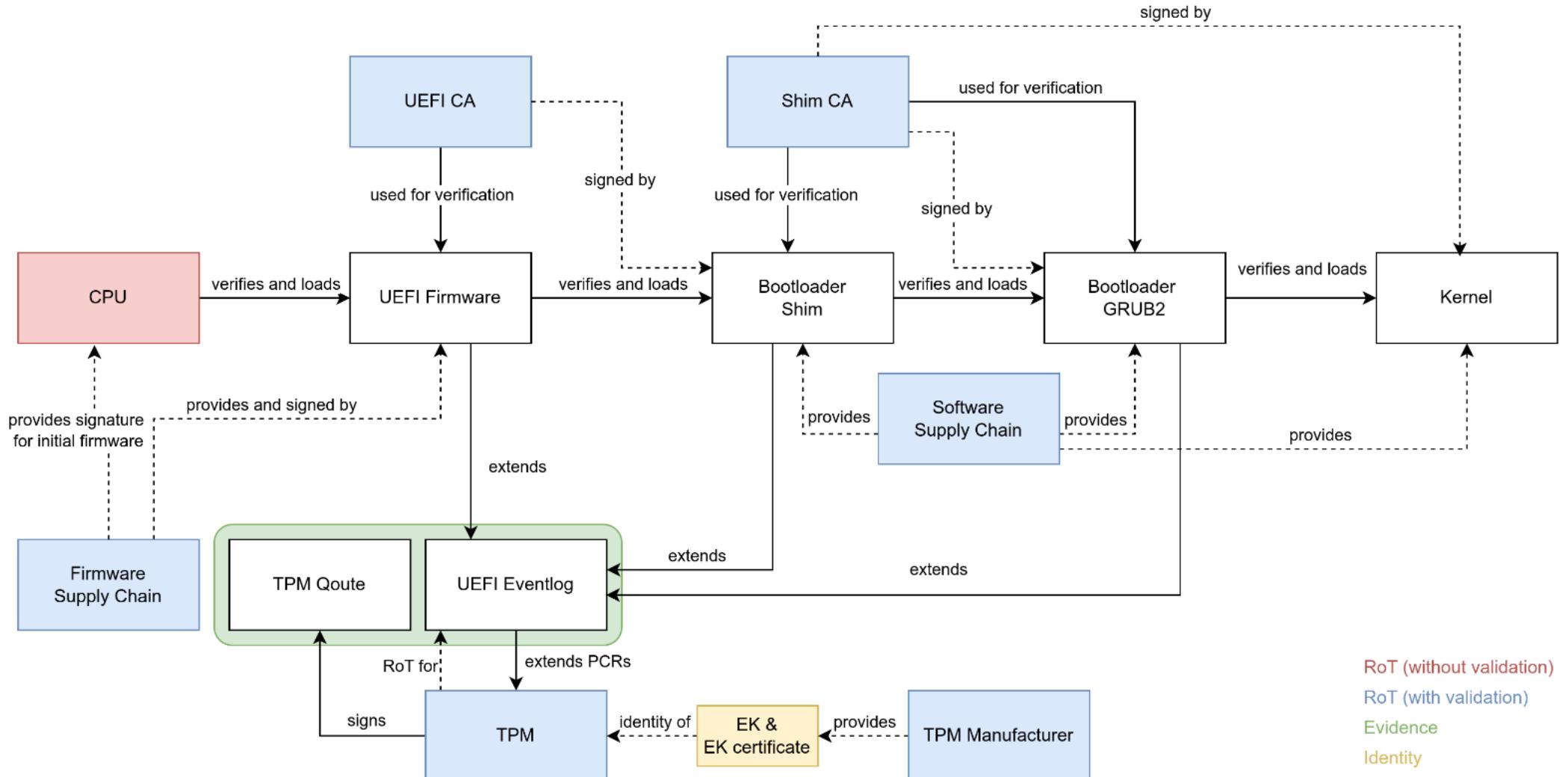
Test Case	Status	Timestamp
sys_taRunningSafely	Pass	2025-10-17 08:44:23
sys_machineID_Agent	Pass	2025-10-17 08:44:23
sys_machineID_CrossRef	Pass	2025-10-17 08:44:23
tpm2_attestedValue	Pass	2025-10-17 08:44:23
tpm2_firmware	Pass	2025-10-17 08:44:23
tpm2_magicNumber	Pass	2025-10-17 08:44:23
tpm2_safe	Fail	2025-10-17 08:44:23
tpm2_validNonce	Pass	2025-10-17 08:44:23
tpm2_attestedValue	Pass	2025-10-17 08:44:24
tpm2_firmware	Pass	2025-10-17 08:44:24
tpm2_magicNumber	Pass	2025-10-17 08:44:24
tpm2_safe	Fail	2025-10-17 08:44:24
tpm2_validNonce	Pass	2025-10-17 08:44:24
tpm2_attestedValue	Pass	2025-10-17 08:44:24

Elements		Tags	Endpoints
Name	Description		
<a href="#">Coreservices</a>	Garage core services machineElement name: (Coreservices) Entry added at 2025-10-17 08:47:41.374436+00:00 UTC	linux x86 tpm2 uefi vbox	ratds <a href="#">RATSD</a> http://127.0.0.1:8531 tarzan <a href="#">A10HTTPRESTv2</a> http://127.0.0.1:8530
<a href="#">NAS1</a>	TrueNAS Server for Garage	freebsd x86	tarzan <a href="#">A10HTTPRESTv2</a> http://192.168.1.15:8530

Result	
Element Information	
Field	Value
Rule Name	tpm2_safe
VerifiedAt	2025-10-17 08:44:23.6237973
Result	<span>Fail</span>
Expected Value	
Element	
Intent	
Claim	<a href="#">4db8dd4fb-ef5b-4d54-952e-c6294976cdc1</a>
Session	<a href="#">2169e978-848c-4930-9675-5e4ab52c0f1f</a>
Message	Uncommanded device/TPM shutdown. TPM safe value is false

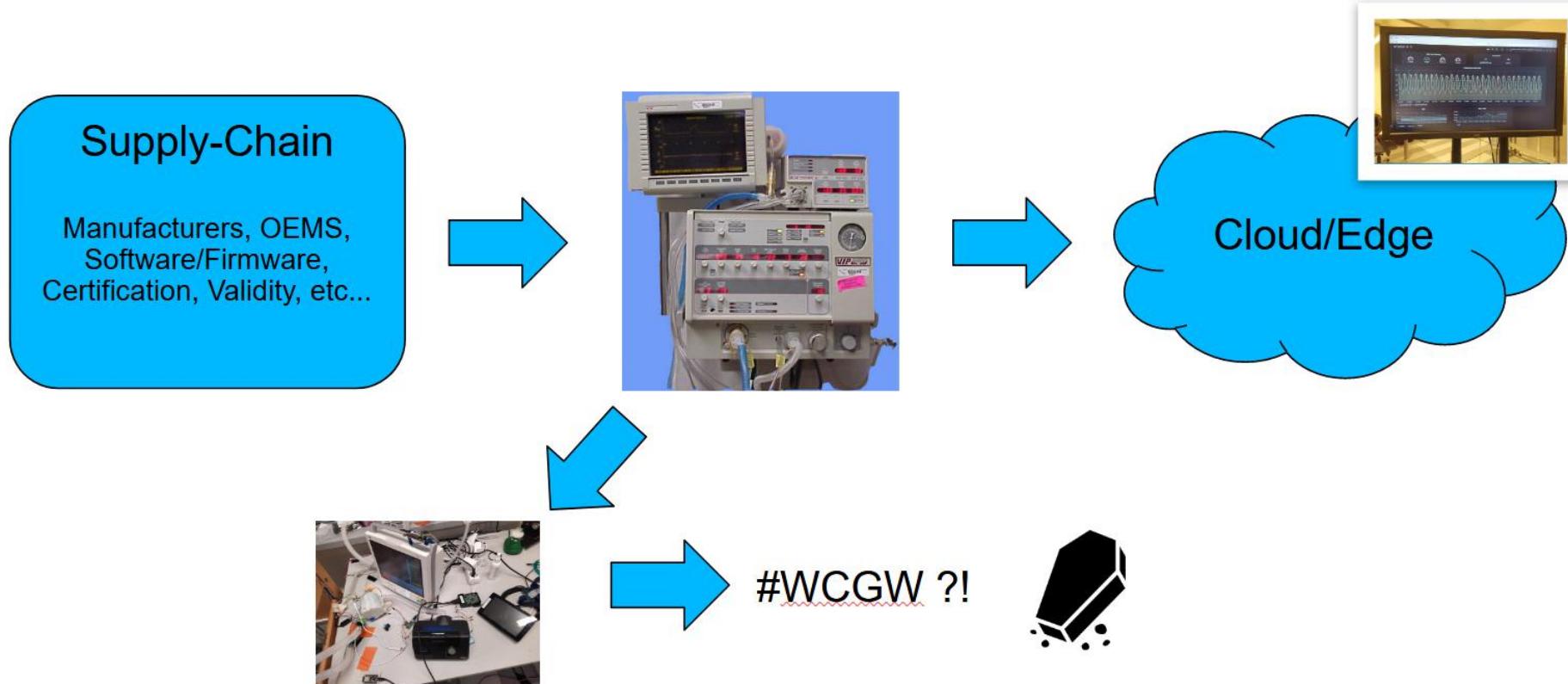


# Chains of Trust





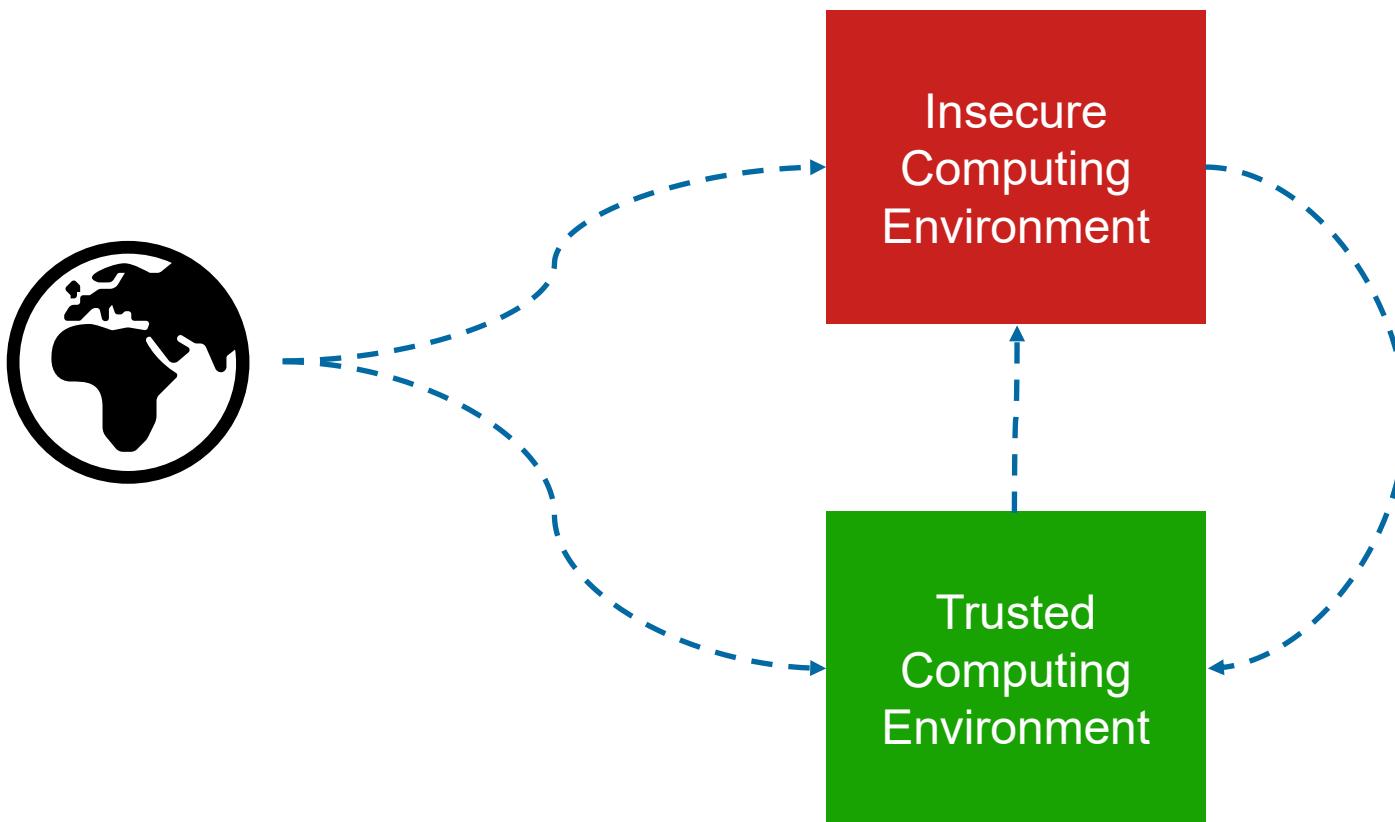
# Supply-Chain



Images: Wikipedia #1 Brian Hall - Own work,  
#2 <https://wellcomeimages.org/indexplus/image/L0001305.html> Wellcome Collection  
gallery (2018-03-29): CC-BY4.0  
#3 © 2020 Ian Oliver



# Enclaves & Confidential Computing

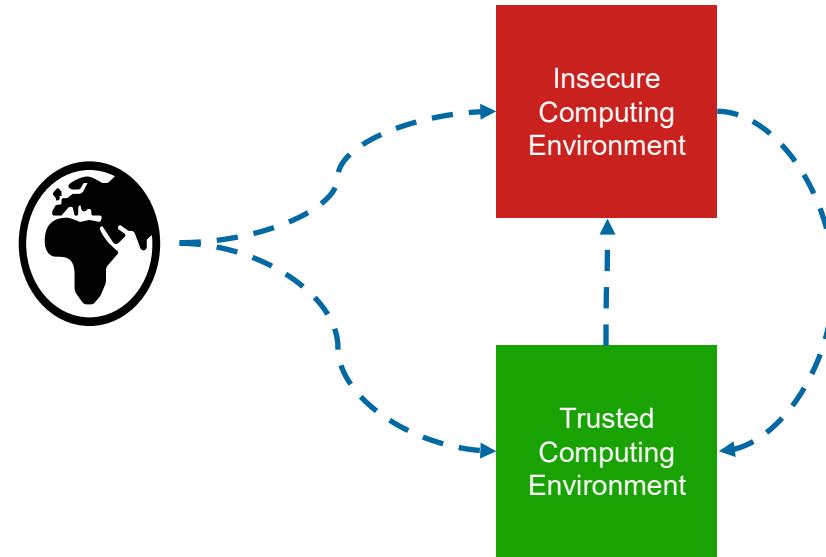


- Identity of the components
- Identity of the workload
- Integrity of the components
- Integrity of the workload
- Ensuring that the workload is untampered, its data is untampered and is running in an identified and attested workspace.



# Enclaves & Confidential Computing

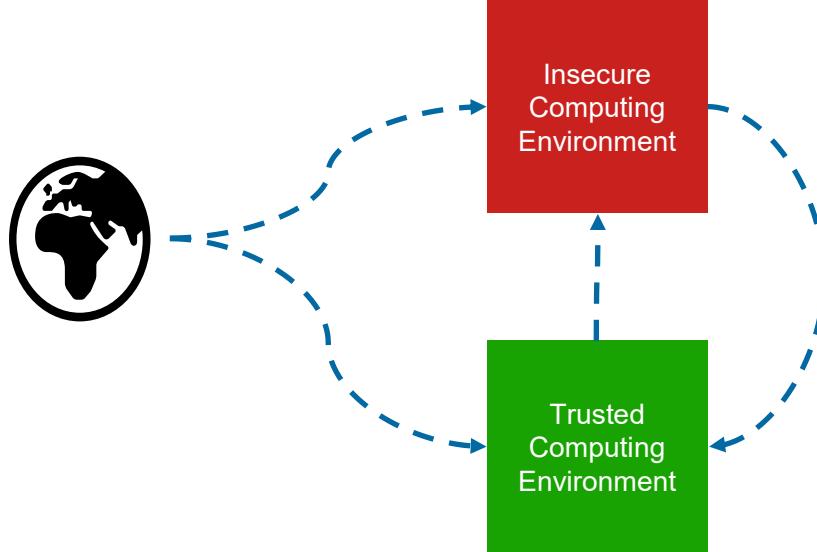
- Intel TXT, SGX, TDX
- AMD PSP, SEV
- Arm TrustZone, CCA
- IBM Secure Execution (z15)
- RISC-V Keystone
  
- JavaCard,
- USIM,
- NFC Secure Execution
- HSM





# Enclaves & Confidential Computing

- Intel TXT, SGX, TDX
- AMD PSP, SEV
- Arm TrustZone, CCA
- IBM Secure Execution (z15)
- RISC-V Keystone
  
- JavaCard,
- USIM,
- NFC Secure Execution
- HSM
  
- Requires customized software (eg: SGX)
- Containers/Virtual Machines were too big
- Confidential Containers Project (  
<https://confidentialcontainers.org/docs/overview/> )



[https://confidentialcomputing.io/wp-content/uploads/sites/10/2023/03/CCC-A-Technical-Analysis-of-Confidential-Computing-v1.3\\_unlocked.pdf](https://confidentialcomputing.io/wp-content/uploads/sites/10/2023/03/CCC-A-Technical-Analysis-of-Confidential-Computing-v1.3_unlocked.pdf)



# Intel TDX

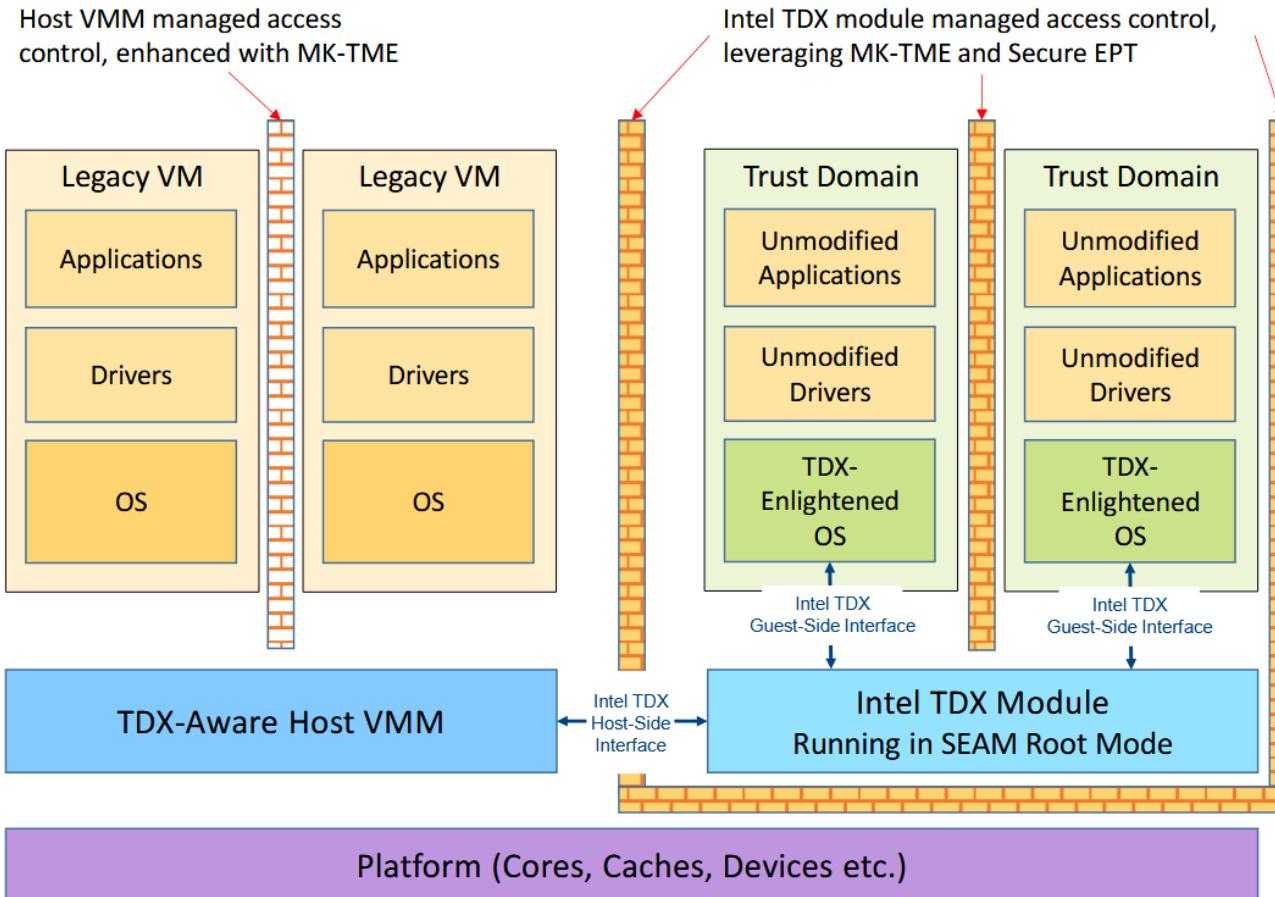
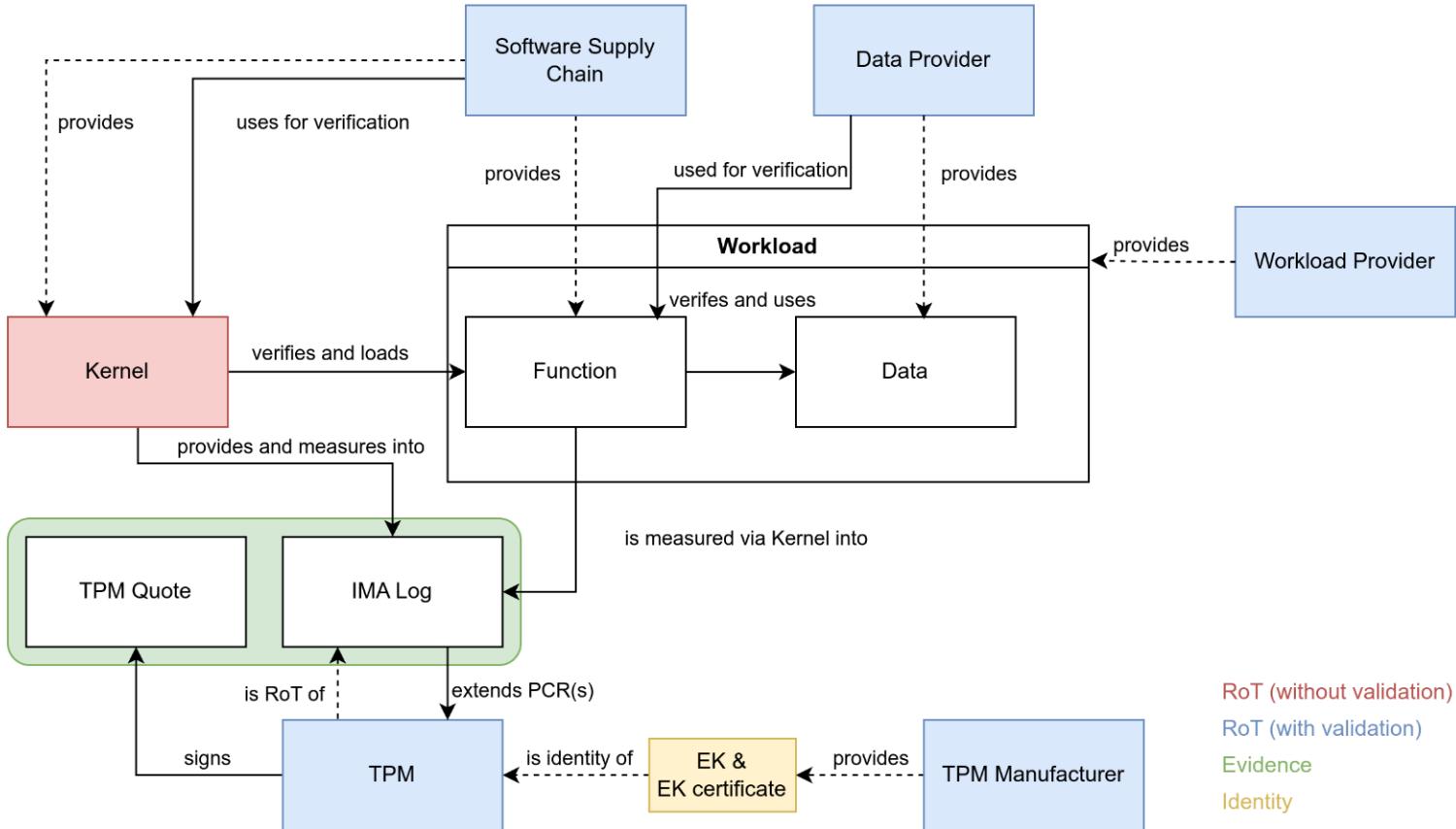


Figure 2.1: Intel® Trust Domain Extension Components Overview

- CPU Overhead
- Early SGX was very limited
  - Cache coherence
  - Shared memory structures in CPU etc
- Untrusted-world communication
  - Requires additional silicon
  - Memory encryption
  - Cryptographic functions
  - IPC
  - I/O (off-chip)
- Attestation Mechanisms
- Malicious code is still code....
- Code signing/attestation before execution
- Privacy Concerns?
- Do you trust the CPU manufacturer's code?



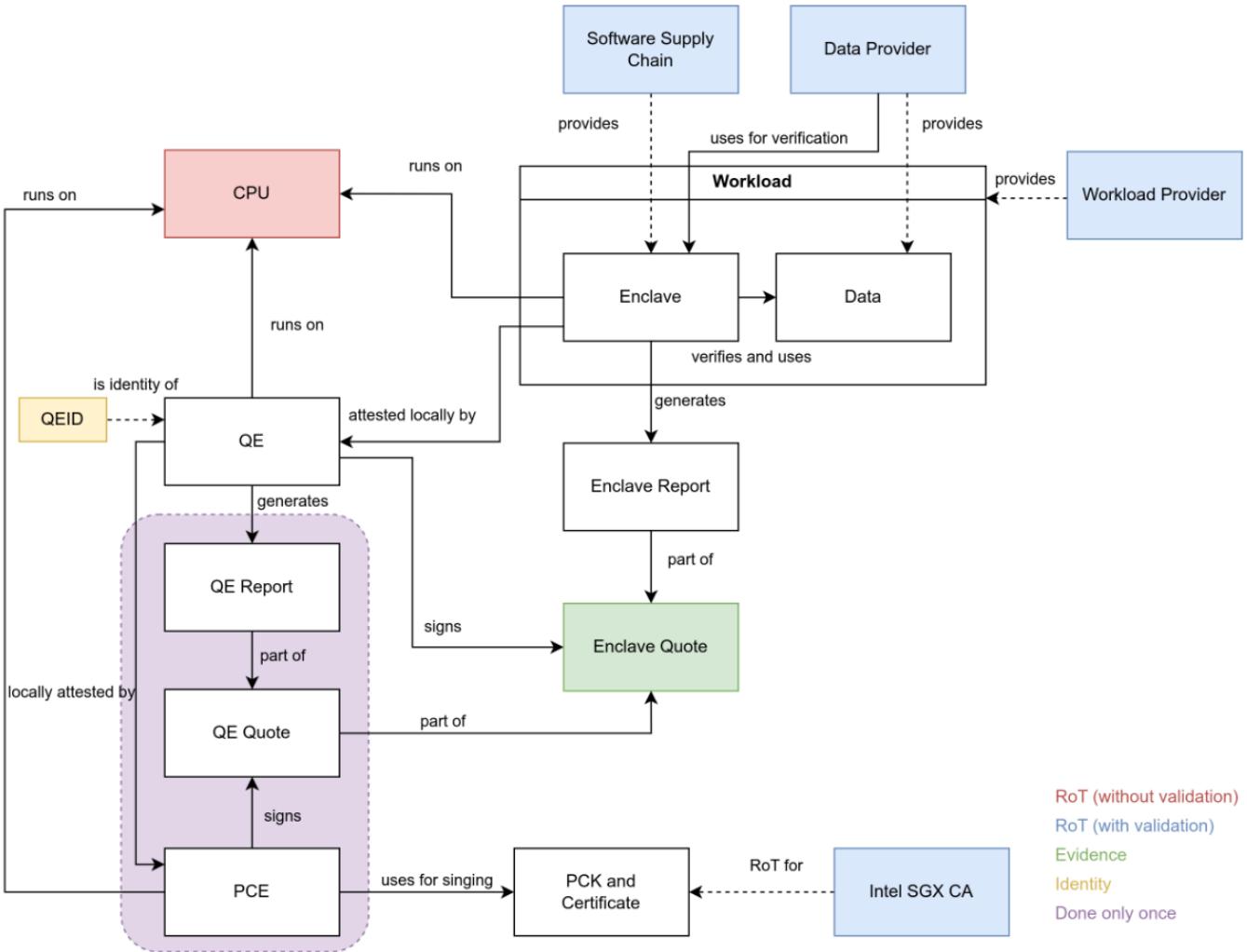
# Chains of Trust (2):



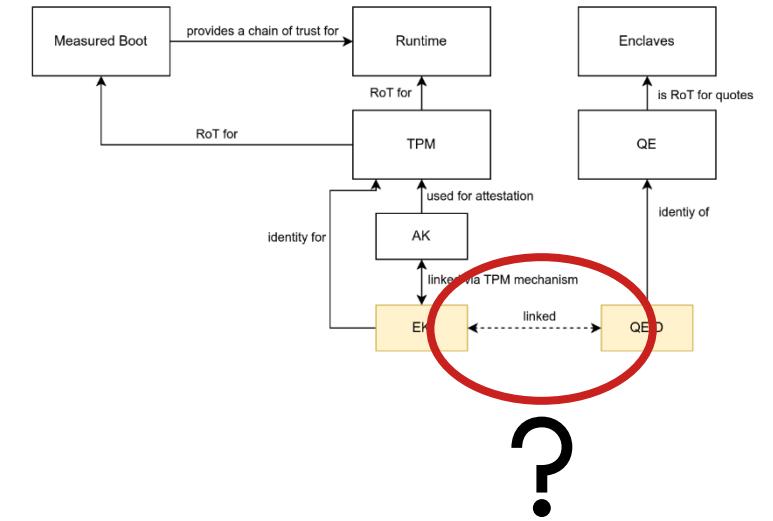
- We can use the TPM to establish the veracity of the system
- IMA to establish veracity at runtime
- No confidentiality



# Chains of Trust (3):

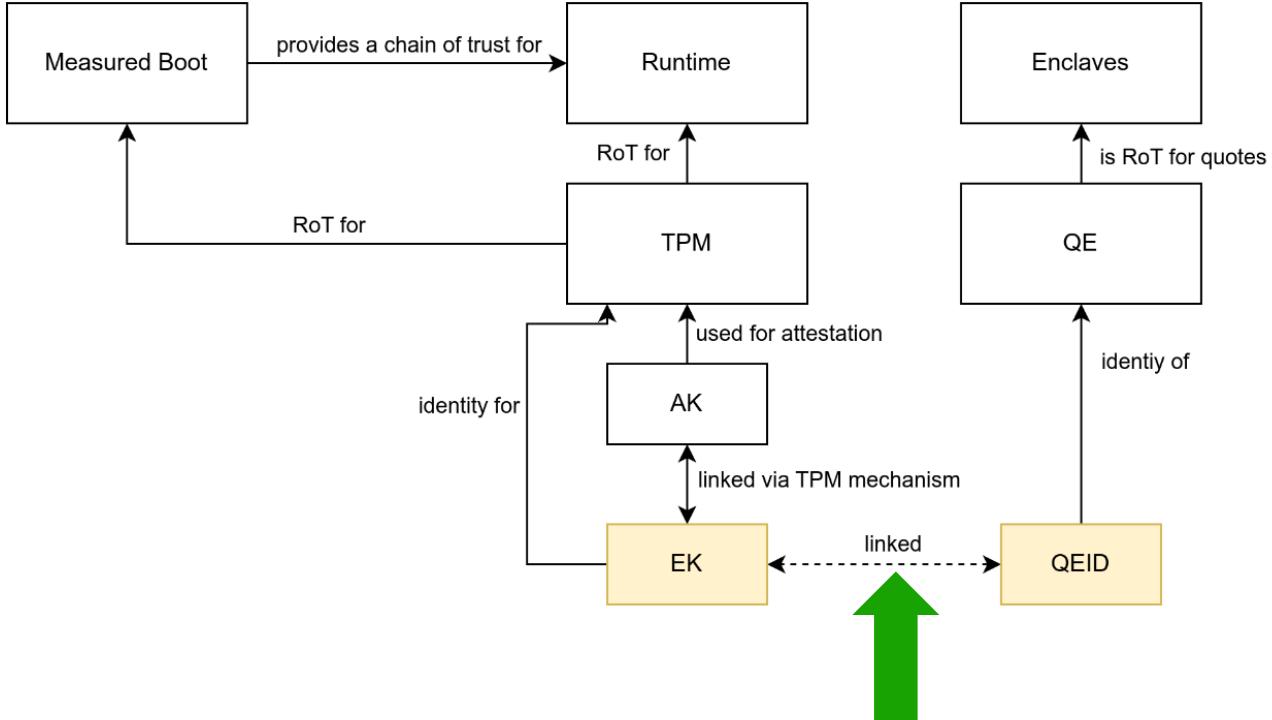


- CPU Enclaves vs TPM
- CPU identity not linked to TPM identity
- CPU integrity not linked to TPM measurements





# Chains of Trust (4)



CPU identity & integrity part of boot/run-time attestation processes....hard.

...and still unsolved for a general case...MSc/PhDs available

Field	Value
ItemID	3c517b59-052a-456c-b4c5-57089f2d8c05
Opened	2024-01-30 15:12:13.872778822 +0000 UTC
Closed	2024-01-30 15:12:14.271701095 +0000 UTC
#Claims / #Results	6 / 11
Message	Session for a8104110-0b77-4970-a509-7d0c24db07b1

Result Types	
Pass	2024-01-30 15:12:13.879196344 +0000 UTC
Fail	2024-01-30 15:12:13.888880682 +0000 UTC
VF	
VCA	
NR	
MEV	
RCF	
U	

marblerun_marbleinstance	Pass	2024-01-30 15:12:13.879196344 +0000 UTC
marblerun_marble	Pass	2024-01-30 15:12:13.888880682 +0000 UTC
marblerun_package	Pass	2024-01-30 15:12:13.893207846 +0000 UTC
marblerun_infrastructure	Pass	2024-01-30 15:12:13.896588736 +0000 UTC
tpm2_validSignature	Pass	2024-01-30 15:12:14.222408095 +0000 UTC
tpm2_validNonce	Pass	2024-01-30 15:12:14.225722217 +0000 UTC
tpm2_safe	Pass	2024-01-30 15:12:14.228814337 +0000 UTC
tpm2_PCRSelection	Pass	2024-01-30 15:12:14.232059546 +0000 UTC
tpm2_quoteDigest256	Pass	2024-01-30 15:12:14.235505859 +0000 UTC
keylime_mb	Pass	2024-01-30 15:12:14.26086657 +0000 UTC
keylime_imx	Pass	2024-01-30 15:12:14.268084426 +0000 UTC

Valid Claims/Errors	
Valid	marblerun/null
Error	marblerun/manifest
Valid	tpm2/quote
Error	tpm2/pcr
Valid	uefi/eventlog
Error	ima/asctldg



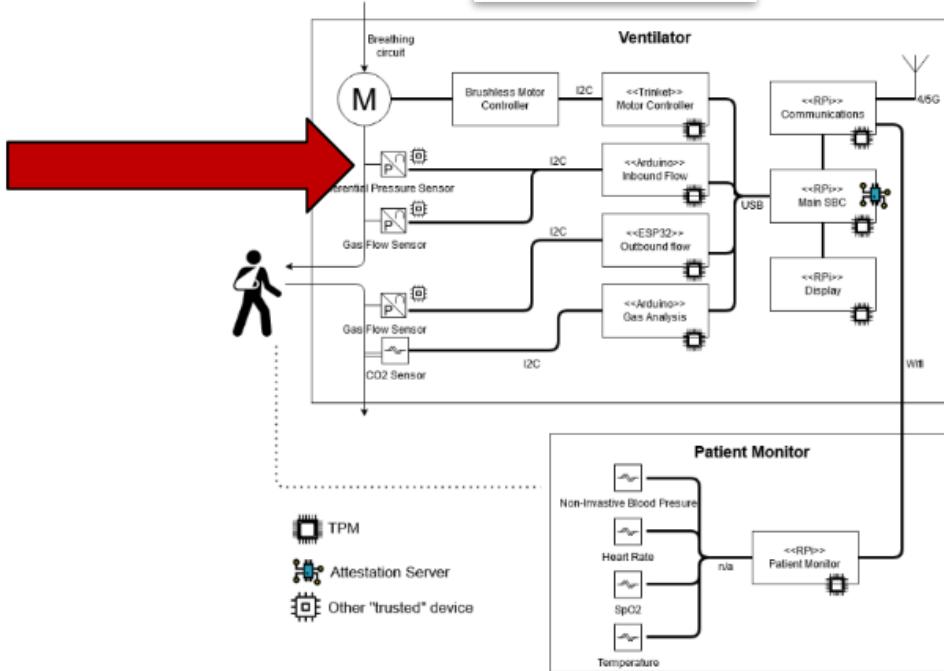
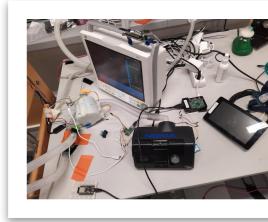
# Forensics





# Changes...

If I change this sensor's  
firmware, identity,  
configuration...





# Quotes...over time

## How do we understand this?

- Magic number? Type?
  - Signed? Verified?
  - QualifiedSigner?
  - AttestedValue?
  - Clock? Reset? Restart? Safe?

## Periodic Attestation Event Based Attestation

## What about the history of quotes?

- Clock updating?
  - PCRs chaning after updates?
  - Which PCRs etc....
  - UEFI eventlog, Manufacturer certs, Other roots of trust, other cross-referencing...
  - etc...



# Analysing Trust Failures

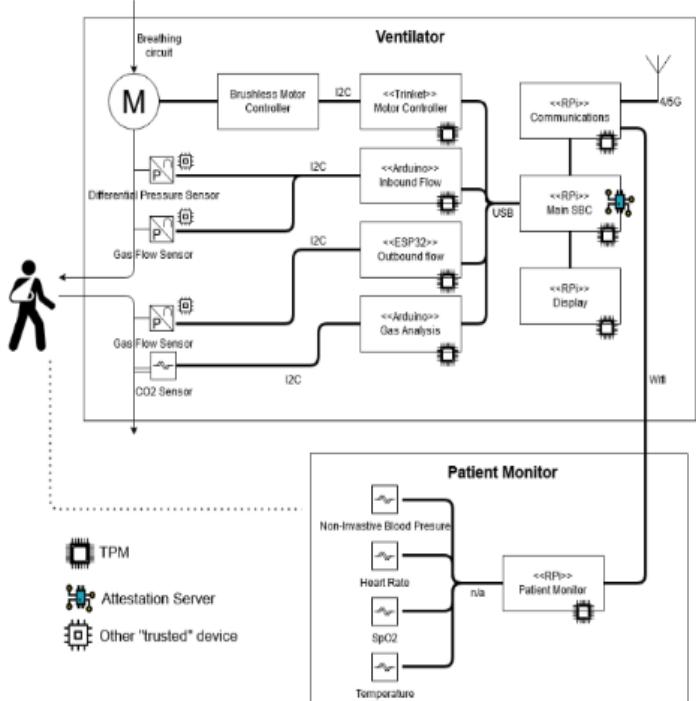
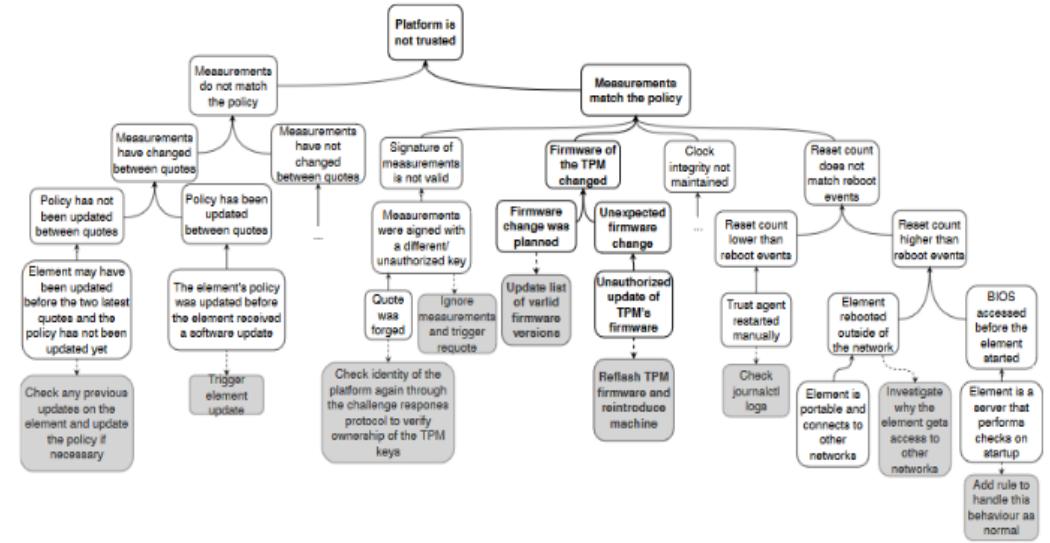


Fig. 1. Ventilator Components

Are we sure that this is a secure failure?





# Real-life Example

Email this comparison

Compare!

Clear all

Time	Type	Value	Description
2022-08-30_07:51:13	result	<a href="#">4b826b48-61af-4b24-b17f-31e4bbd331a0</a>	Result value=9001 for rule tpm2rules/TPM2QuoteStandardVerify and claim 9a8.
2022-08-30_07:51:13	claim	<a href="#">9a8b5cdb-3e27-4c5f-b048-b5c60e076bdf</a>	Contains: quote
2022-08-30_07:51:13	QuoteResetDifference	old: <a href="#">299567fa-4953-4070-b39f-b1fb6bb92848</a> new: <a href="#">9a8b5cdb-3e27-4c5f-b048-b5c60e076bdf</a>	Reboot/powercycle count is now 116, was 115
2022-08-30_07:51:13	QuoteRestartDifference	old: <a href="#">299567fa-4953-4070-b39f-b1fb6bb92848</a> new: <a href="#">9a8b5cdb-3e27-4c5f-b048-b5c60e076bdf</a>	Hibernation/Sleep count is now 1, was 0
2022-08-30_07:51:13	PCRDigestDifference	old: <a href="#">299567fa-4953-4070-b39f-b1fb6bb92848</a> new: <a href="#">9a8b5cdb-3e27-4c5f-b048-b5c60e076bdf</a>	Now 100485573376e32c640df66f44c6b145dd7ca74c088d1a4b50bd48e96e86 'sizeofSelect': 3}}
2022-08-30_07:51:12	result	<a href="#">2e205f8f-f18e-44d3-97d9-62df45736902</a>	Result value=9001 for rule tpm2rules/TPM2QuoteStandardVerify and claim d8f6
2022-08-30_07:51:12	claim	<a href="#">d8f02a9c-370e-415b-a055-c25bf27a692b</a>	Contains: quote

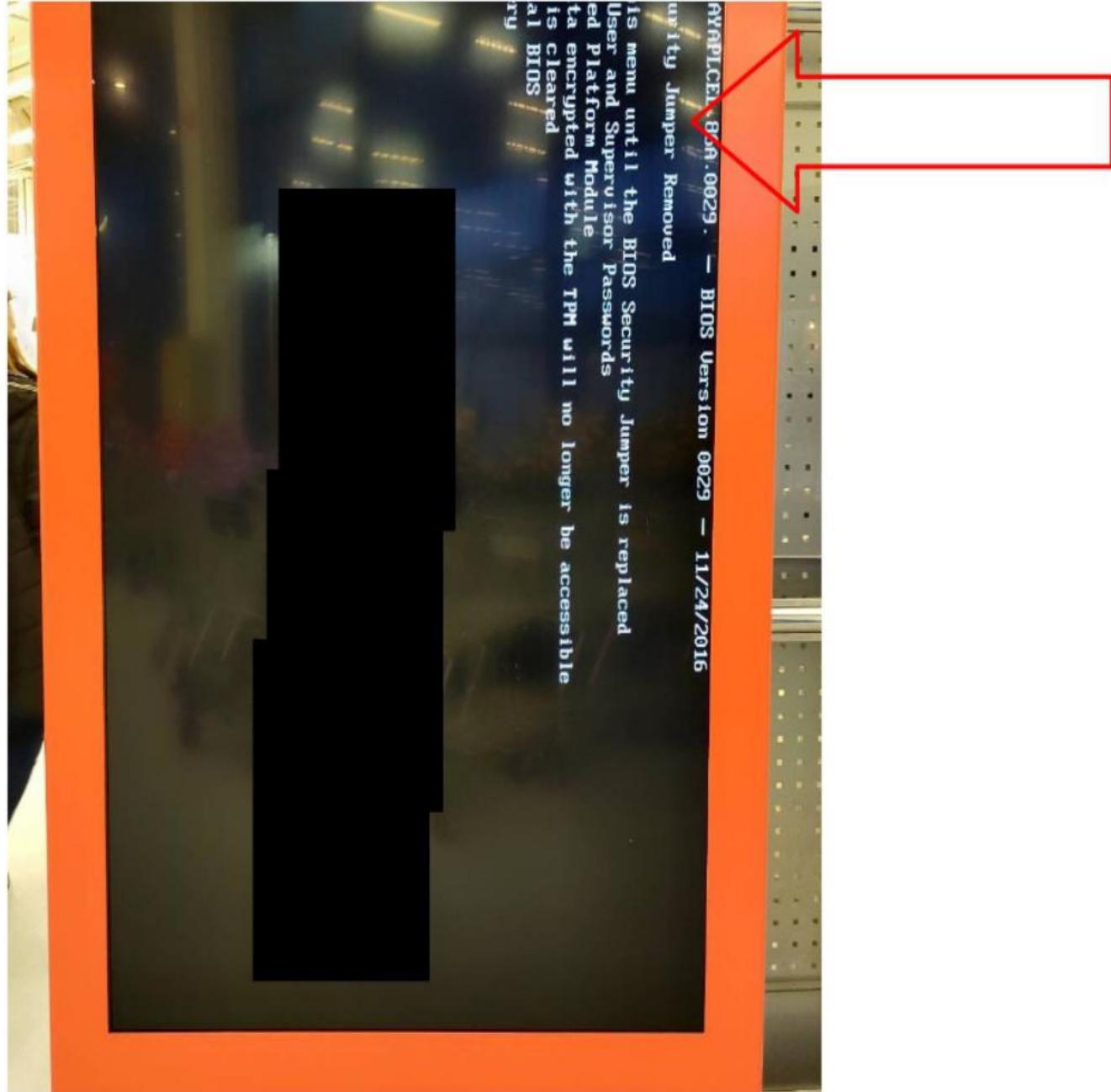
Roosa Risto (2022). **Forensics from Trusted Computing and Remote Attestation**. MSc Thesis. University of Oulu.

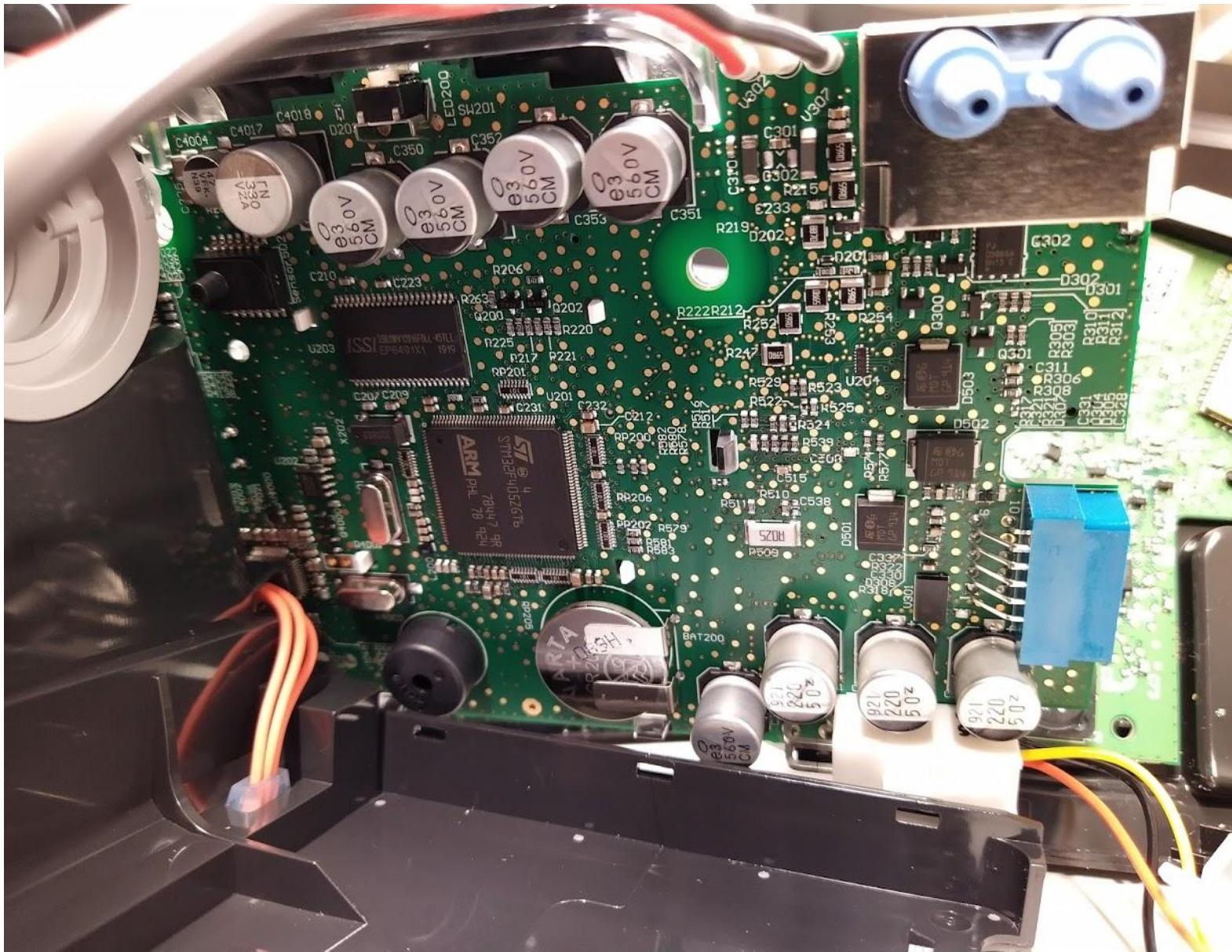


# Examples











# Case Studies





# Case Study 1



1. Changing environment
2. Device provisioning vs Device Interactions
3. Real-Time Data Flows
  1. Trusted vs Untrusted flows
  2. Bulk vs Continuous
4. Data Provenance
5. Notarisation and Auditing
6. Trusted Control and Data Plane
7. Remote Working

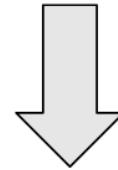


# Case Study 2

The screenshot shows the 'Rail scenario' administration interface. It includes sections for 'Attestation containers' and 'Device containers'. Under 'Attestation containers', there are four entries: '/Attestation' (running for 4 hours), '/AttestationFrontend' (running for 47 hours), '/MongoDB' (running for 4 hours), and '/MQTT' (no status). Under 'Device containers', there are two entries: '/Interlocking' (running for 4 hours) and '/Train100' (running for 4 hours). Each entry has buttons for Create, Run, Stop, Delete, Shell, and Details.

Administration

Attack  
Injection



Operator Response &  
Security Procedures

The screenshot shows the 'Rail scenario' operator control interface. It features a track diagram with several train car icons and numerical labels (321, 331, 332, 333, 341, 342, 351) indicating their positions. A red line highlights a segment of the track between positions 332 and 333. Buttons at the top include 'Set Scenario', 'Reserve track', and 'Move train'.

Operator & Simulation Control

The screenshot shows the 'Last Trust Decision' and 'BASIC RULESET' sections. The 'Last Trust Decision' table lists two policies: 'v311 policy sha1:0.1.2' and 'v311 policy sha256:0.1.2', each with checkmarks for various validation fields. The 'BASIC RULESET' table lists five rulesets: 'Cloud Slices', 'Trust Slices', 'Control', 'Main Route', 'Shunting Route', and 'Train', each with a 'TRUST' button.

Attestation

- Ronny Bäckman, Ian Oliver, Gabriela Limonta (2020). **Integrity Checking of Railway Interlocking Firmware**. In Proceedings of 15th International Workshop on Dependable Smart Embedded Cyber-Physical Systems and Systems-of-Systems (DECSoS), Lisbon, 15 Sept 2020
- Ronny Bäckman. **Simulating Rail Traffic Management with Trusted Computing**. BSc Thesis. XAMK Kotka Finland. May 2020



# Additional Information





# Additional Information

**IC00AZ56 Trusted and Confidential Computing, 5 op**

4 Jan 2026 – 8 March 2026

MSc/BSc thesis topics available

**Jane: Experimental Attestation Server**

<https://github.com/iolivergithub/jane/>

Includes: attestation server, trust agent, policy system, example code,

**TPM Course (forked from Nokia TPM Course)**

<https://github.com/iolivergithub/TPMCourse>

Includes docker container with TPM simulator, TPM tools, CRIM worksheets!



# The End

