

### General Conventions:


Signal names beginning with a '/' are active low

Unless Specified Otherwise:

- All resistors are 1% metal film, 1/10W, in 0603 package
- All non-polarized capacitors are ceramic, in 0603 package
- All ceramic capacitors up to and including 1,000pF are NPO, 50V or higher, 5% or better
- All ceramic capacitors over 1,000pF up to and including 1.0uF are X7R, 16V or higher, 10% or better
- All ceramic capacitors over 1.0uF are of the specified type and voltage
- All polarized capacitors are of the specified type and voltage

### Missing Designators:

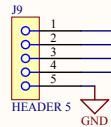
C39

Title: <b>IEEE-1394 FPGA Controller</b>			 <b>LABORATORY FOR Computational Sensing + Robotics</b> THE JOHNS HOPKINS UNIVERSITY <a href="http://lcsr.jhu.edu">http://lcsr.jhu.edu</a>
Part No:	Rev: 1.3	Print Date: 2/17/2015	
File Name: S01.SchDoc			

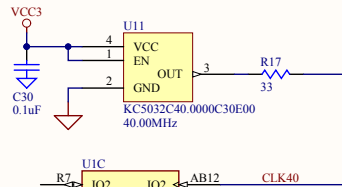
Sheet 1 of 6

# Amp Board Connector

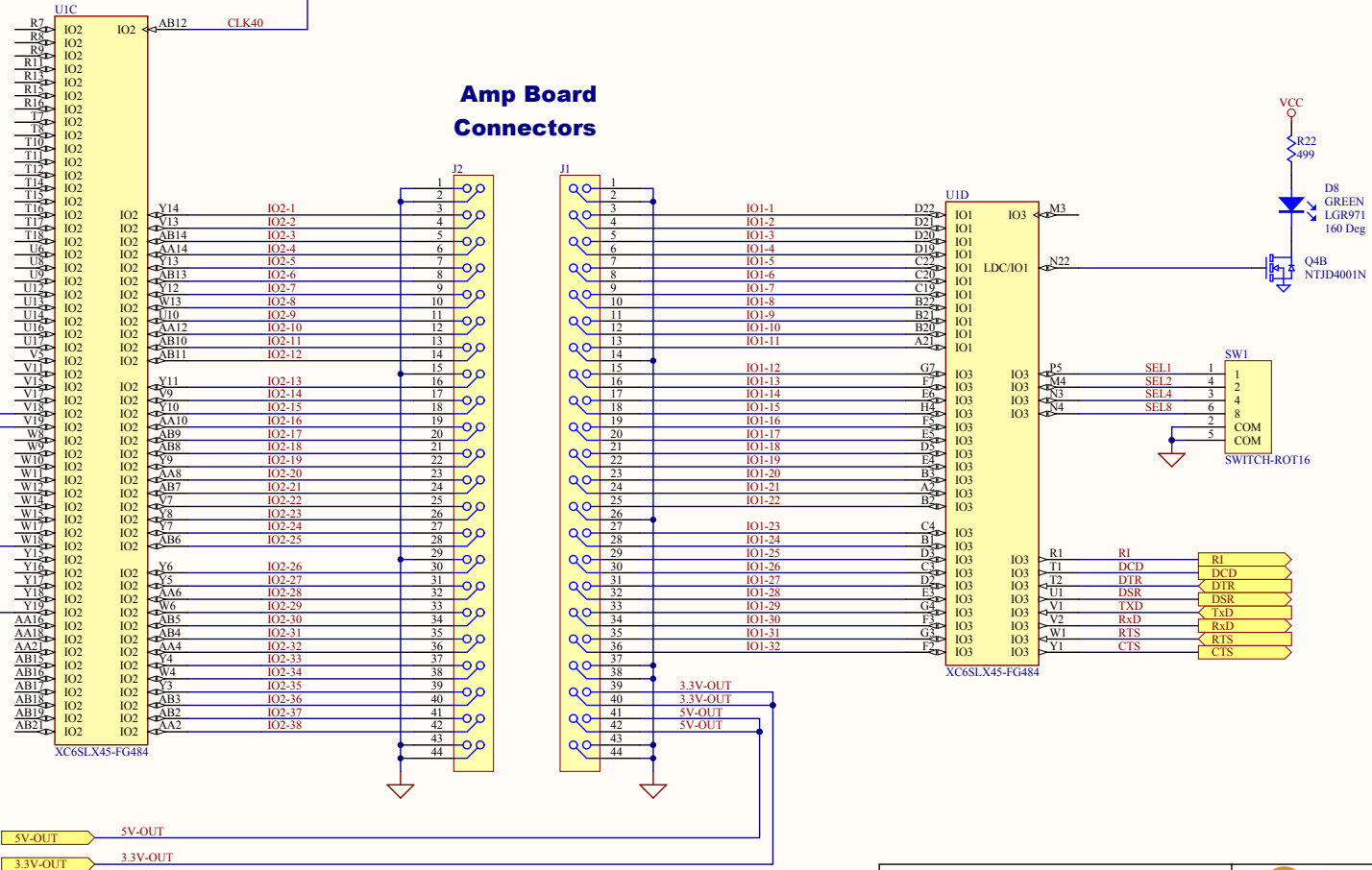
## Soft JTAG Port



SOFT-TMS  
SOFT-TCK  
SOFT-TDO  
SOFT-TDI

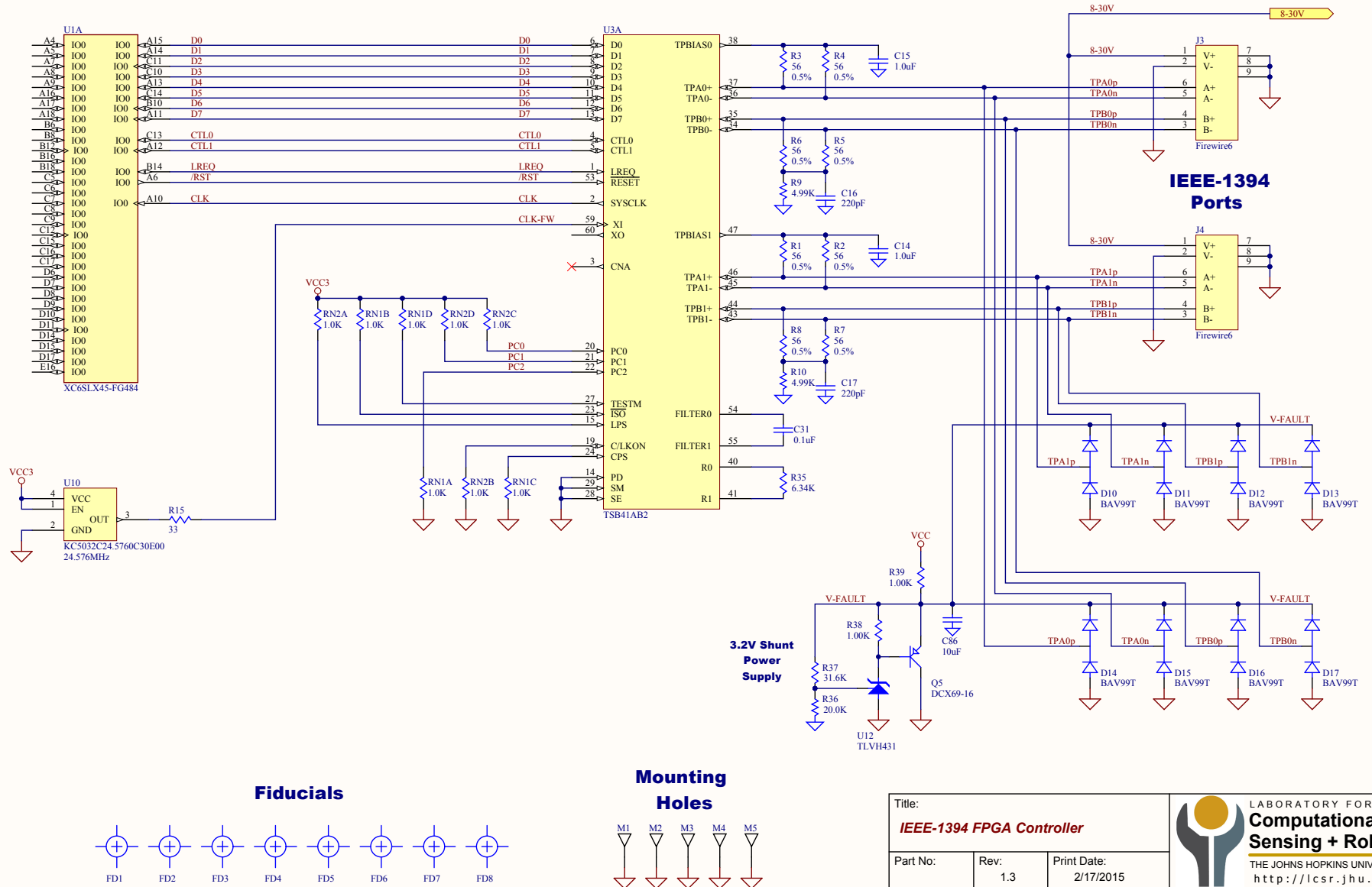


## Amp Board Connectors



Title: <b>IEEE-1394 FPGA Controller</b>			
Part No:	Rev: 1.3	Print Date: 2/17/2015	<p>LABORATORY FOR <b>Computational Sensing + Robotics</b> THE JOHNS HOPKINS UNIVERSITY <a href="http://lcsr.jhu.edu">http://lcsr.jhu.edu</a></p>
File Name: S02.SchDoc			
Sheet 2 of 6			

# FireWire Controller & Connectors



A

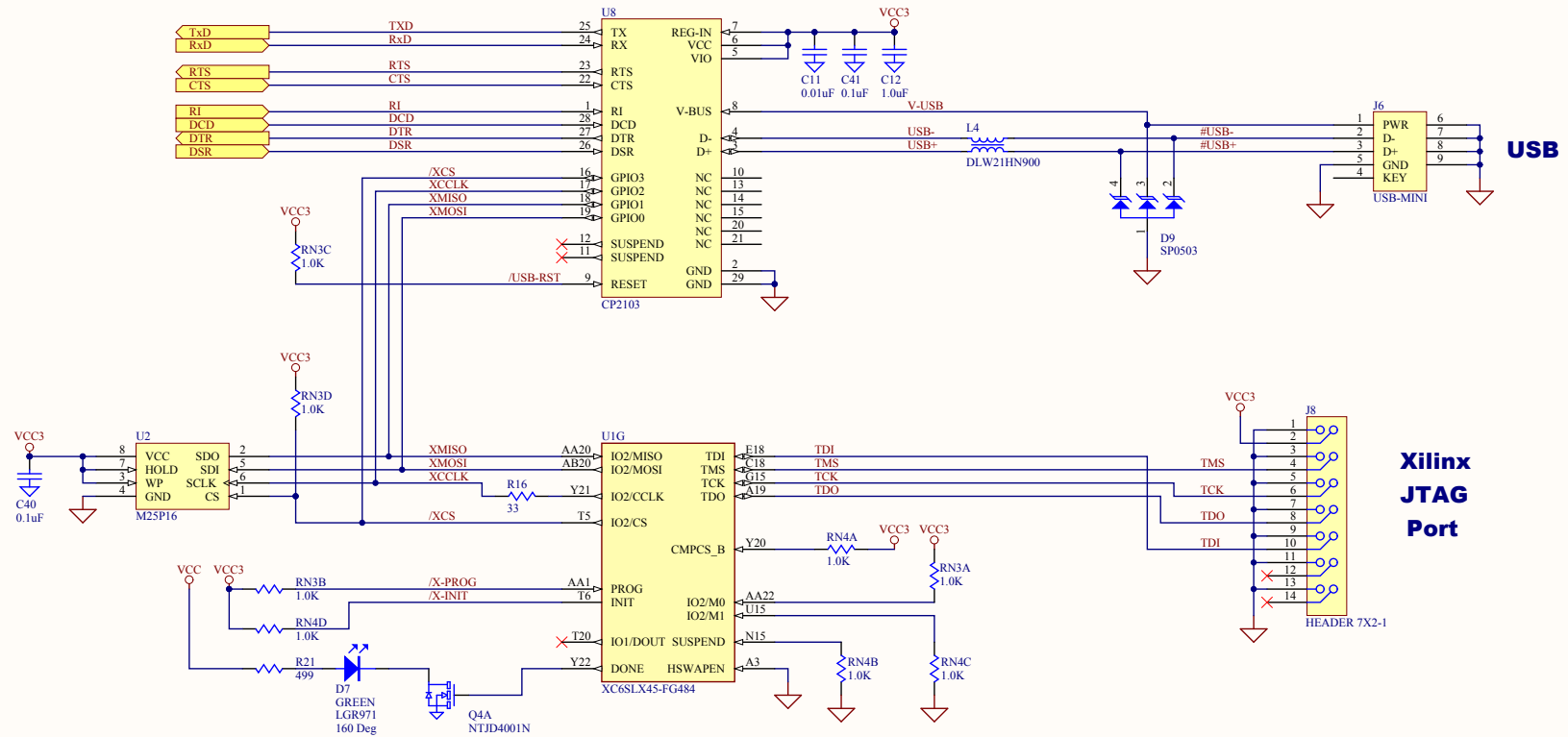
B

E

3

5

# JTAG Port



**Xilinx  
JTAG  
Port**

# Power Supplies

