

General Conventions:

Signal names beginning with a '/' are active low

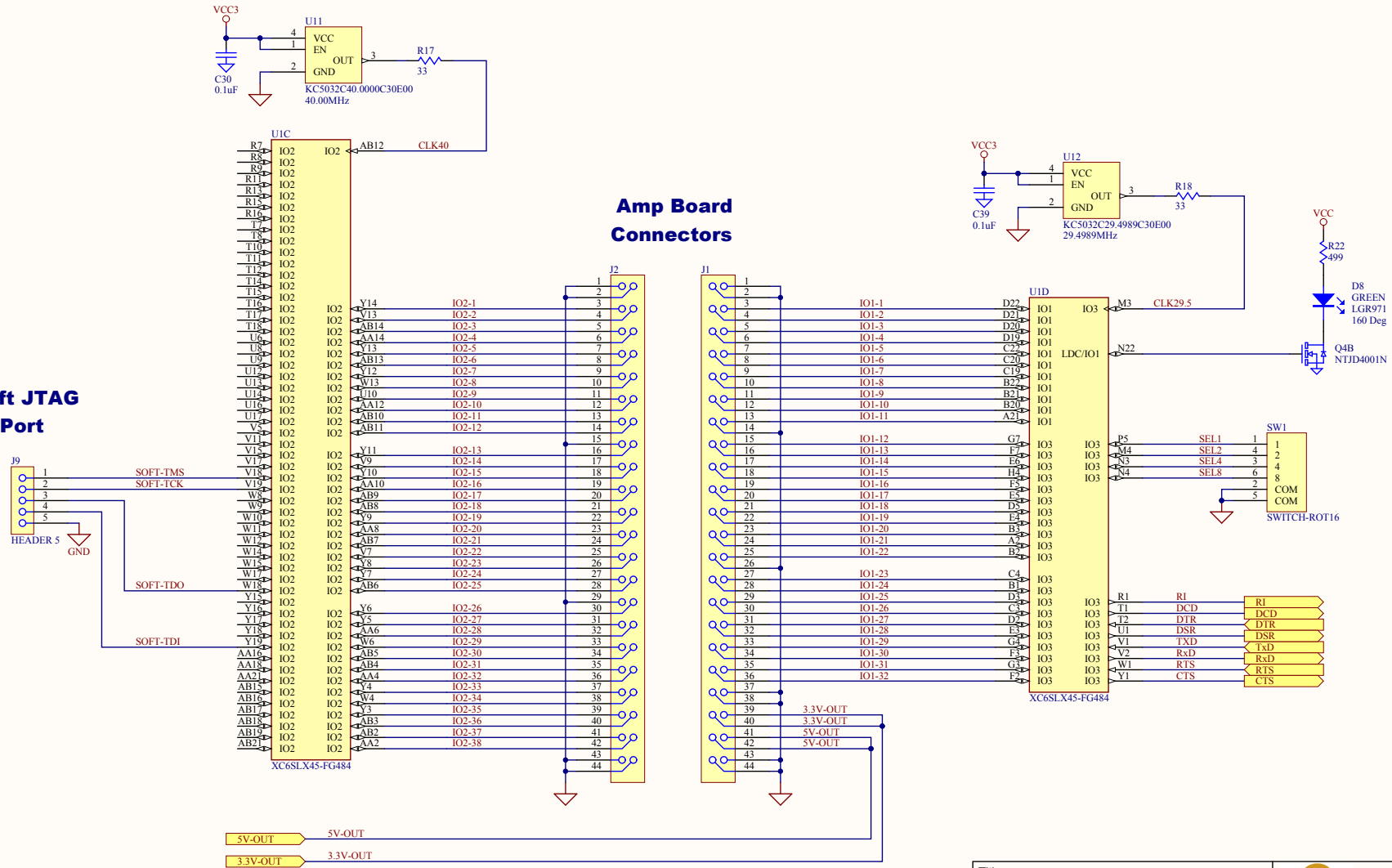
Unless Specified Otherwise:

- All resistors are 1% metal film, 1/10W, in 0603 package**
- All non-polarized capacitors are ceramic, in 0603 package**
- All ceramic capacitors up to and including 1,000pF are NPO, 50V or higher, 5% or better**
- All ceramic capacitors over 1,000pF up to and including 1.0uF are X7R, 16V or higher, 10% or better**
- All ceramic capacitors over 1.0uF are of the specified type and voltage**
- All polarized capacitors are of the specified type and voltage**

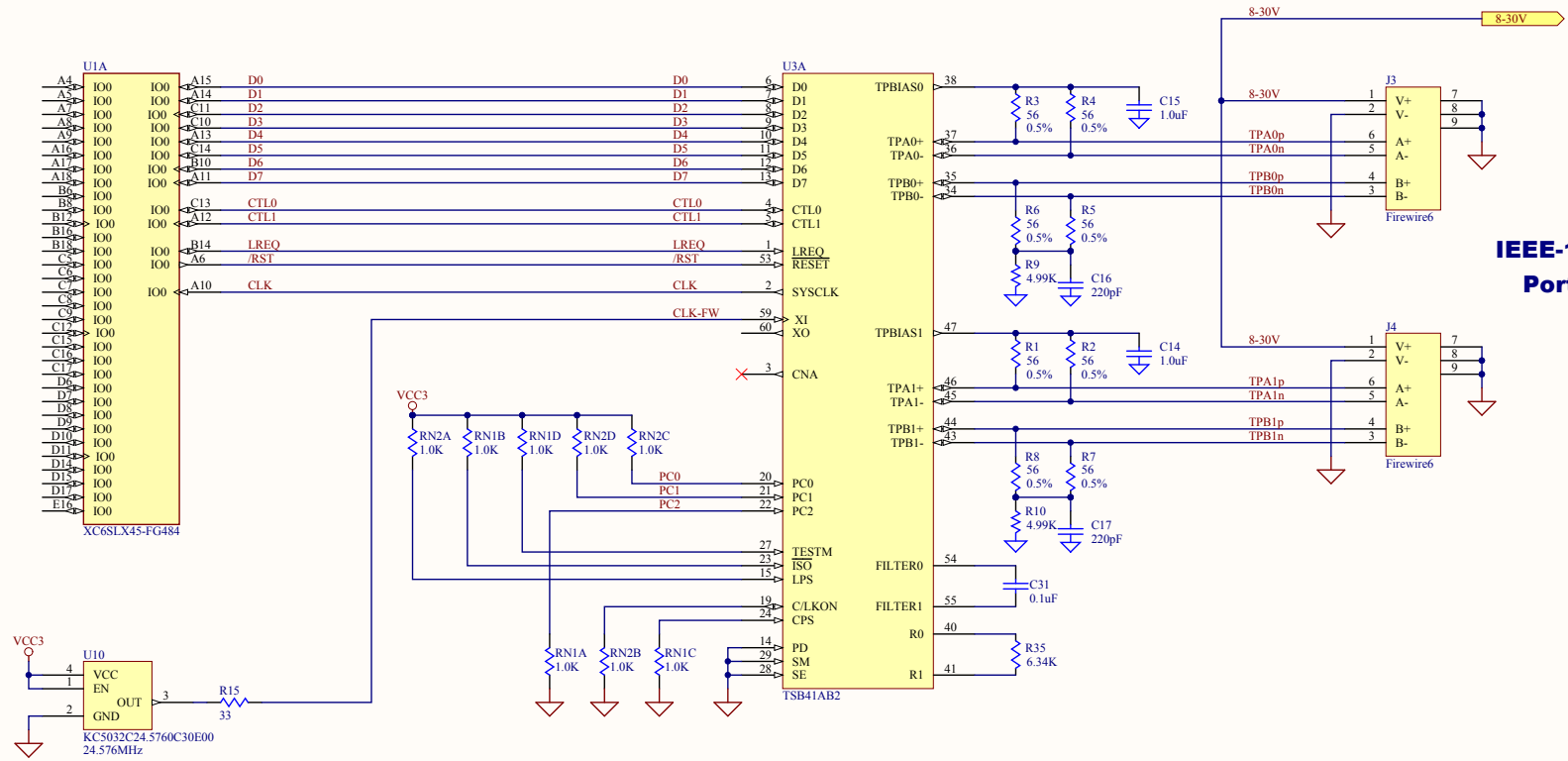
Amp Board Connector

Soft JTAG Port

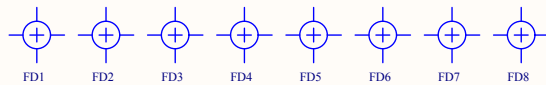
Amp Board Connectors



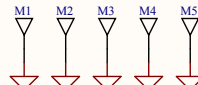
FireWire Controller & Connectors



Fiducials



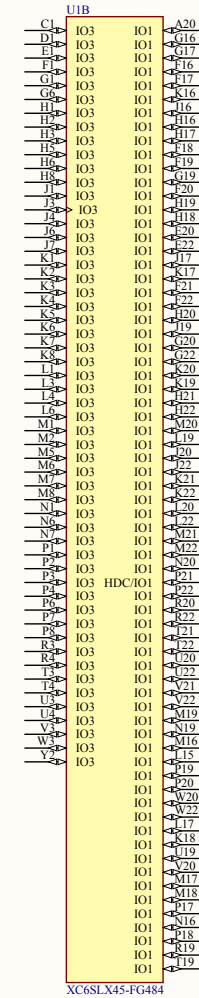
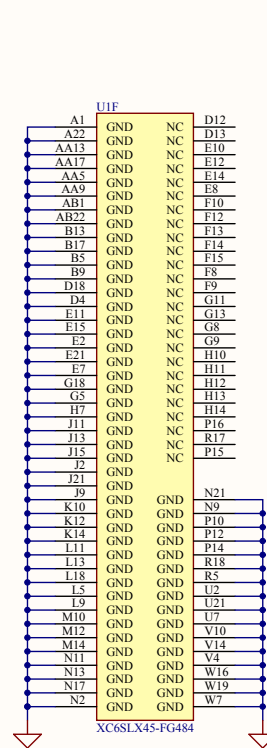
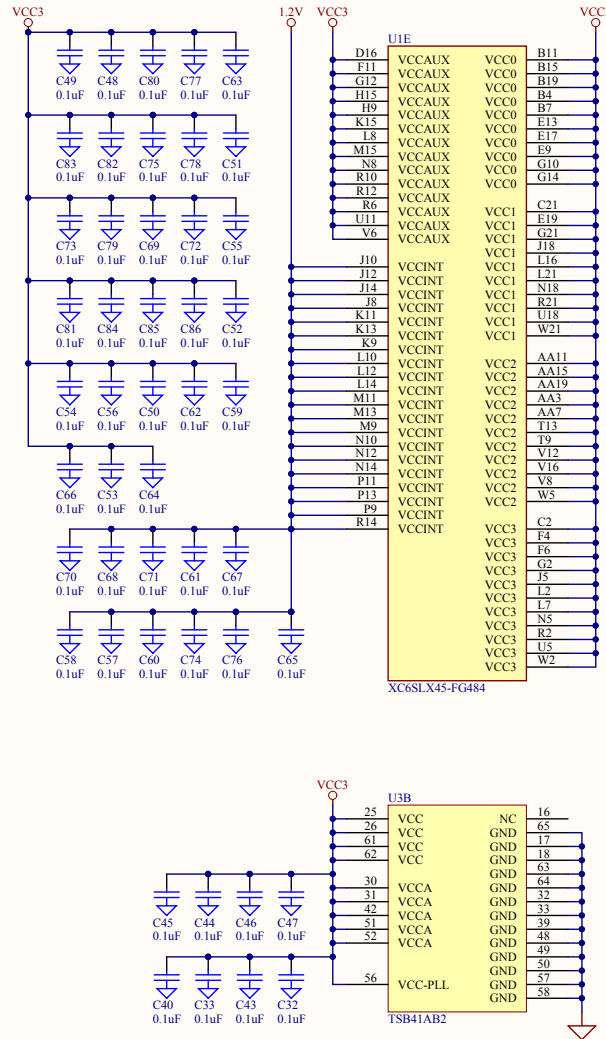
Mounting Holes



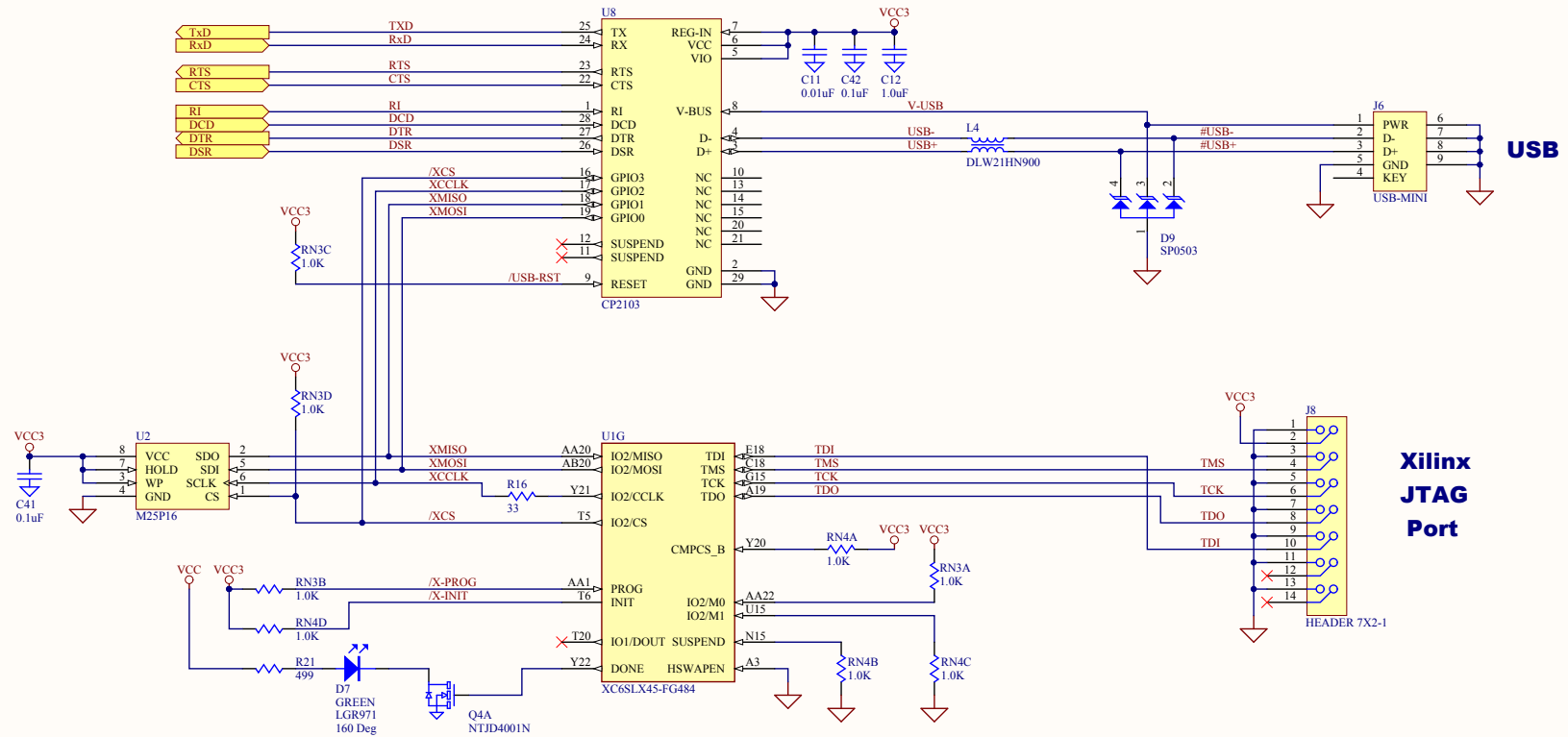
Title: IEEE-1394 FPGA Controller			 <div>LABORATORY FOR Computational Sensing + Robotics</div> <hr/> <div>THE JOHNS HOPKINS UNIVERSITY http://lcsr.jhu.edu</div>
Part No:	Rev: 1.2	Print Date: 6/13/2013	
File Name: S03.SchDoc			
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Power Pins & Unused



JTAG Port



Power Supplies

Power Connectors

