1-XI D,16-Bit

(Mnemonics	Hex codes
Mnemonics	Hex codes	MOV D, E	53
MOV A, D.	7A		54
MOV A, E	7B	MOV D, H	55
MOV A, H	7C	MOV D, L	56
MOV A, L	7D	MOV D, M	5F
MOV A, M	7E	MOV E, A	58
1-1	47	MOV E, B	59
MOV B, A	40	MOV E, C	5A
MOV B, B	41	MOV E, D	5B
MOV B, C	42	MOV E, E	5C
MOV B, D	43	MOV E, H	5D
MOV B, E	44	MOV E, L	5E
MOV B. H	45	MOV E, M	67 .
MOV B. L	46 .	MOV H, A	60
MOV B, M	ÅF	MOV H, B	61
MOV C, A	48	MOV H, C	62
MOV C. B	49	MOV H, D	63
MOV C. C	4A	MOV H, E	64
MOV C, D	4B	MOV H, H	65
MOV C, E	4C	MOV H, L	66
MOV C, H	4D	MOV H, M	6F
MOV C, L	4E.	MOV L, A	68
MOV C, M	57	MOV L, B	69
MOV D, A	50	MOV L, C	6A
MOV D, B	51	MOV L, D	6B
MOV D, C		MOV L, E	
MOV D, D	52		
IVIO			

(Mnemonics	Hex codes	Mnemonics	Hex codes
)	MOV L, H	6C '	(ORA L	B5
	MOV L, L	6D	ORA M	В6
	MOV L, M	6E	ORI	F6
	MOV M, A	77	OUT	D3
	MOV M, B	70	PCHL	E9
	MOV M, C	71	POP B	C1
	MOV M, D	- 72	POP D	D1
-	MOV M, E	73	POP H	E1
	MOV M, H	74	POP PSW	F1
,	MVI A	75	PUSH B	
	MVI B	3E	/ PUSH D	C5
	MVI C	06	PUSH H	D5
	MVI D	0E	PUSH PSW	E5
_	MVI E	16.		F5
_	MVI H	1E	RAR	17
_	MVI L	26	7 RC	1F
_	MVI M	2E	RET	D8
4	NOP	. 36	RIM	C9
7	ØRA A	00	RLC	20
X	ORA B	B7	RM	07
X	ORA C	B0	RNC	F8
X	ORA D	B1	RNZ	D0
X	ORA E	B2	₩ RP	CO
X	ORA H	B3	RPE	F0
		P ₃ 4	✓ RPO	E8
		*	14 KPO	EO
				20

R RS

R

F

	Mnemonics	Hex codes		Mnemonics	Hex codes
	RRC	OF	-	STAX D	12
	RST 0	C7	-	STÇ	37
	RST 1	CF	E.	SUB A	97
	RST 2	D7	X	> SUB B	90
	RST 3	DF	X	SUB C	91
	RST 4	E7	×	SUB D	92
	RST 5	EF	Y	SUB E	93
	RST 6	F7	X	SUB H	94
1	RST 7	FF	+	SUB L	95
	RZ	C8		SUB M	96
1	SBB A	9F	4	SUI	D6
1	SBB B	98	_	XCHG	EB
)	SBB C	99	-	XRA A	AF
	SBB D	9A		XRA B	A8
1	SBB E	9B	4	XRA C	A9
(SBB H	<u>9C</u>	•	XRA D	AA
	SBB L	9D		XRA E	AB
-	SBB M	9E		XRA H	AC
	∑ SBI	DE		XRA L	AD
,	SHLD	22		XRA M	. AE
	SIM	30		L XRI	EE
(SPHL	F9		XTHL	E3
,	STA	32			•
	STAX B	02			

(c) 8085 Instructions in the order of their Hex codes

How	, soos instructions in	the order of the	
Hex codes	Mnemonics	Hex codes	Mnemonics
00	NOP	1C	INR E
01	LXI B, 16-bit Data	ID ID	X DCR E
02	STAX B	1E	MVI E
03	INX B		V RAR
04		lF 20	RIM
05	INR B	20 .	LXI H
06	DCR B	21	SHLD
	MVI B, 8-bit Data	22	
07	RLC	23	INA II
09	DAD B	24	INR H
0A	LDAX B	25	DCR H
0B	DCX B	26	MVI H
0C	INR C	27	DAA
0D	DCR C	29	DAD H
0E \leftarrow	MVI C	2A	LHLD
0F	RRC ,	2B	DCX H
11	LXI D	2C	INR L
12	STAX D	2D	DCR L
13	INX D	2E	MVI L, 8-bit Data
14	INR D	2F .	CMA .
15	DCR D	30	SIM
16	MVI D	31	LXI SP, 16-bit Data
17	RAL	31	LXI SP, 16-bit Data
19	DAD D	32	STA 16-bit address
1A	LDAX D	33	INX SP · *
1B	DCX D	34	INR M

HEX MNEMONIC C2 JNZ 16-Bit F2 JP JNZ 16-Bit EA JPE 16-Bit E2 JPO 16-Bit CA JZ 16-			0	
C2		MNEMONIC		
F2	C2			MNEMONIC
EA JPE 16-Bit	F2		1	11000
E2 JPO 16-Bit	EA			******
CA	E2			MOV B,A
3A LDA 16-Bit 0A LDAX B 1A LDAX D 1A LDAX D 2A LHLD 16-Bit 01 LXI B,16-Bit 1 LXI D,16-Bit 1 LXI SP,16-Bit 7B MOV A,A MOV B,C 42 MOV B,D 43 MOV B,E 44 MOV B,H 45 MOV B,H 46 MOV B,M 47 MOV C,A 48 MOV C,B 49 MOV C,C 40 MOV C,C 40 MOV C,L 41 MOV B,D 42 MOV B,H 44 MOV C,A 45 MOV C,A 46 MOV C,B 47 MOV A,A 48 MOV C,B 49 MOV C,C 40 MOV C,C 40 MOV C,L 41 MOV C,D 42 MOV C,D 43 MOV C,A 44 MOV C,B 45 MOV C,B 46 MOV C,B 47 MOV C,C 48 MOV C,B 49 MOV C,C 40 MOV C,L 41 MOV C,L 42 MOV C,L 44 MOV C,L 45 MOV C,L 46 MOV C,L 47 MOV C,L 48 MOV C,L 49 MOV C,L 40 MOV C,L 41 MOV C,L 42 MOV C,L 44 MOV C,L 45 MOV C,L 46 MOV C,L 47 MOV C,L 48 MOV C,L 49 MOV C,L 40 MOV C,L 40 MOV C,L 41 MOV C,L 42 MOV C,L 44 MOV C,L 45 MOV C,L 46 MOV C,L 47 MOV D,A 48 MOV C,L 49 MOV C,L 40 MOV C,L 40 MOV C,L 41 MOV C,L 42 MOV C,L 44 MOV C,L 45 MOV C,L 46 MOV C,L 47 MOV D,A 48 MOV C,L 49 MOV C,L 40 MOV C,L 40 MOV C,L 41 MOV C,L 42 MOV C,L 44 MOV C,L 45 MOV C,L 46 MOV C,L 47 MOV D,A 48 MOV D,A 49 MOV D,A 40 MOV D,A 40 MOV D,A 41 MOV D,A 41 MOV D,A 42 MOV D,A 43 MOV B,D 44 MOV B,B 45 MOV B,B 46 MOV B,B 46 MOV B,B 47 MOV B,B 48 MOV B,B 49 MOV B,B 40 MOV B,B 40 MOV B,B 40 MOV B,B 41 MOV B,B 42 MOV B,B 44 MOV B,B 45 MOV B,B 46 MOV B,B 46 MOV B,B 46 MOV B,B 47 MOV B,B 48 MOV C,B 48 MOV C,B 48 MOV C,B 49 MOV C,B 40 MOV D,B 41 MOV D,B 41 MOV B,B 42 MOV B,B 44 MOV B,B 45 MOV B,B 46 MOV B,B 46 MOV B,B 46 MOV B,B 47 MOV B,B 48 MOV C,B 48	CA			
1A	3A	(2)		
1A	V OA			MOV B,D
2A LHLD 16-Bit Q1 LXi B,16-Bit LXi D,16-Bit 45 MOV B,H MOV C,A 48 MOV C,A 48 MOV C,B 49 MOV C,C MOV A,A 4A MOV C,C MOV A,B 70 MOV A,C 74 MOV A,C 75 MOV A,B 70 MOV A,B 70 MOV A,B 71 MOV A,B 72 MOV A,B 73 MOV A,B 74 MOV A,B 75 MOV A,B 76 MOV A,B 77 MOV A,B 78 MOV A,B 70 MOV A,B 71 MOV A,B 72 MOV A,B 73 MOV D,A 50 MOV D,&B				MOV B,E
1	2A			MOV B,H
1				MOV, B,L
LNI H, 16-Bit 48 MOV C,A LXI SP, 16-Bit 49 MOV C,C X 7F MOV A,A 4A MOV C,D X 78 MOV A,B 4B MOV C,E Y 79 MOV A,C 4C MCV C,E Y 70 MOV A,D 4D MOV C,L Y 70 MOV A,E 4E MOV C,M T 70 MOV A,H 57 MOV D,A T RAR 50 MOV D, GR	1 11			MOV B,M
LXI SP,16-Bit 49 MOV C,C X 7F	(21)			MOV C,A
7F MOV A,A 4A MOV C,D 78 MOV A,B 4B MOV C,E 79 MOV A,C 4C MCV C,H 7A MOV A,B 4D MOV C,L 7B MOV A,E 4E MOV C,M 7C MOV A,H 57 MOV D,A 1F RAR 50 MOV D,A	31			MOV C,B
78 MOV A,B 4B MOV C,B 79 MOV A,C 4C MOV C,H 7A MOV A,D 4D MOV C,L 7B MOV A,H 4E MOV C,M 7C MOV A,H 57 MOV D,A 1F RAR 50 MOV D,&R	X 78			MOV C,C
79 MOV A,C 7A MOV A,D 40 MOV C,L 7B MOV A,E 7C MOV A,H 57 MOV D,A 1F RAR 50 MOV D,ER				MOV C,D
7A MOV A,D 4D MOV C,L 7B MOV A,E 7C MOV A,H 57 MOV D,A 1F RAR 50 MOV D,ER		· · · · ·		MOV CE
70 MOV A,H .4E MOV C,M 70 MOV A,H .57	1	,		MCV CH
7C MOV A,H 57, MOV D,A				MOV C,L
1F RAR 50 MOV D.EB.	1			
				-
51 MOV DC:	1F	RAR		MOV DEB
NOV D,C	•		. 51	MOV D,C

NOTE: 1. Byte Instruction: Operand R,M or implicit

2. Byte Instruction: Operand 8-Bit

3. Byte Instruction: Operand 16-Bit

No.

lang

40 + 4