

Project:  
Location:  
Contract:  
Engineer:

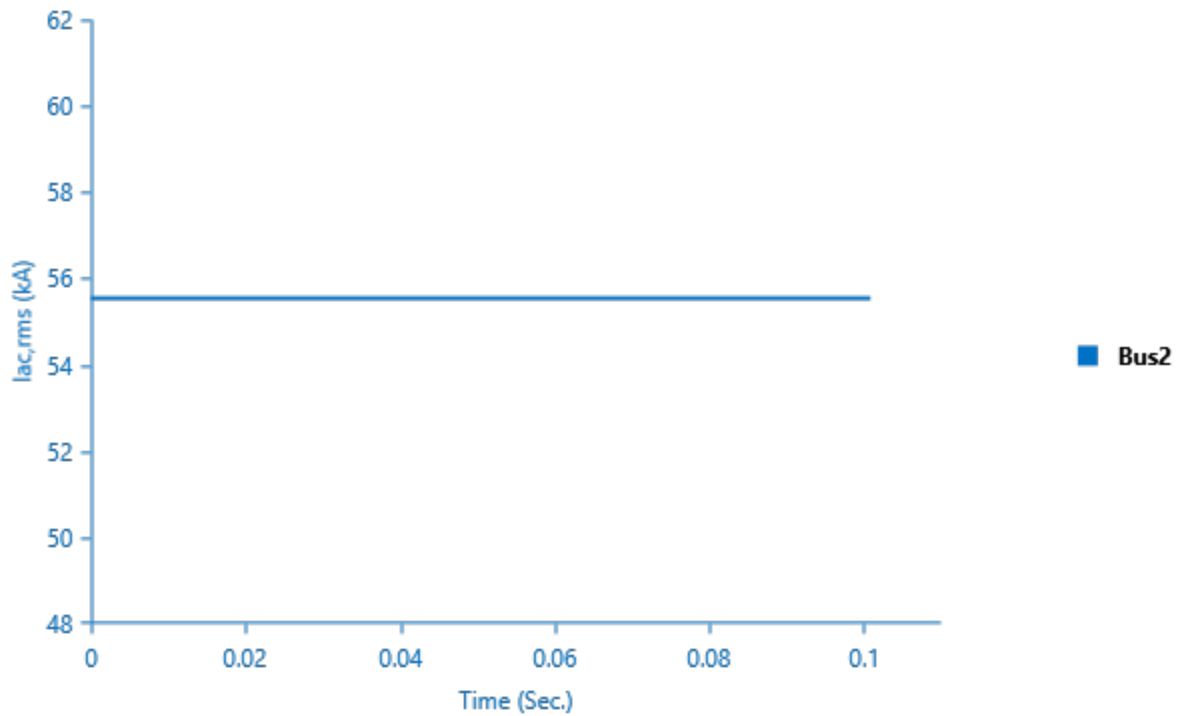
Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)

#### AC Component of Fault Current (rms)



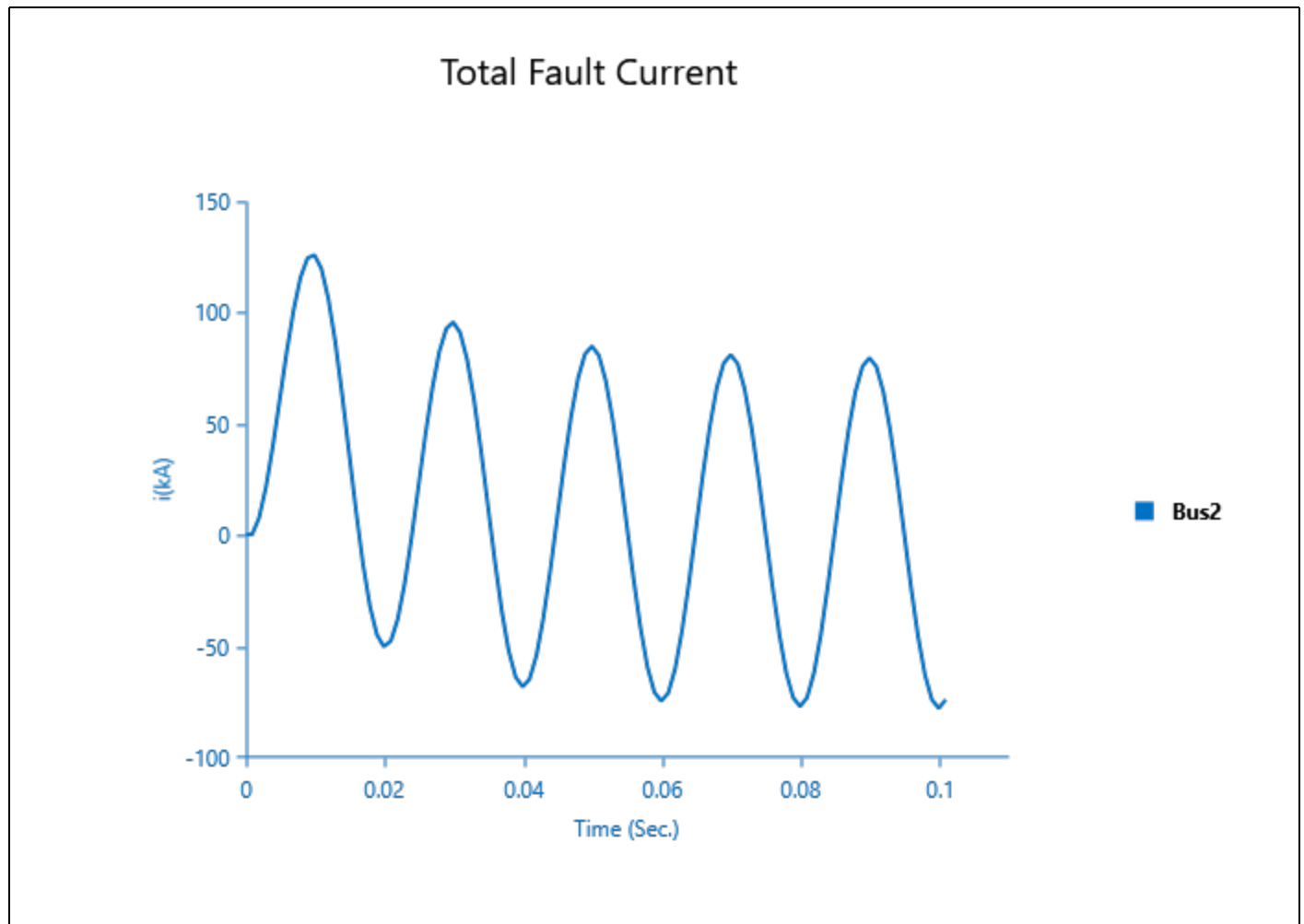
Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)



ETAP  
19.0.1C

Project:  
Location:  
Contract:  
Engineer:

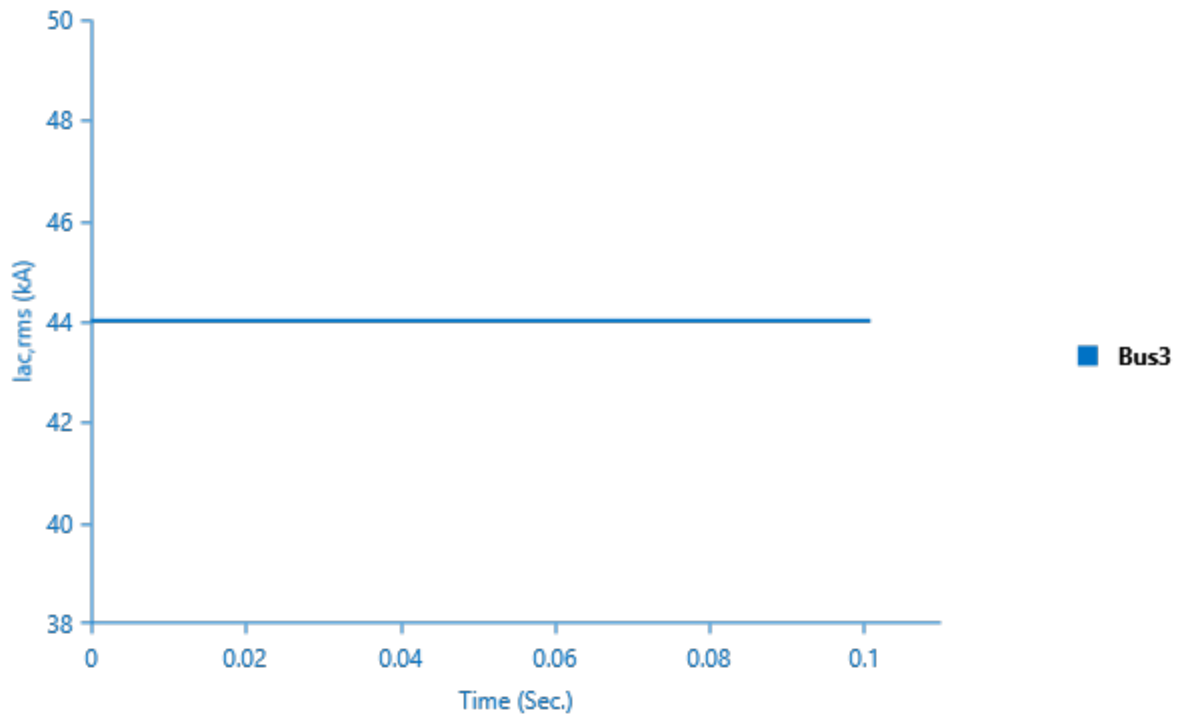
Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)

#### AC Component of Fault Current (rms)



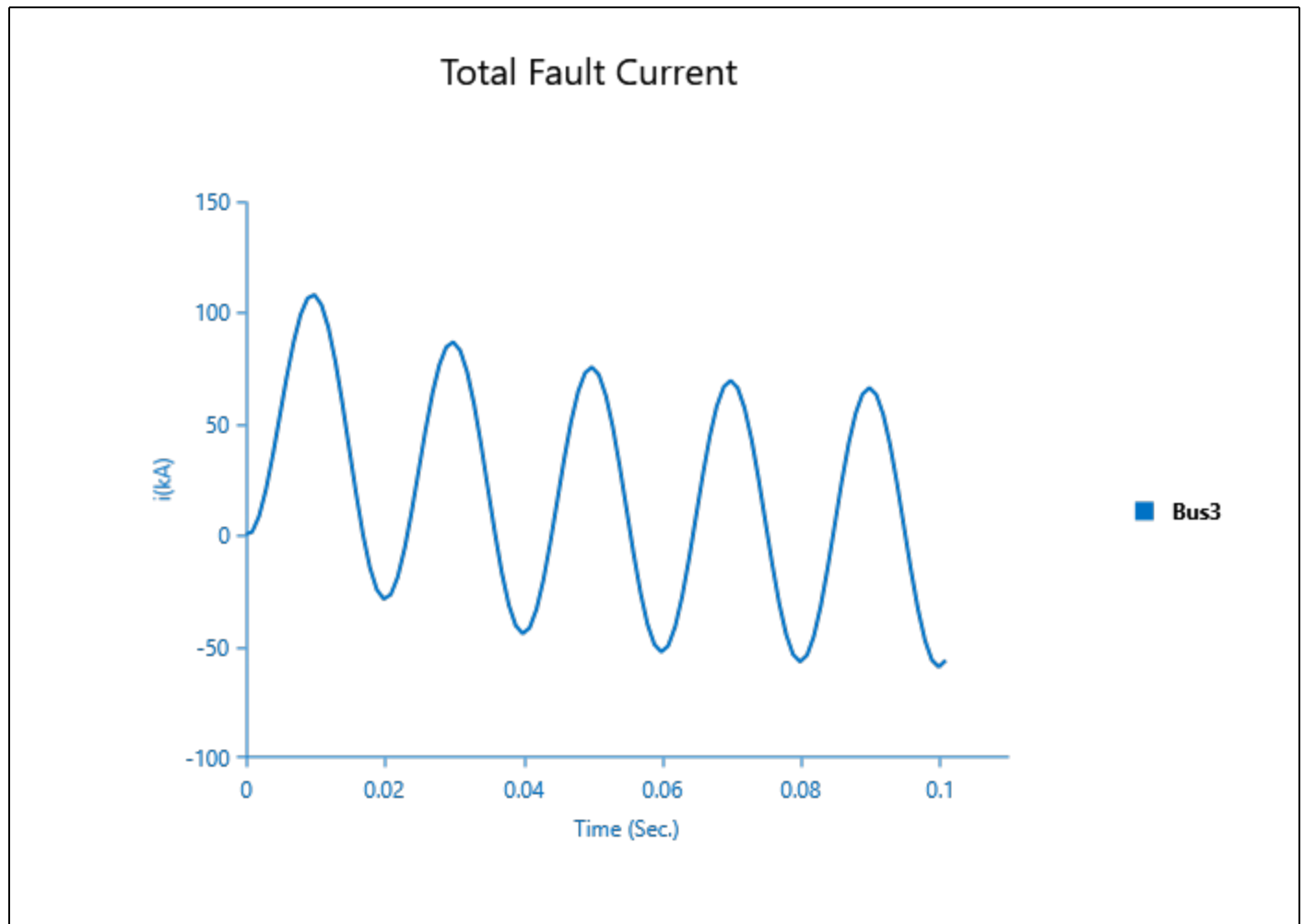
Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)



ETAP  
19.0.1C

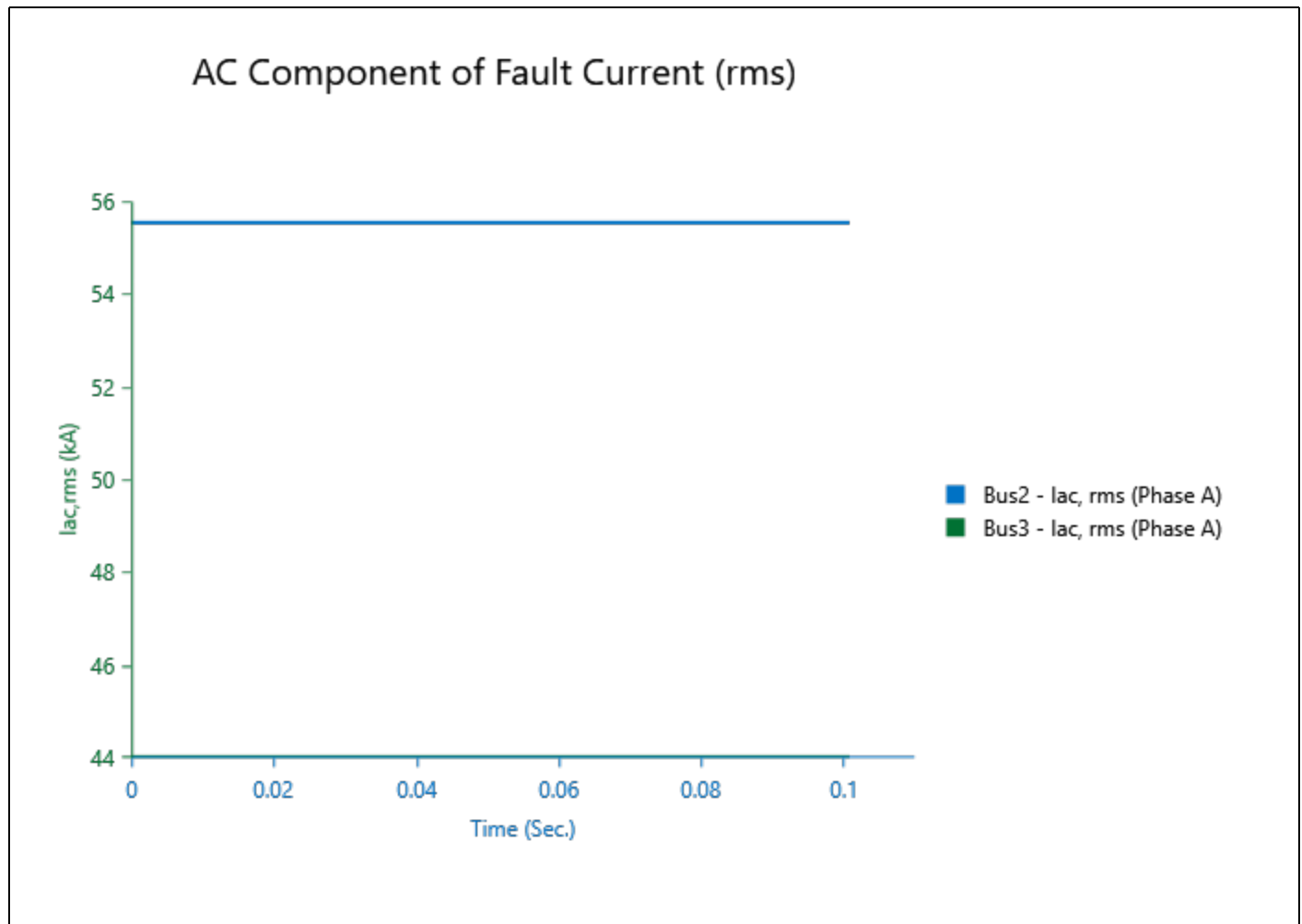
Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
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Project File: C:\ETAP 1901\mustafaday5  
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### Short-Circuit Analysis (IEC363)



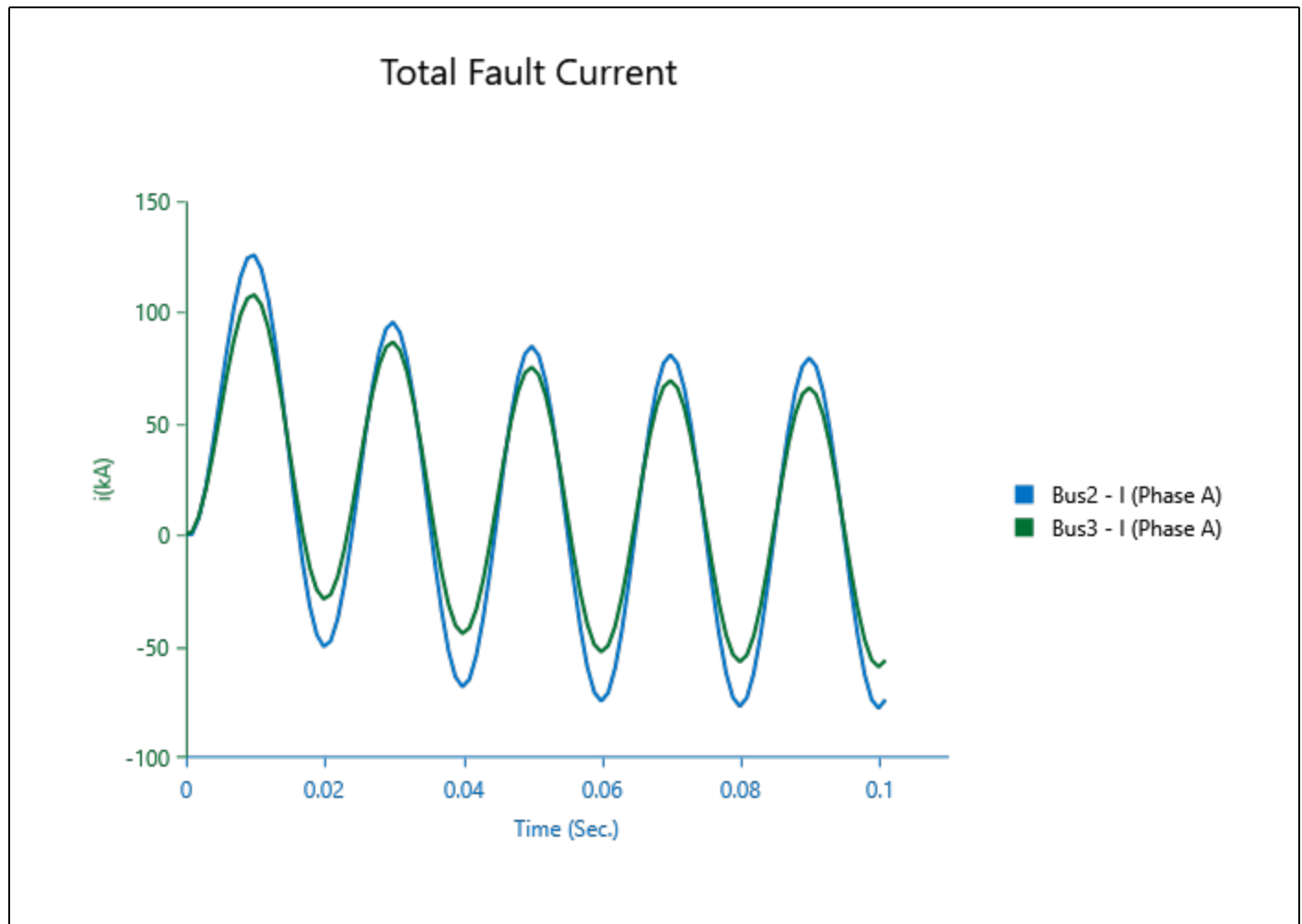
Project:  
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### Short-Circuit Analysis (IEC363)



Project:  
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Contract:  
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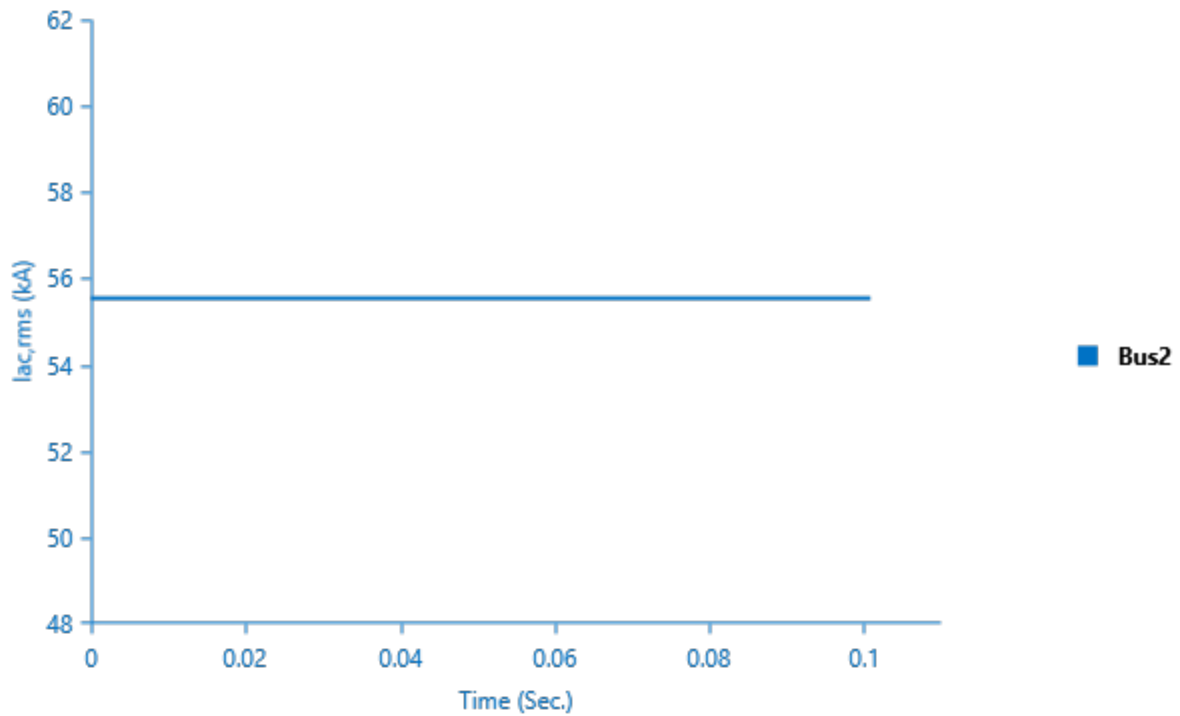
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### Short-Circuit Analysis (IEC363)

#### AC Component of Fault Current (rms)



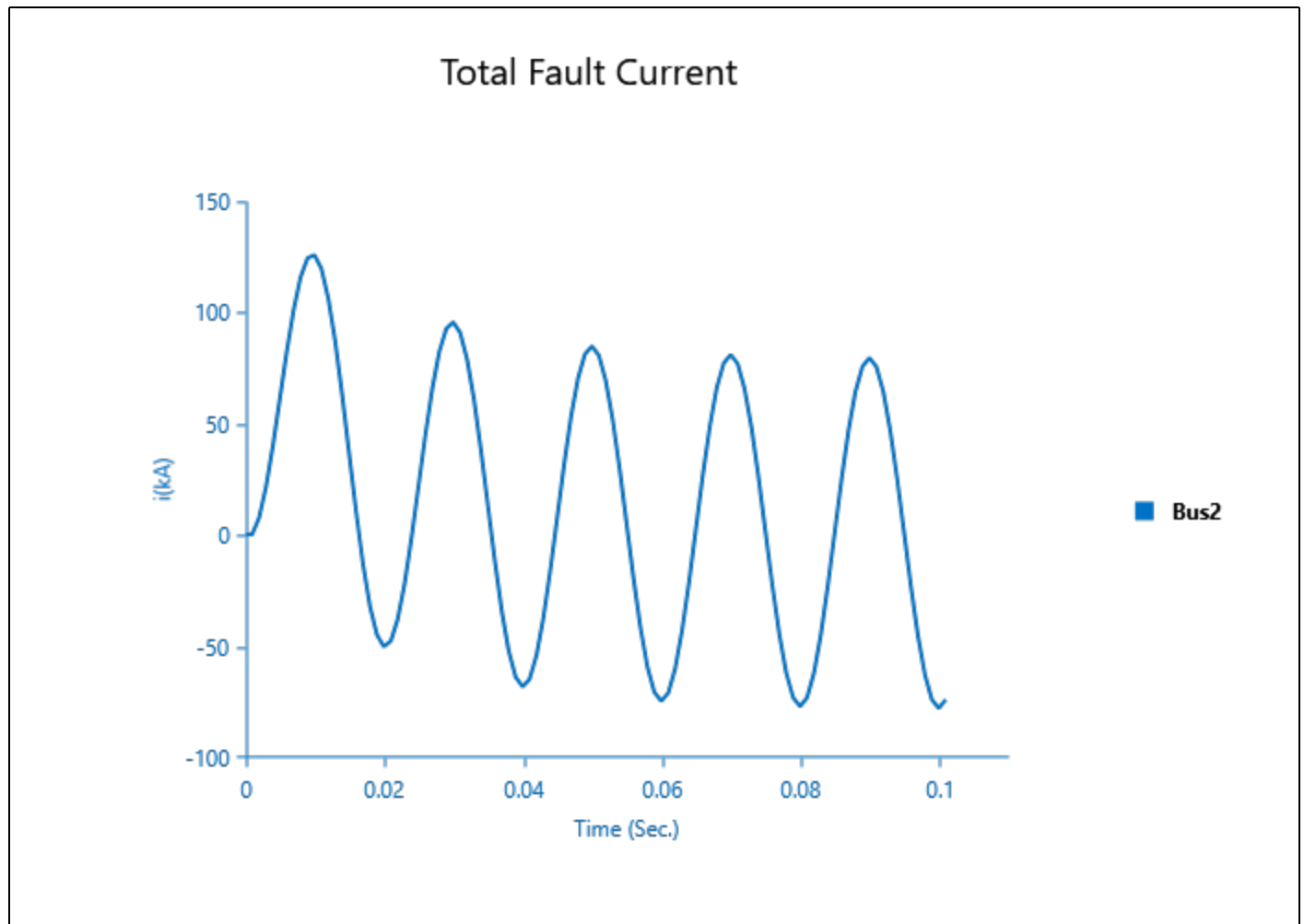
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### Short-Circuit Analysis (IEC363)





ETAP  
19.0.1C

Project:  
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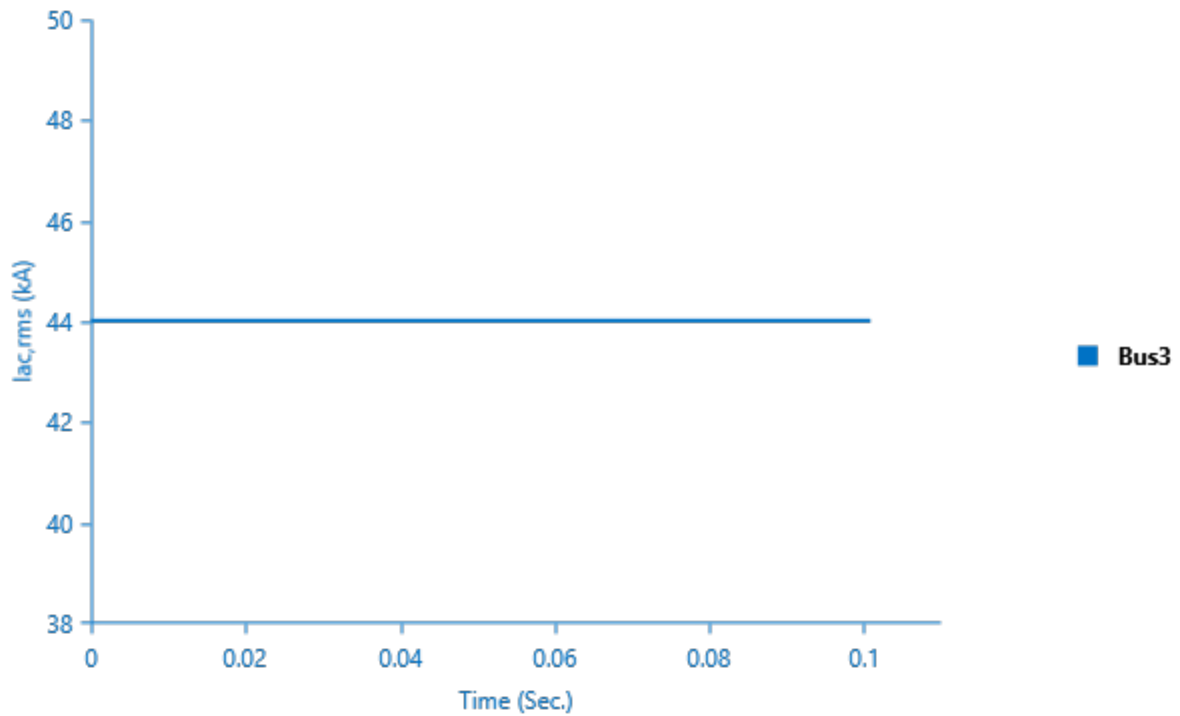
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### Short-Circuit Analysis (IEC363)

#### AC Component of Fault Current (rms)



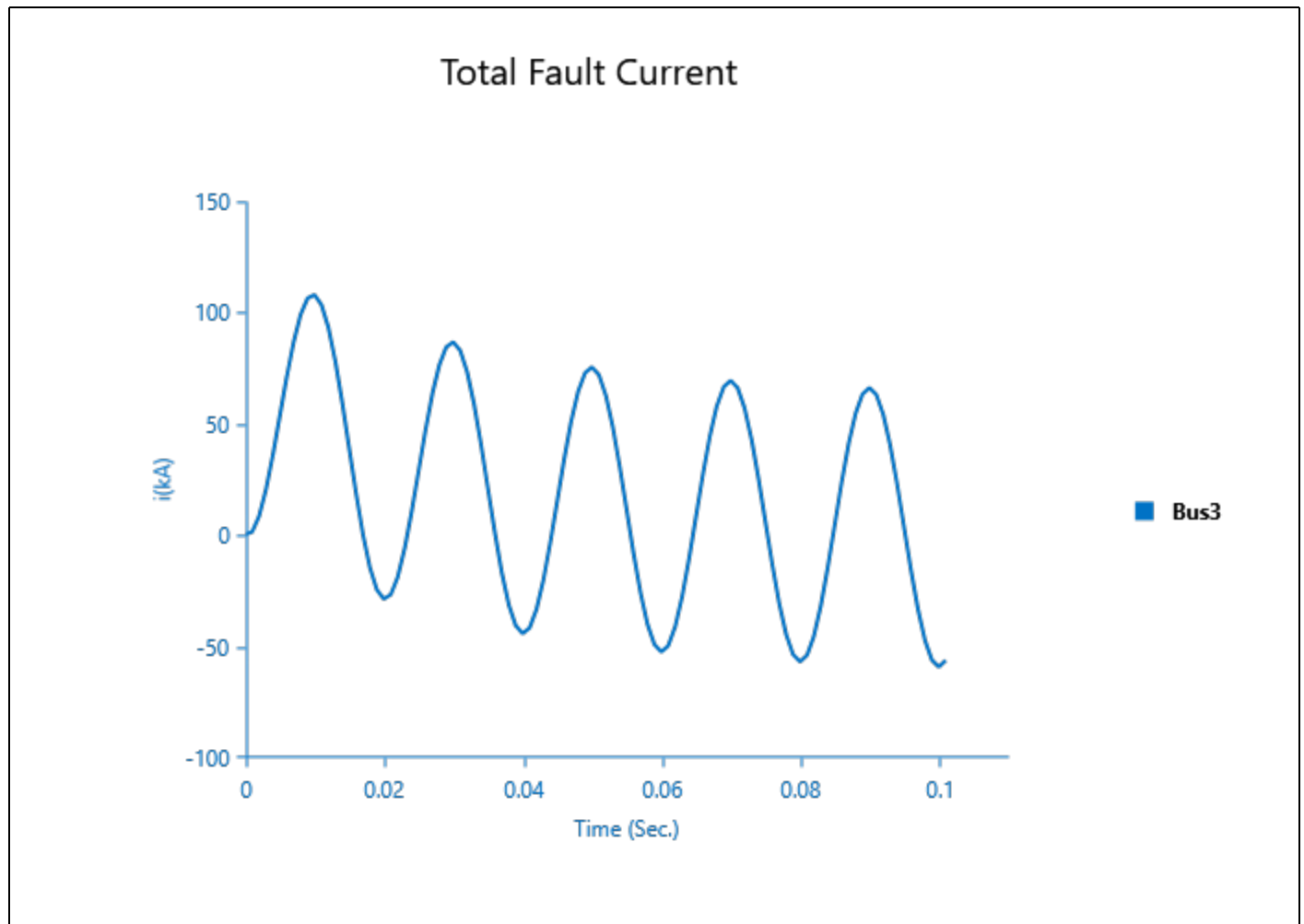
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### Short-Circuit Analysis (IEC363)



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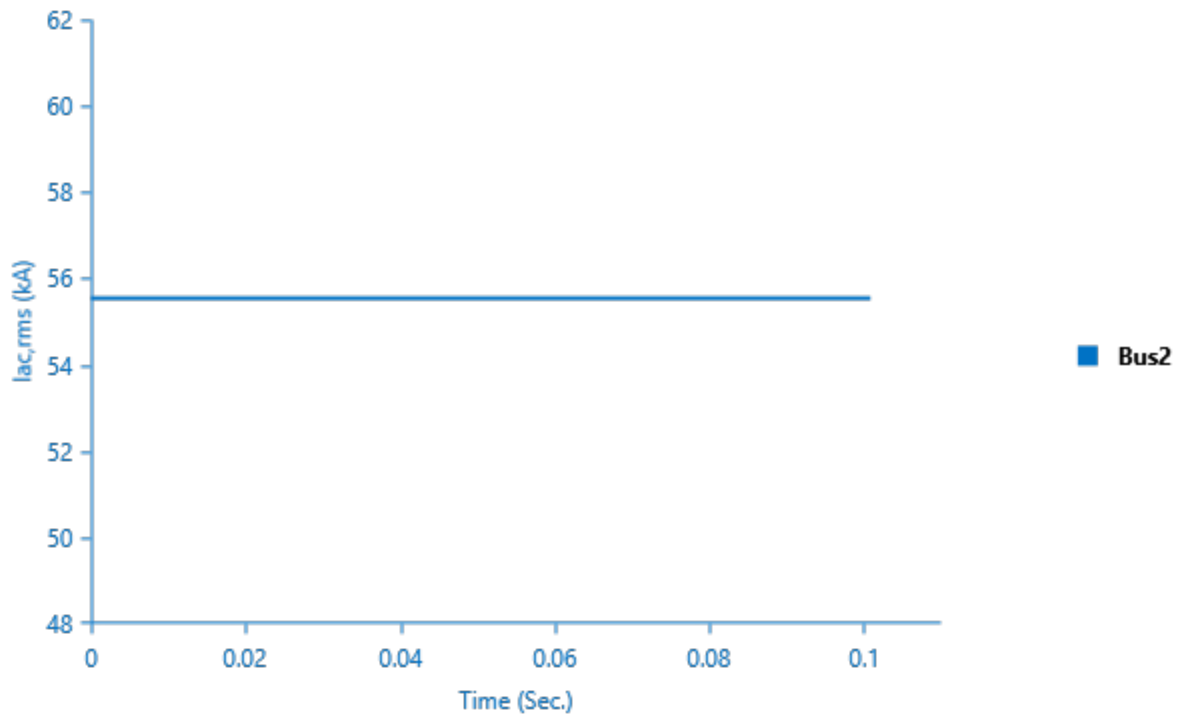
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### Short-Circuit Analysis (IEC363)

#### AC Component of Fault Current (rms)



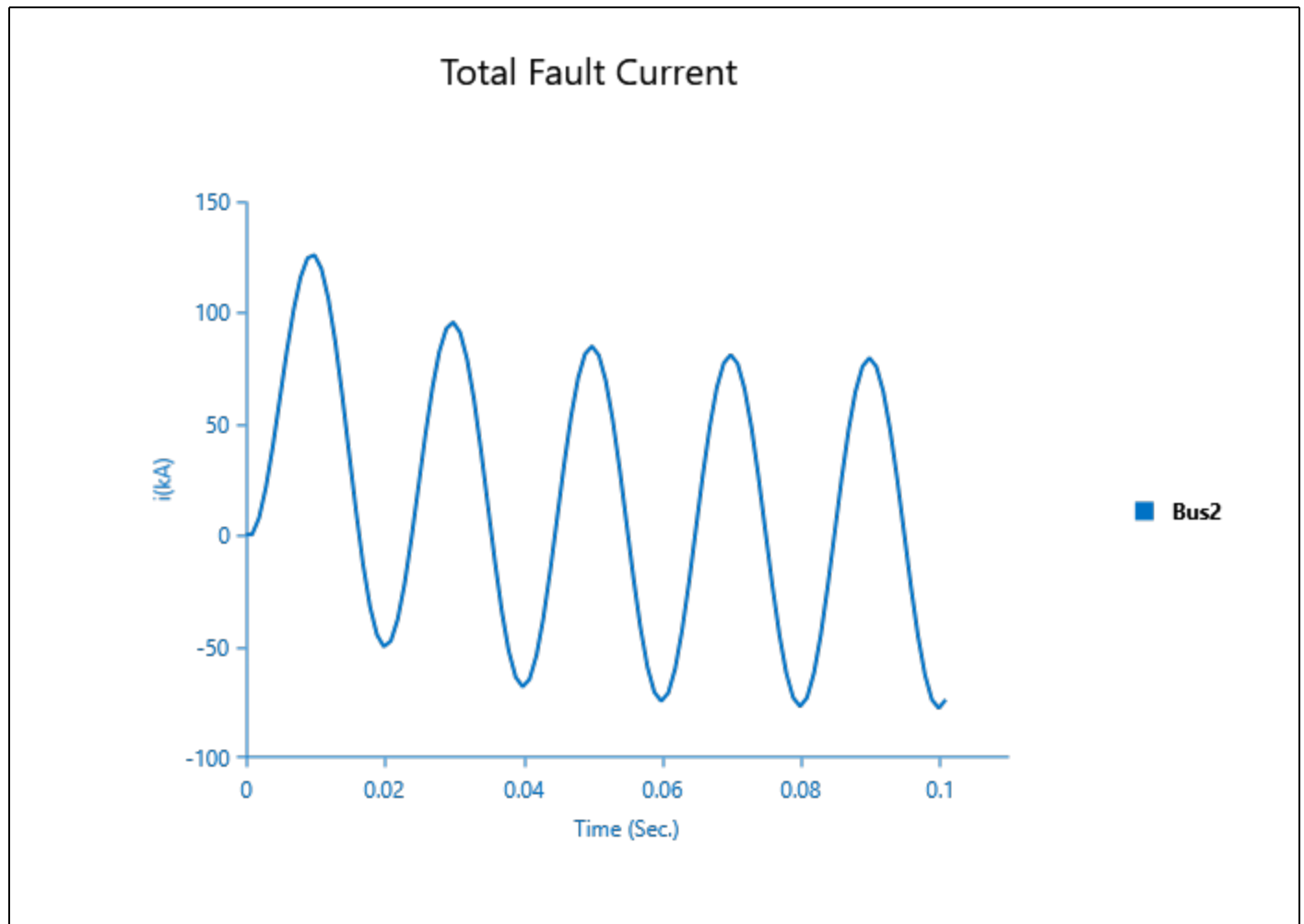
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### Short-Circuit Analysis (IEC363)



ETAP  
19.0.1C

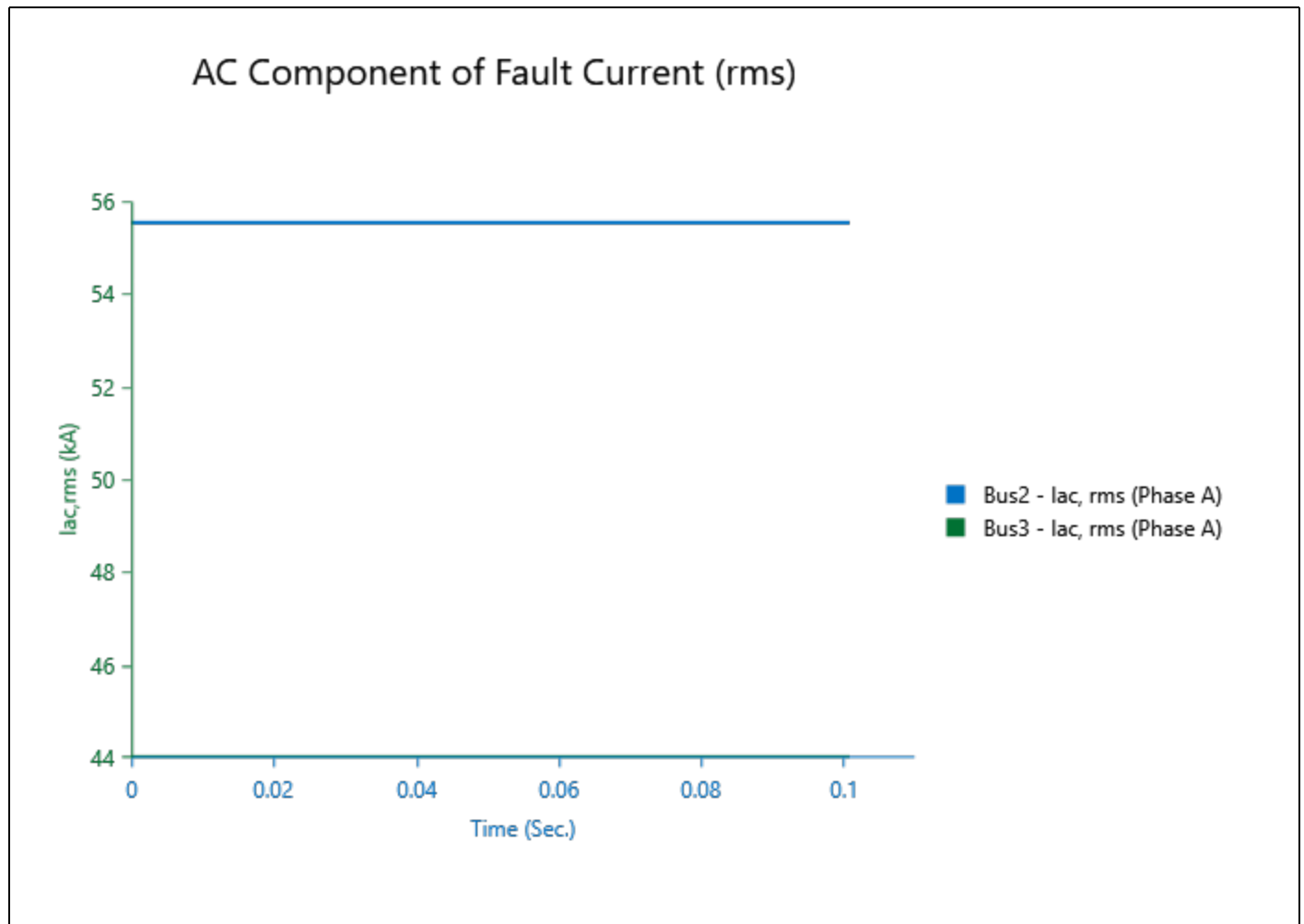
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Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

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### Short-Circuit Analysis (IEC363)



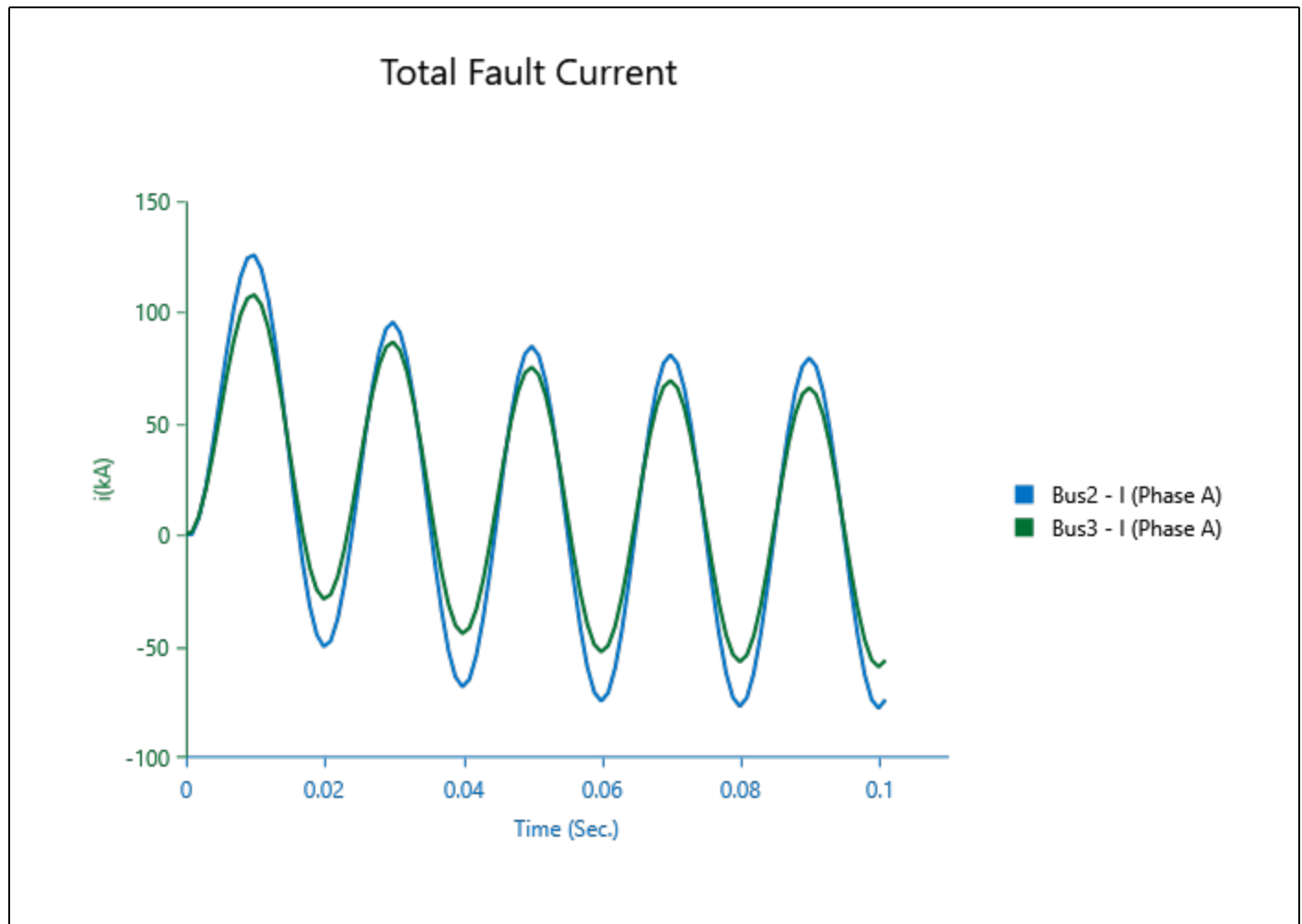
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Study Case: SC

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### Short-Circuit Analysis (IEC363)



Project:  
Location:  
Contract:  
Engineer:

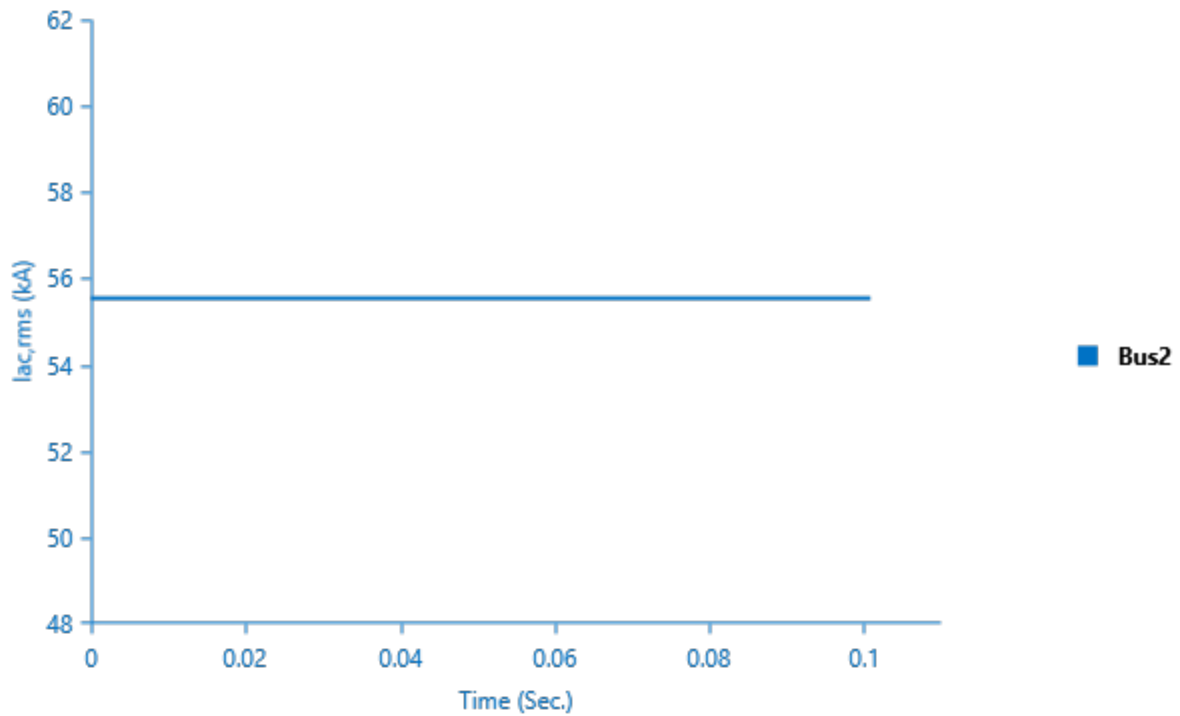
Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)

#### AC Component of Fault Current (rms)



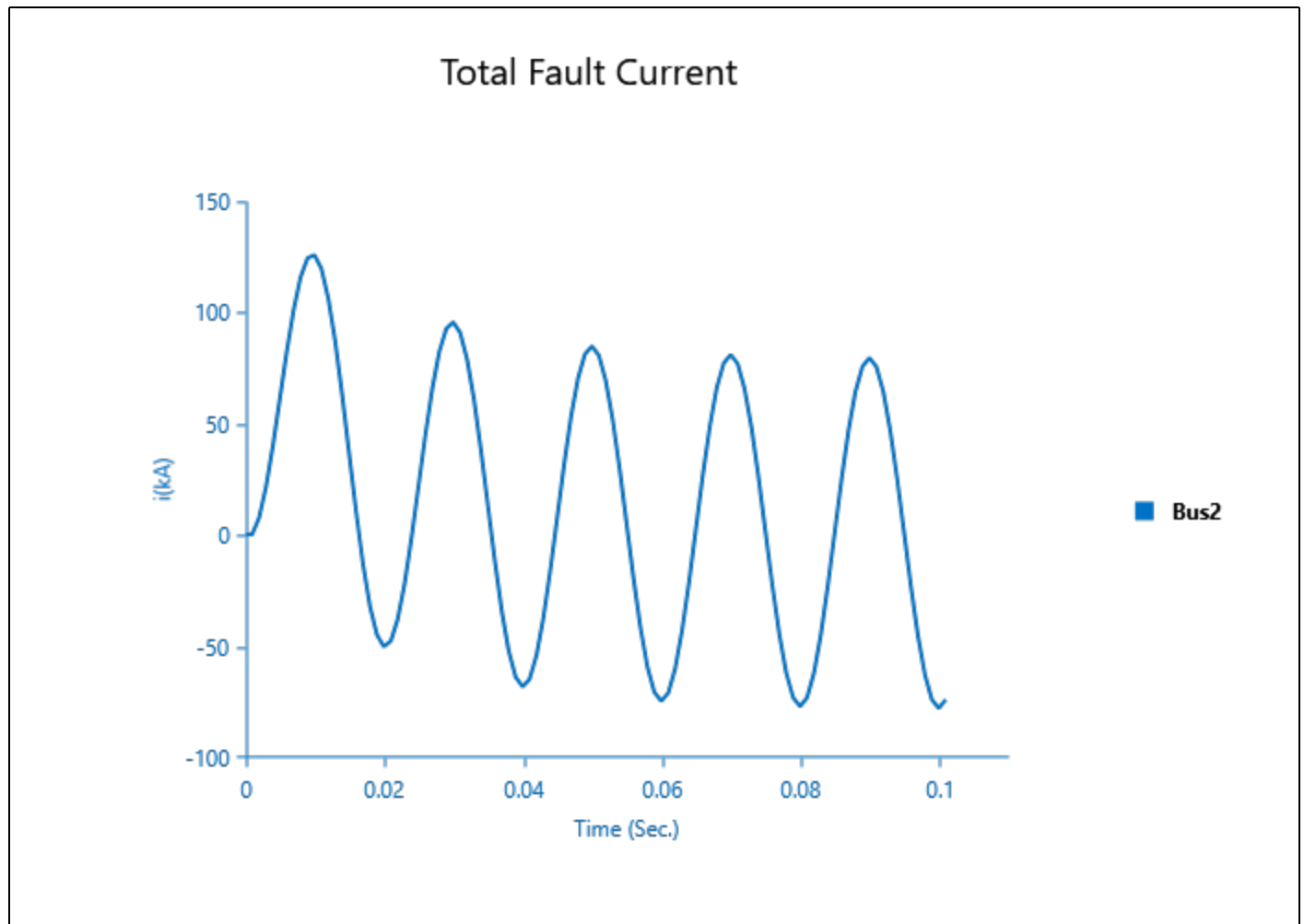
Project:  
Location:  
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Study Case: SC

Date: 7/15/2024  
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Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)





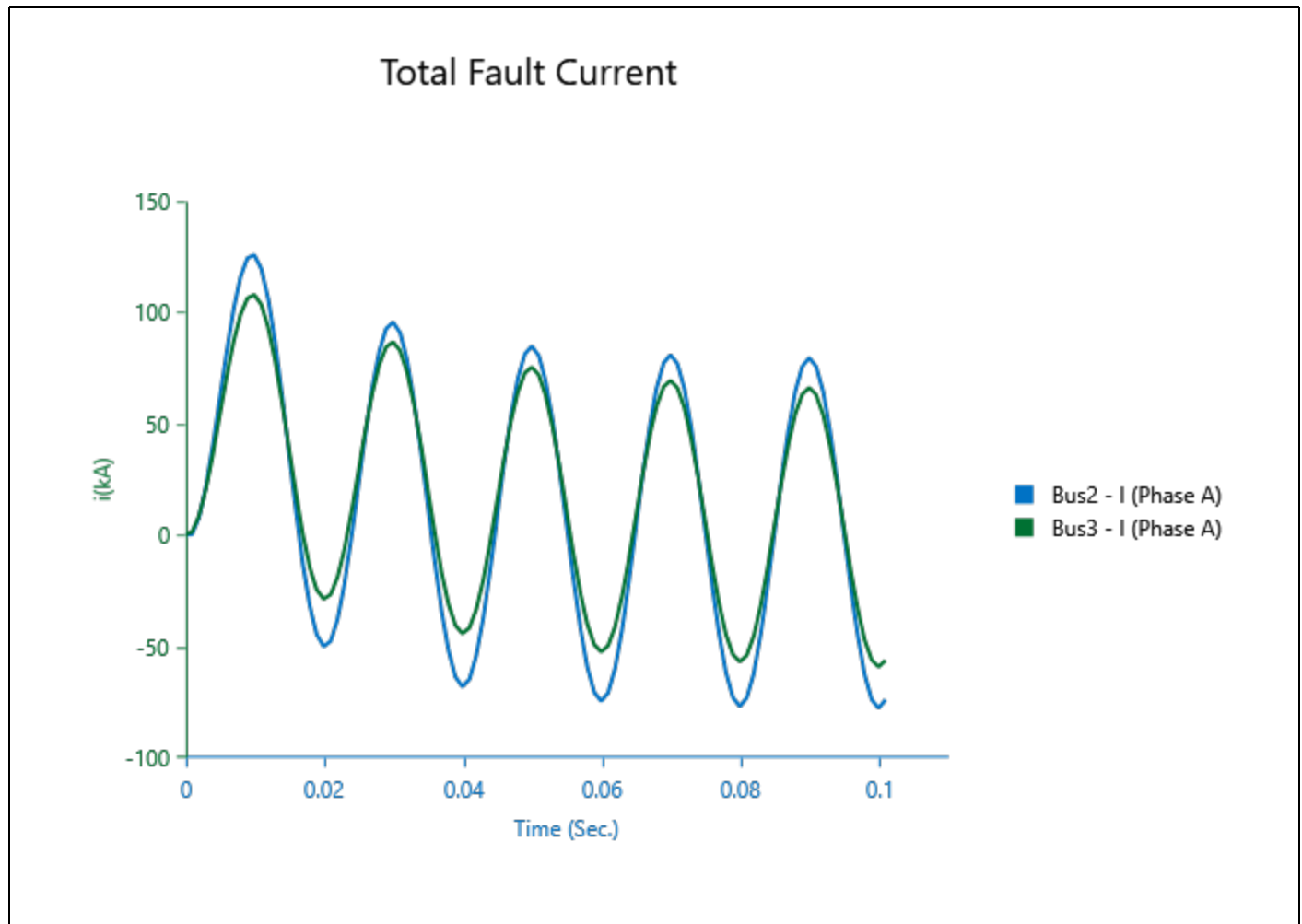
Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)



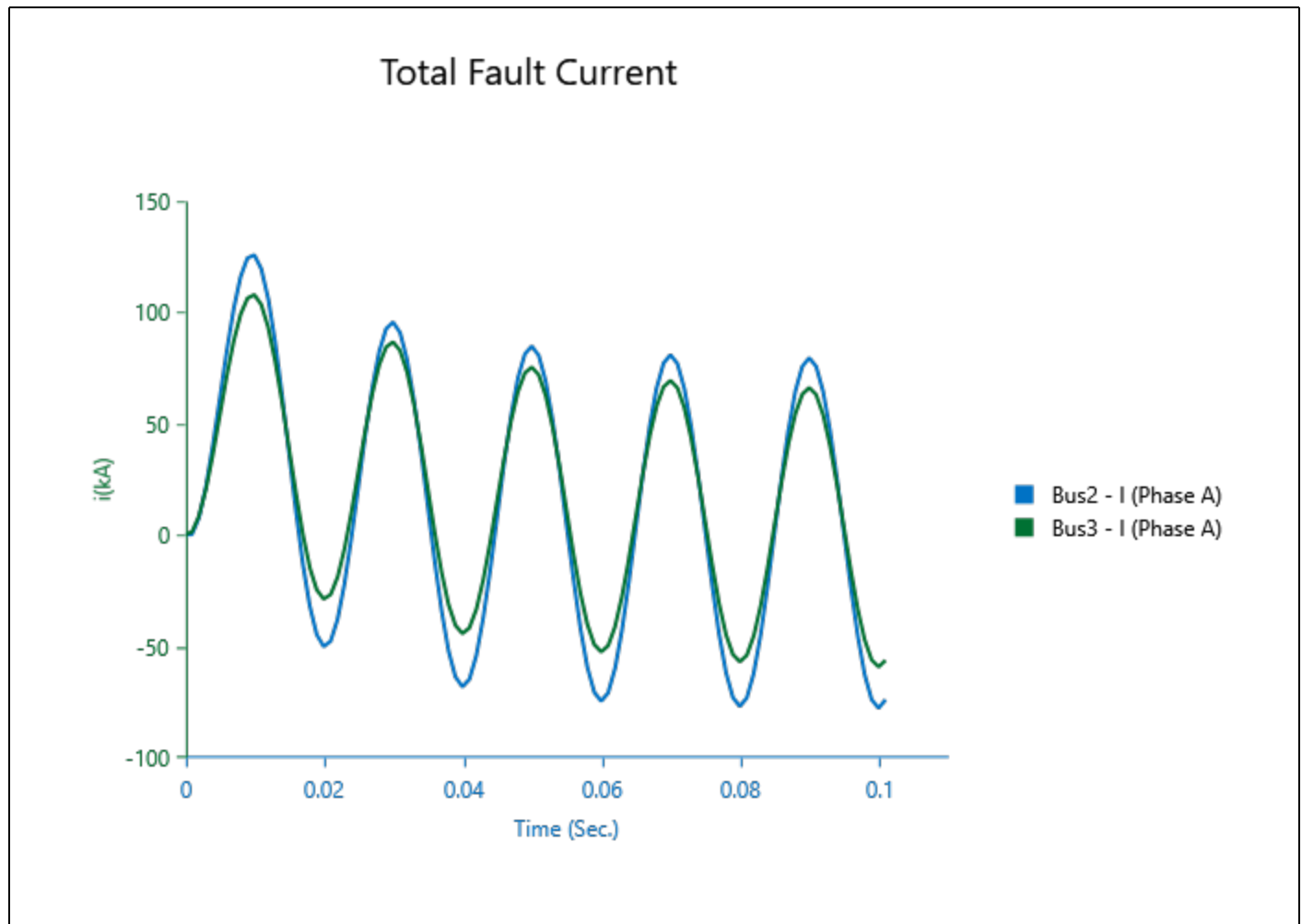
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Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)



ETAP  
19.0.1C

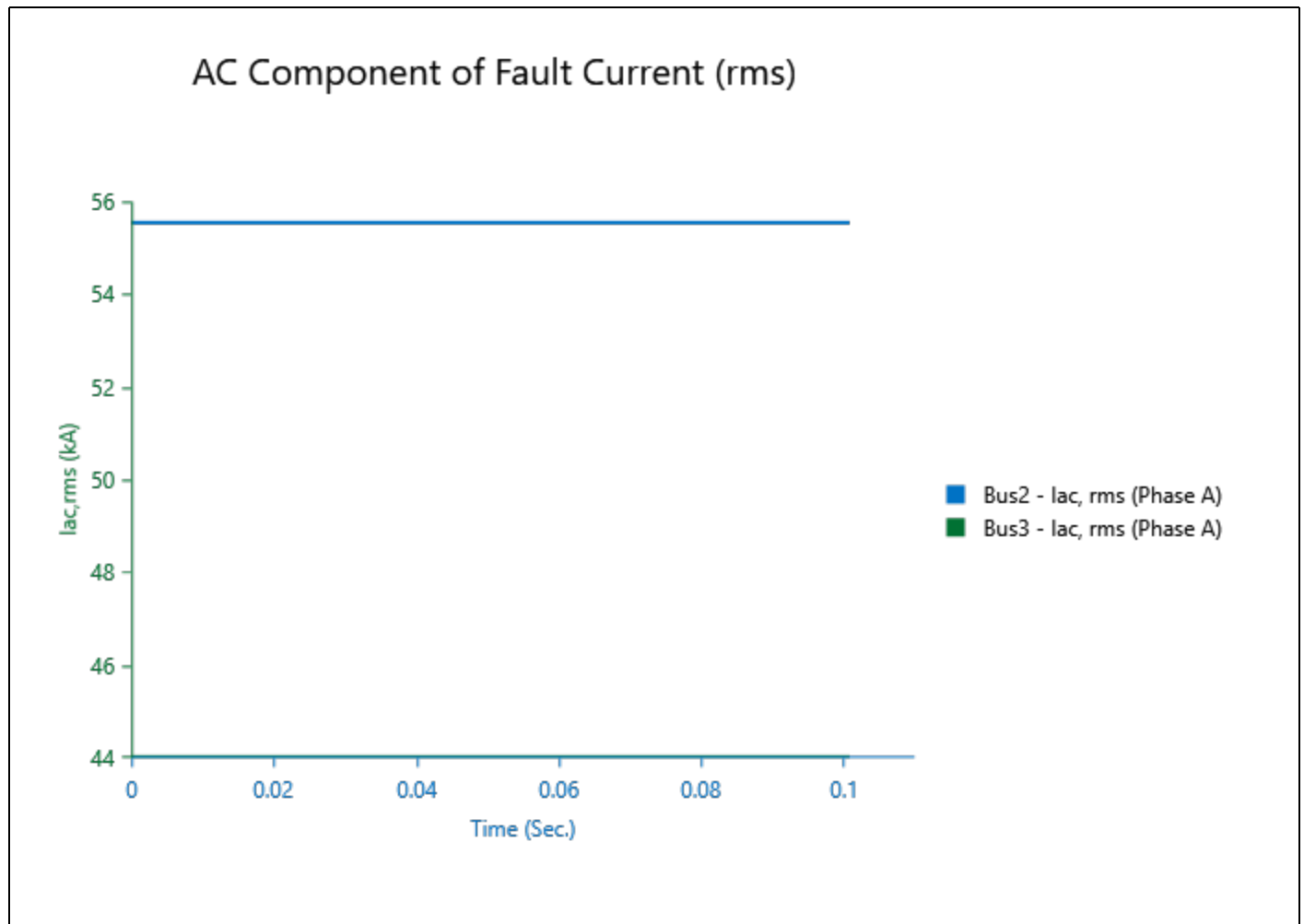
Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
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### Short-Circuit Analysis (IEC363)



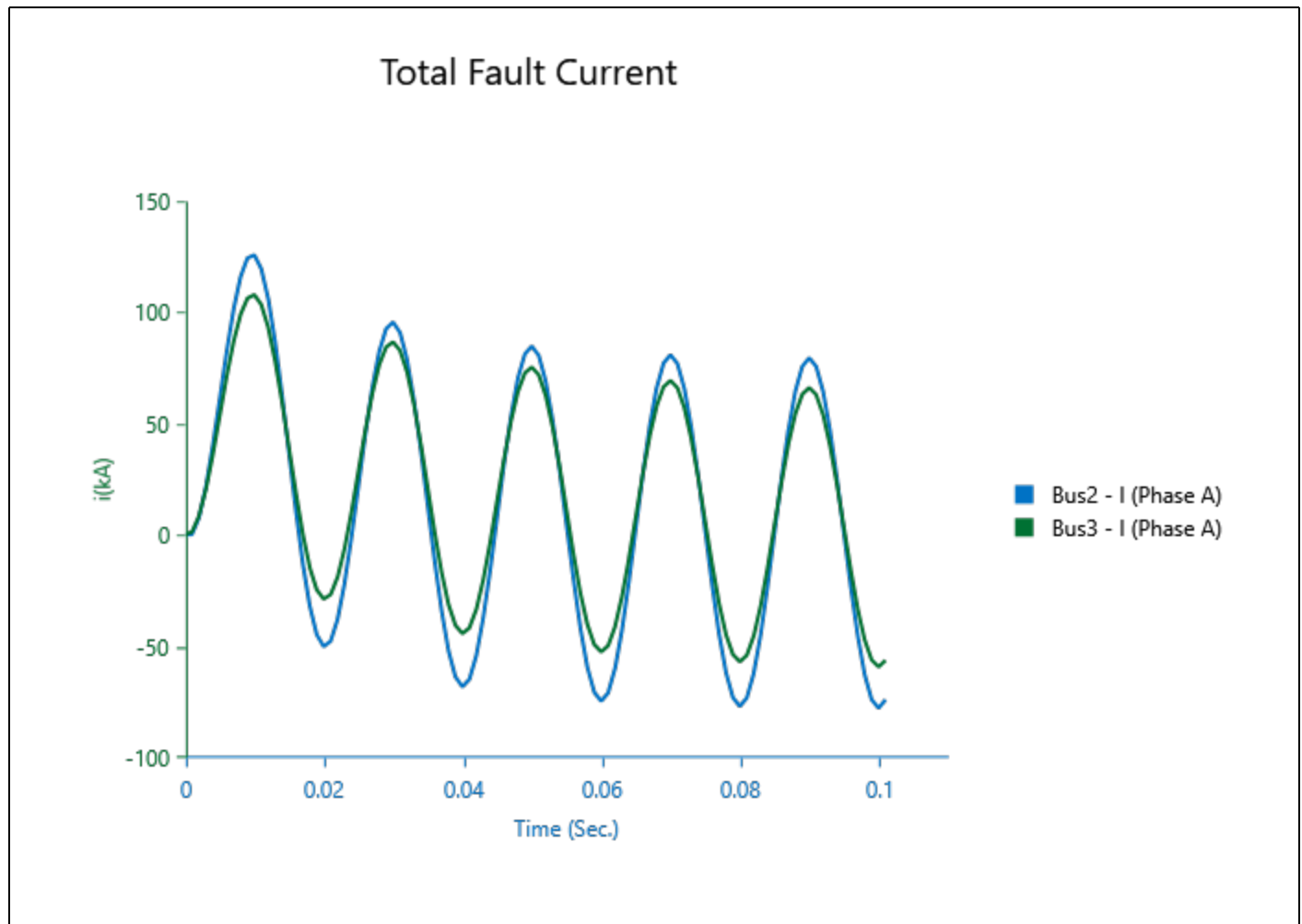
Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)



ETAP  
19.0.1C

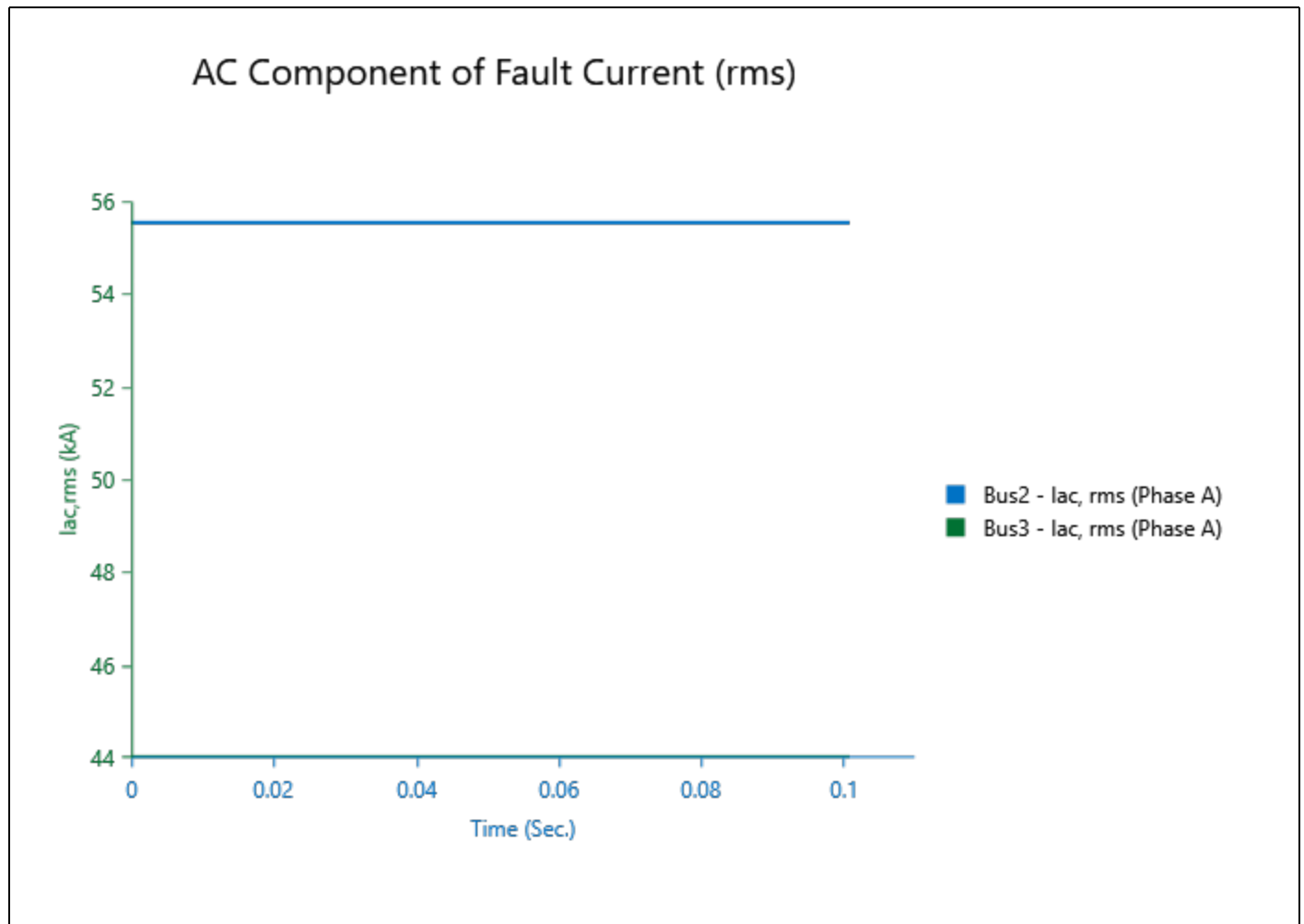
Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)



ETAP  
19.0.1C

Project:  
Location:  
Contract:  
Engineer:

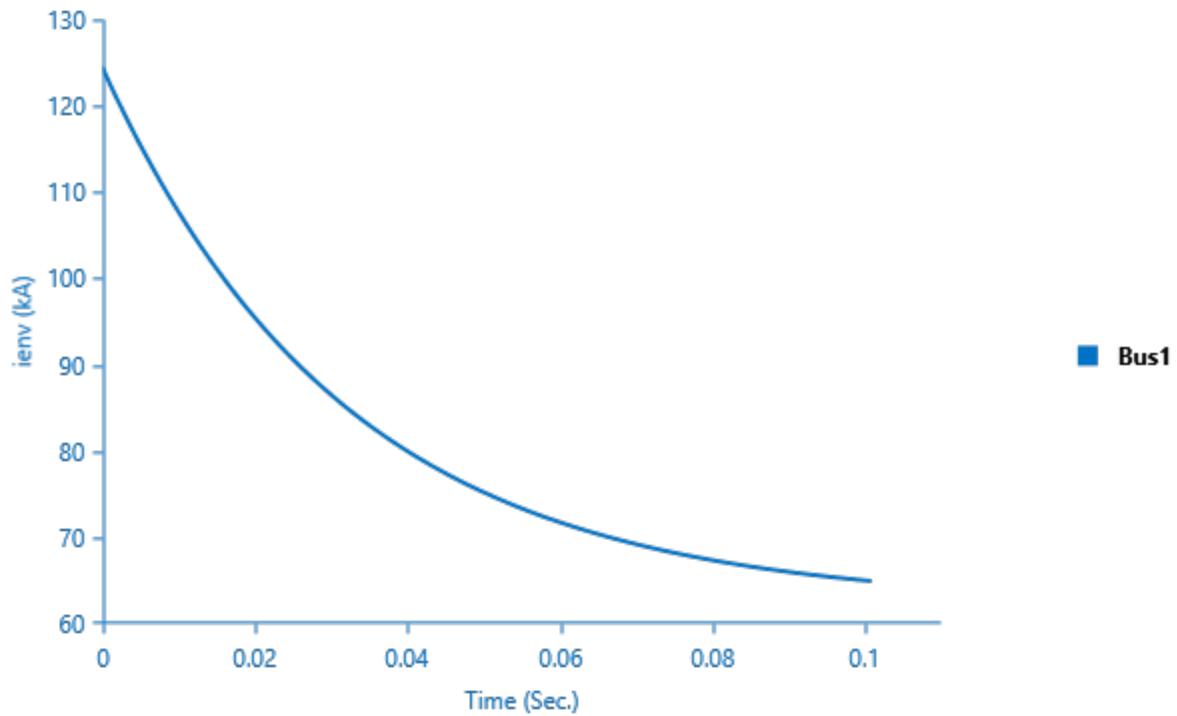
Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)

Top Envelop of Fault Current



Project:  
Location:  
Contract:  
Engineer:

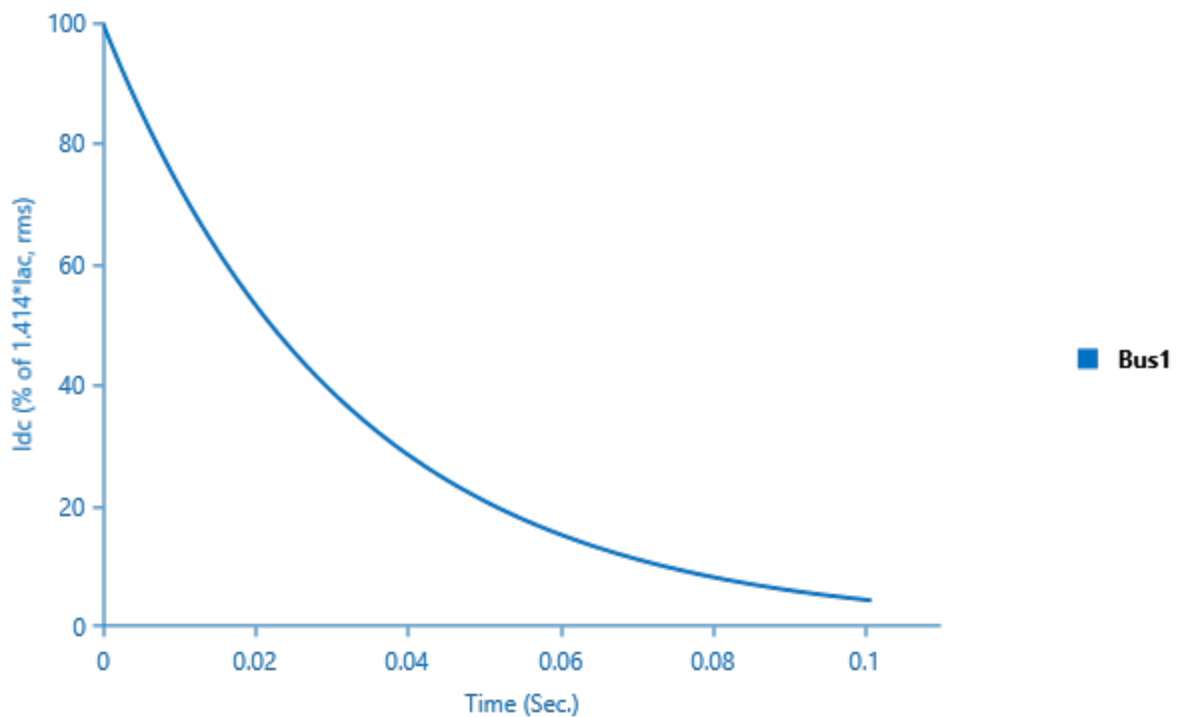
Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)

Percent DC Component of Fault Current



Project:  
Location:  
Contract:  
Engineer:

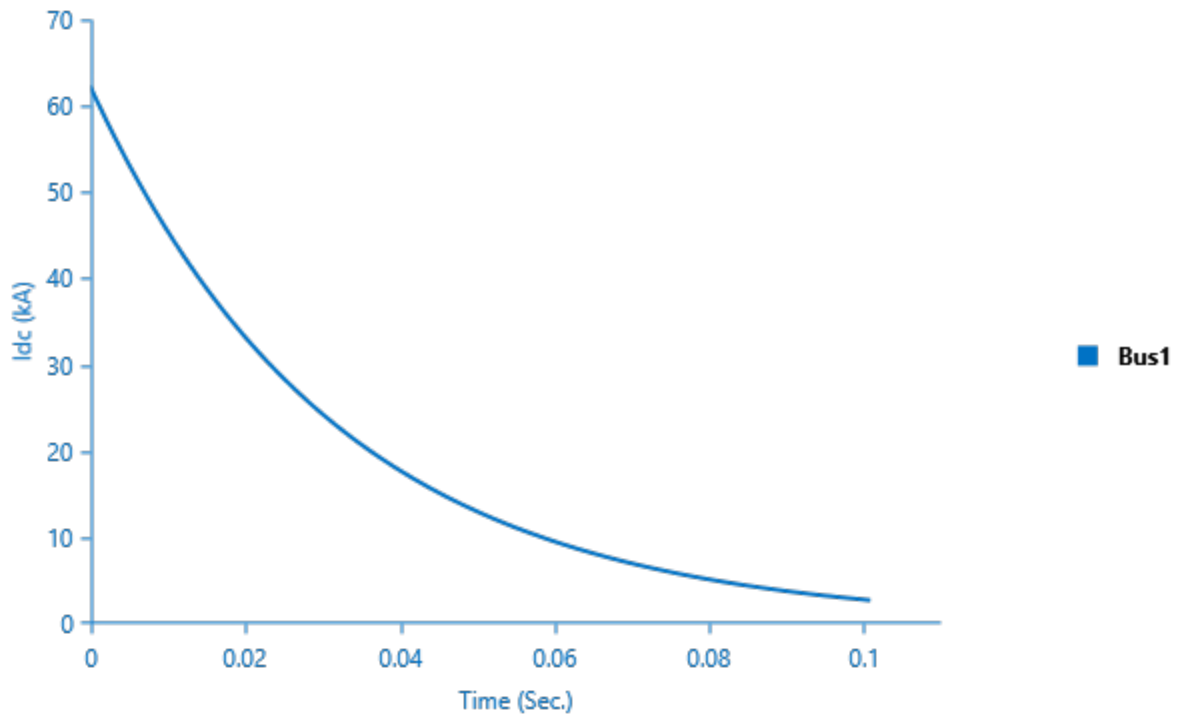
Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)

#### DC Component of Fault Current





ETAP  
19.0.1C

Project:  
Location:  
Contract:  
Engineer:

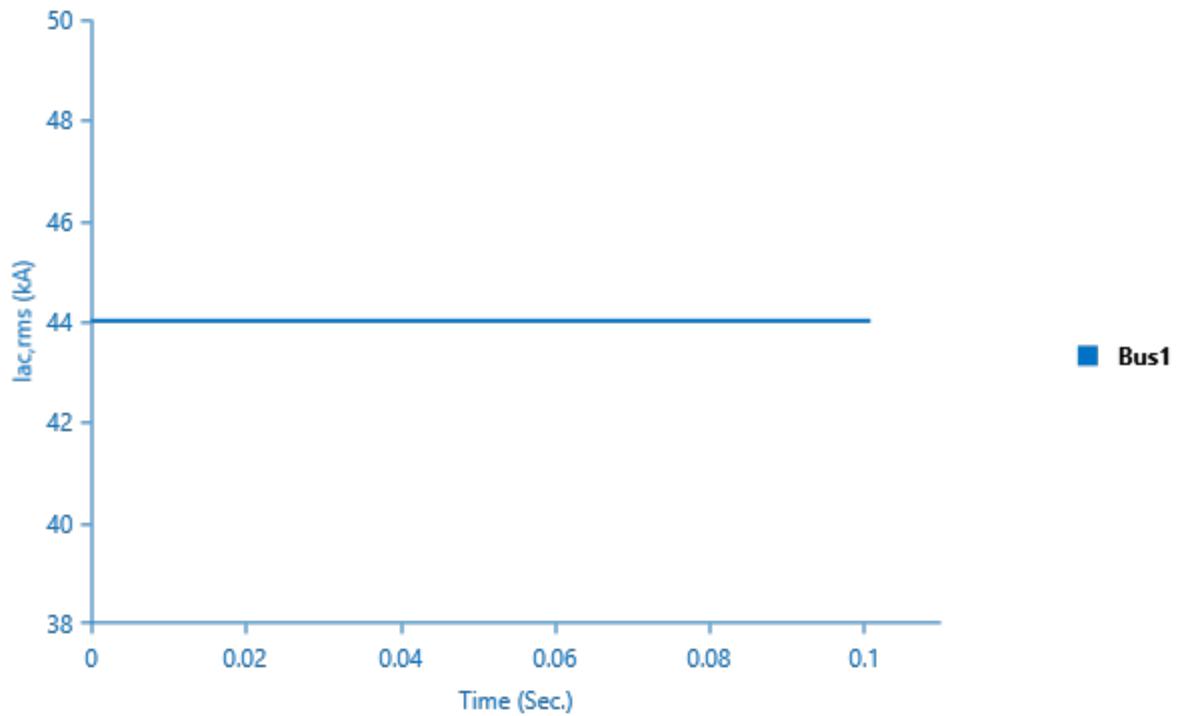
Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
Output Report: C:\ETAP 1901\mustafaday5\Untitled.sip

### Short-Circuit Analysis (IEC363)

#### AC Component of Fault Current (rms)



Project:  
Location:  
Contract:  
Engineer:

Study Case: SC

Date: 7/15/2024  
SN:  
Revision: Base  
Config: Normal

Project File: C:\ETAP 1901\mustafaday5  
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### Short-Circuit Analysis (IEC363)

