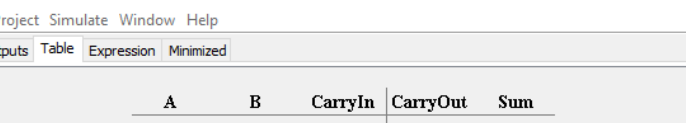
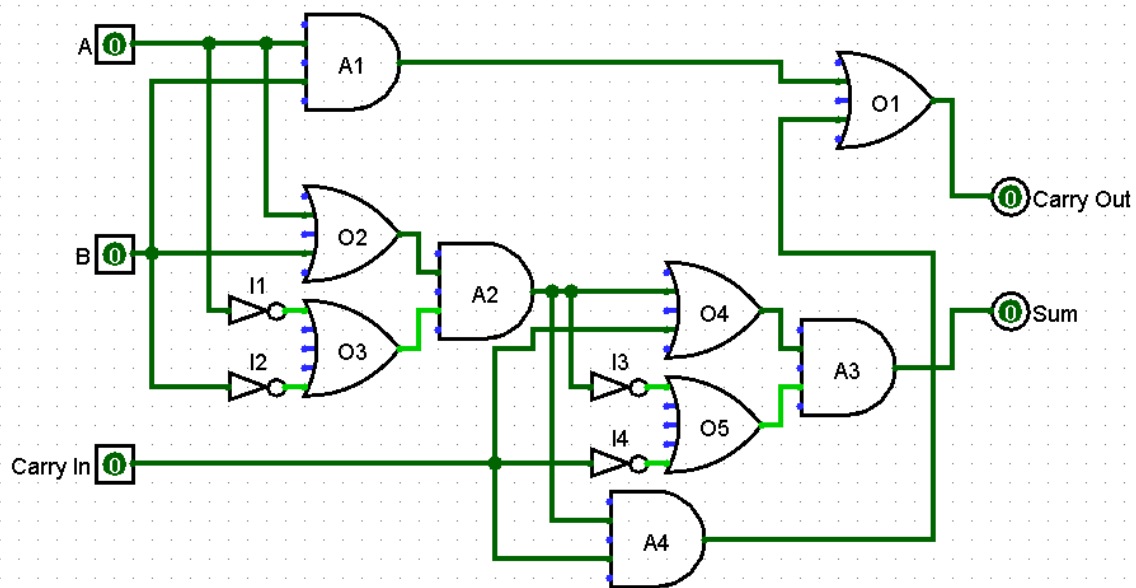


## Project: Full Adder Circuit

By Max Edward

**Part A: Design a Full Adder circuit in Logisim.**



Combinational Analysis

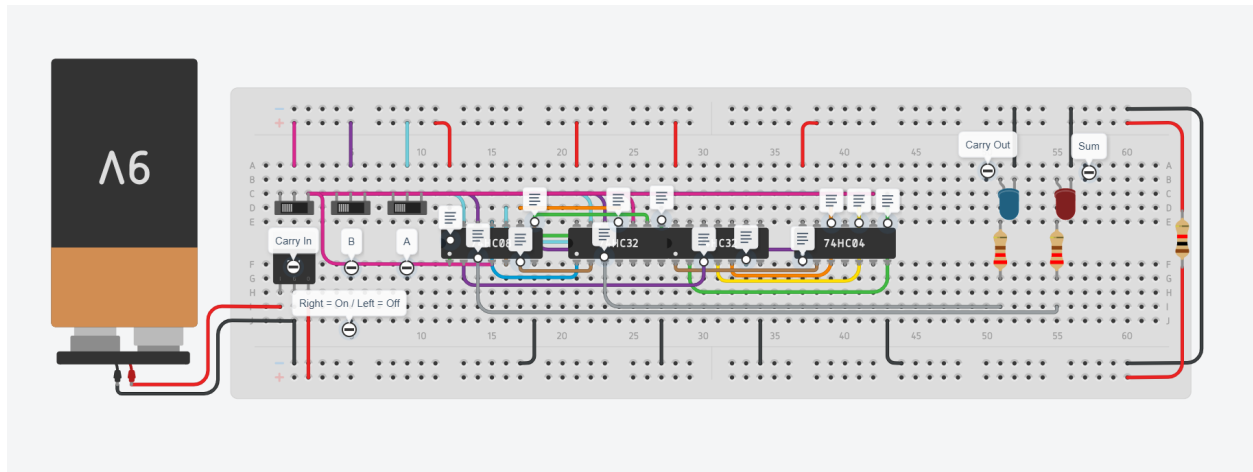
File Edit Project Simulate Window Help

Inputs Outputs Table Expression Minimized

| A | B | CarryIn | CarryOut | Sum |
|---|---|---------|----------|-----|
| 0 | 0 | 0       | 0        | 0   |
| 0 | 0 | 1       | 0        | 1   |
| 0 | 1 | 0       | 0        | 1   |
| 0 | 1 | 1       | 1        | 0   |
| 1 | 0 | 0       | 0        | 1   |
| 1 | 0 | 1       | 1        | 0   |
| 1 | 1 | 0       | 1        | 0   |
| 1 | 1 | 1       | 1        | 1   |

Build Circuit

**Part B: Construct the Full Adder circuit on a breadboard using the Full Adder circuit you designed in Logisim.**



**Also recorded a video of it in action, see attachment.**