KIT_T2G-B-H_LITE_REV-01

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PCBA	
PCB	
FAB DRW	
ASSY DRW	
SCH DRW	

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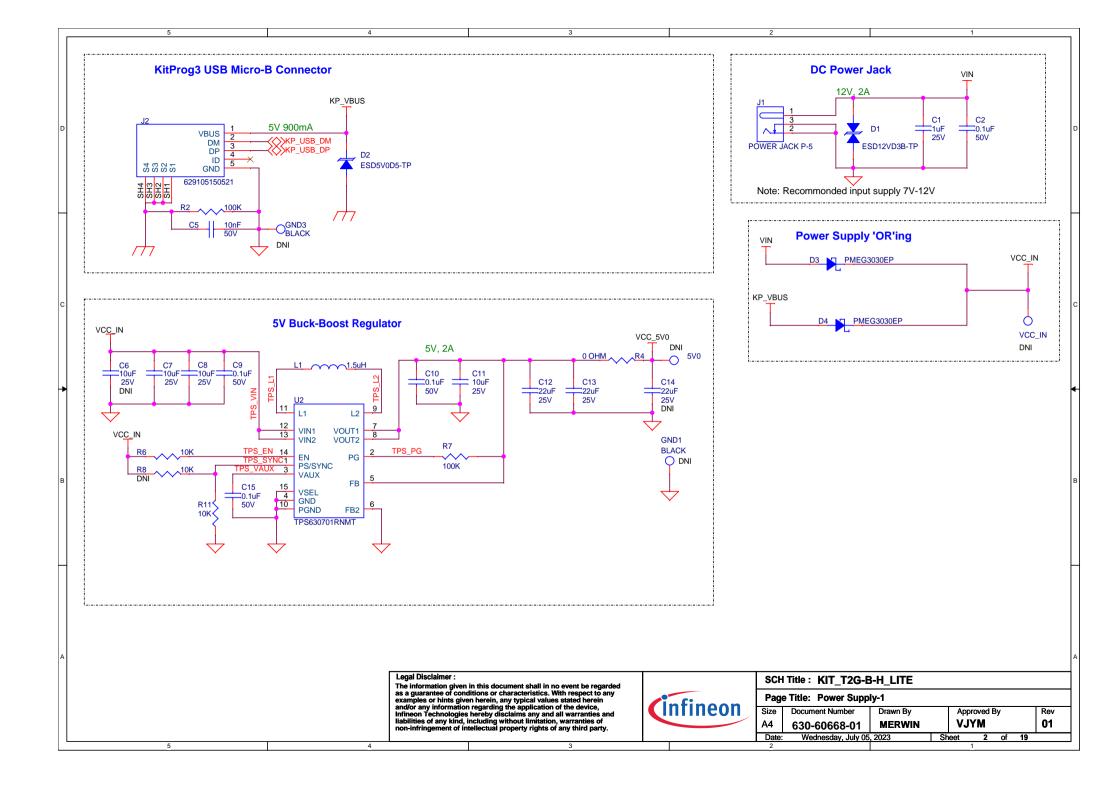
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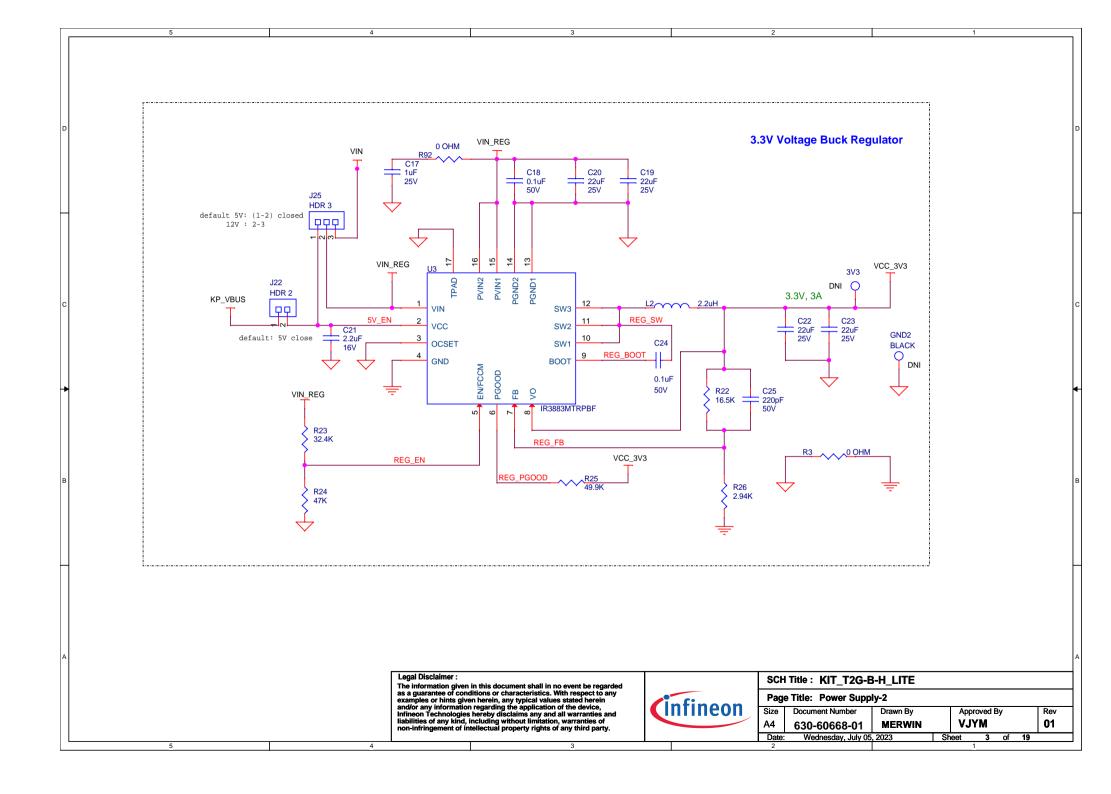


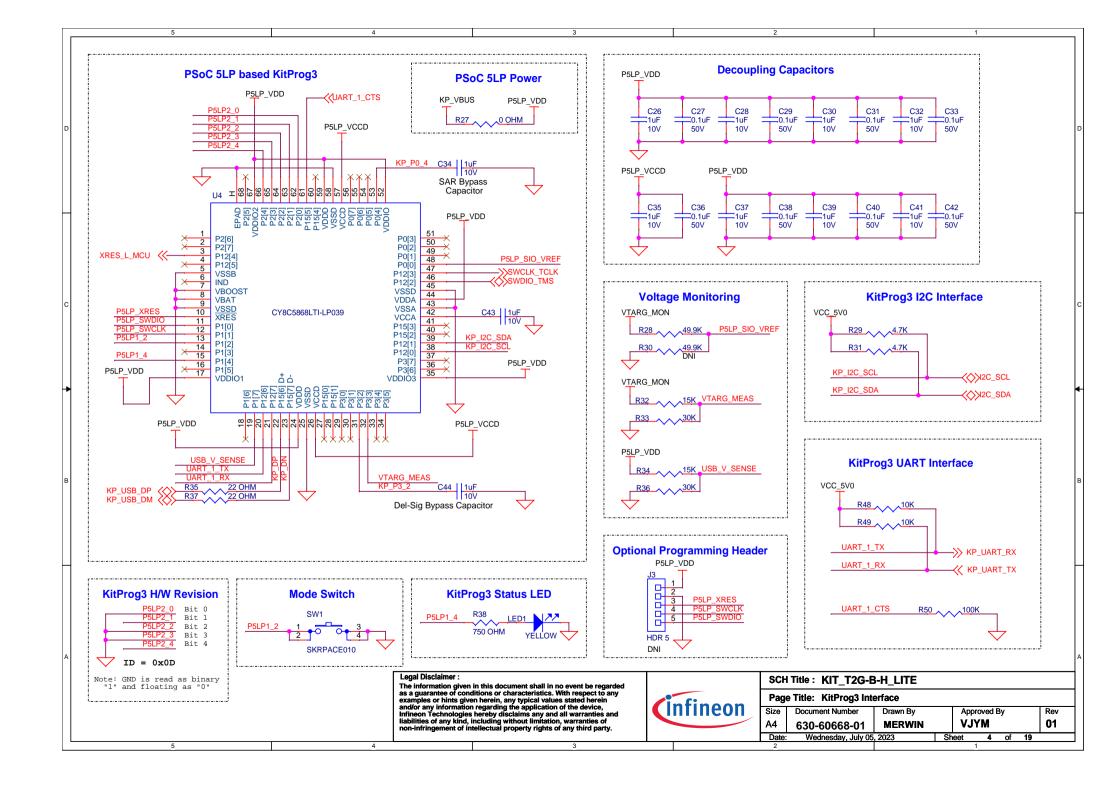
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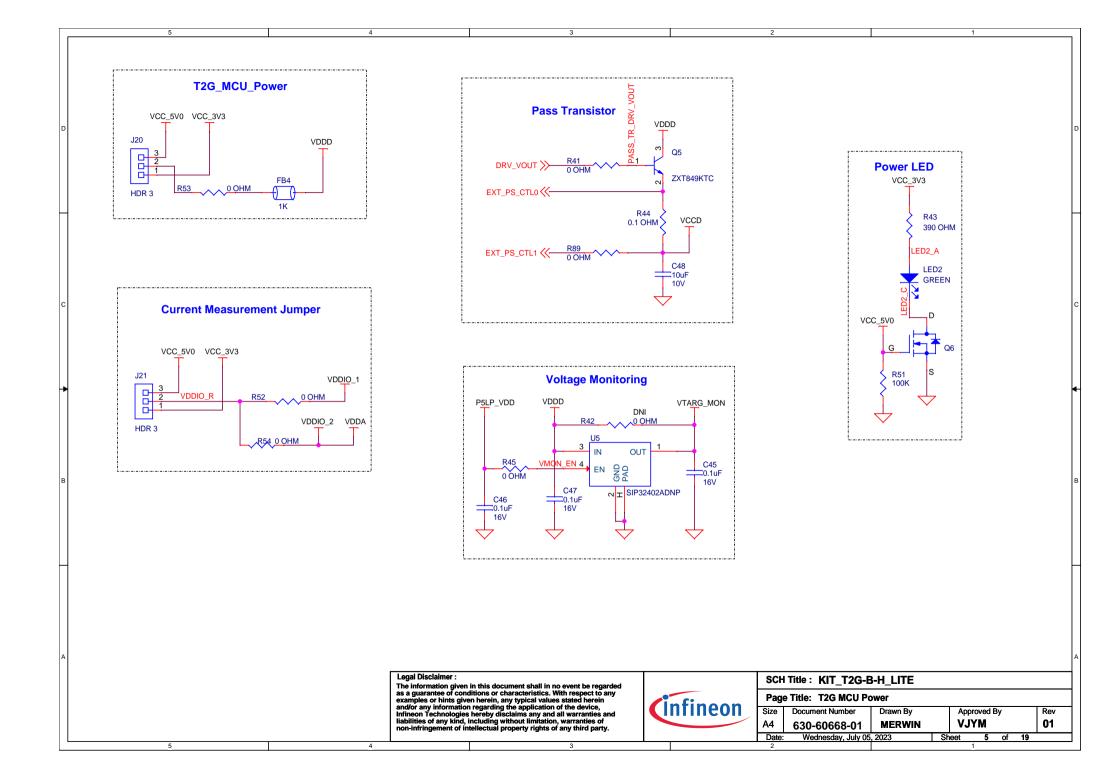
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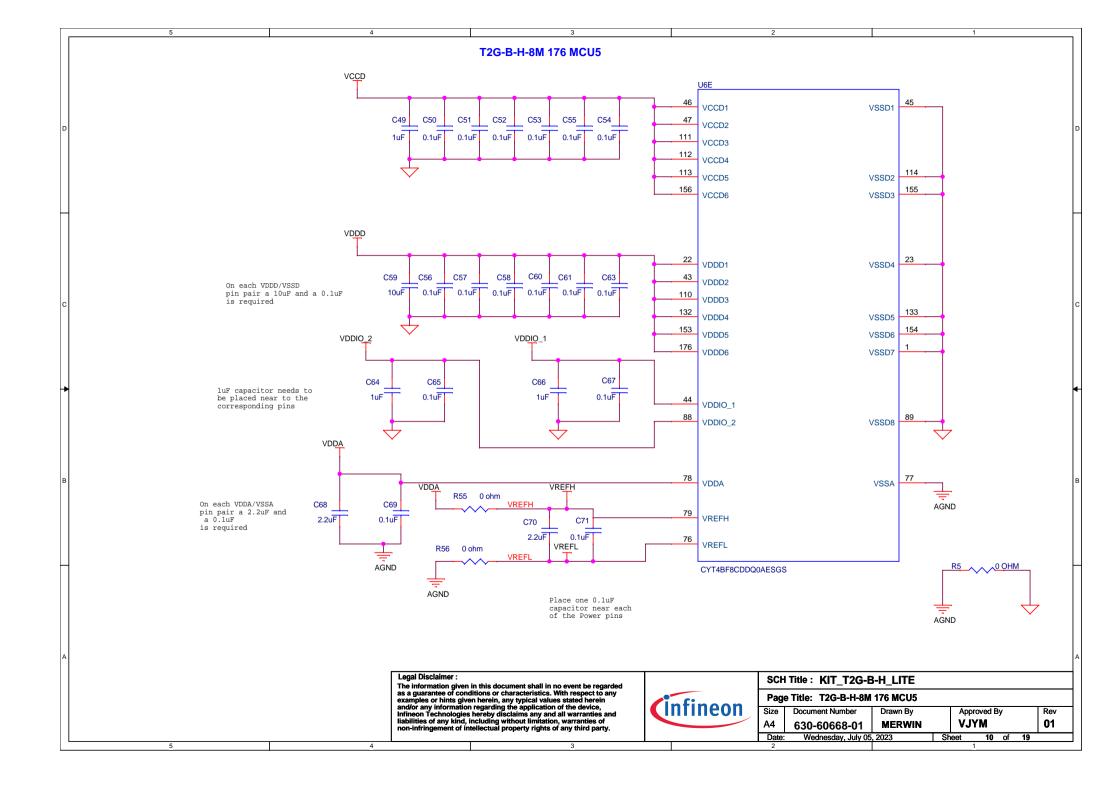
T2G-B-H-8M 176 MCU1 U6A KP UART RX P0_0/PWM1_18/PWM1_22_N/TC1_18_TR0/TC1_22_TR1/PWM0_H_0/SCB0_RX/SCB7_SDA/SCB0_MISO/LIN1_RX KP UART TX P0_1/PWM1_17/PWM1_18_N/TC1_17_TR0/TC1_18_TR1/PWM0_H_0_N/SCB0_TX/SCB7_SCL/SCB0_MOSI/LIN1_TX PO 2/PWM1 14/PWM1 17 N/TC1 14 TR0/TC1 17 TR1/TC0 H 0 TR0/SCB0 RTS/SCB0 SCL/SCB4 MISO/LIN1 EN/CAN0 1 TX P0 3/PWM1 13/PWM1 14 NTC1 13 TR0/TC1 14 TR1/TC0 H 0 TR1/SCB0 CTS/SCB0 SDA/SCB0 SEL0/SCB4 MOSI/CAN0 1 RX P1_0/PWM1_12/PWM1_13_N/TC1_12_TR0/TC1_13_TR1/PWM1_H_4/SCB0_SCL/SCB0_MISO/SCB4_CLK P1_1/PWM1_11/PWM1_12_N/TC1_11_TR0/TC1_12_TR1/PWM1_H_5/SCB0_SDA/SCB0_MOSI/SCB4_SEL0 P1_2/PWM1_10/PWM1_11_N/TC1_10_TR0/TC1_11_TR1/PWM1_H_6/SCB0_CLK/LIN0_RX/TRIG_IN[0] P1 3/PWM1 8/PWM1 10 N/TC1 8 TR0/TC1 10 TR1/PWM1 H 7/SCB0 SEL0/LIN0 TX/TRIG IN[1] P2_0/PWM1_7/PWM1_8_N/TC1_7_TR0/TC1_8_TR1/TC1_H_4_TR0/SCB7_RX/SCB0_SEL1/SCB7_MISO/LIN0_RX/CAN0_0_TX/SWJ_TRSTN/TRIG_IN[2] P2 0 P2 1/PWM1 6/PWM1 7 N/TC1 6 TR0/TC1 7 TR1/TC1 H 5 TR0/SCB7 TX/SCB7 SDA/SCB0 SEL2/SCB7 MOSI/LIN0 TX/CAN0 0 RX/TRIG INI31 P2_2/PWM1_5/PWM1_6_N/TC1_5_TR0/TC1_6_TR1/ETH0_RX_ER/TC1_H_6_TR0/SCB7_RT5/SCB7_SCL/SCB0_SEL3/SCB7_CLK/LIN0_EN/TRIG_IN[4] P2 3/PWM1 4/PWM1 5 N/TC1 4 TR0/TC1 5 TR1/ETH0 ETH TSU TIMER CMP VAL/TC1 H 7 TR0/SCB7 CTS/SCB7 SEL0/LIN5 RX/TRIG IN[5] P2 4/PWM1 3/PWM1 4 N/TC1 3 TR0/TC1 4 TR1/PWM1 H 4 N/SCB7 SEL1/LIN5 TX/TRIG INI61 P2_5/PWM1_2/PWM1_3_N/TC1_2_TR0/TC1_3_TR1/PWM1_H_5 N/SCB7 SEL2/LIN5 EN/TRIG INI7 ETH MDIO P3 0/PWM1 1/PWM1 2 N/TC1 1 TR0/TC1 2 TR1/ETH0 MDIO/PWM1 H 6 N/SCB6 RX/SCB6 MISO/CAN0 3 TX/TRIG DBG[0] ETH MDC P3_1/PWM1_0/PWM1_1_N/TC1_0_TR0/TC1_1_TR1/ETH0_MDC/PWM1_H_7_N/SCB6_TX/SCB6_SDA/SCB6_MOSI/CAN0_3_RX/TRIG_DBG[1] P3_2/PWM1_M_3/PWM1_0_N/TC1_M_3_TR0/TC1_0_TR1/TC1_H_4_TR1/SCB6_RTS/SCB6_SCL/SCB6_CLK P3_3/PWM1_M_2/PWM1_M_3_N/TC1_M_2_TR0/TC1_M_3_TR1/TC1_H_5_TR1/SCB6_CTS/SCB6_SEL0 P3 4/PWM1 M 1/PWM1 M 2 N/TC1 M 1 TR0/TC1 M 2 TR1/TC1 H 6 TR1/SCB6 SEL1/LIN1 RX P3 5/PWM1 M 0/PWM1 M 1 N/TC1 M 0 TR0/TC1 M 1 TR1/TC1 H 7 TR1/SCB6 SEL2/LIN1 TX P4_0/PWM1_4/PWM1_M_0_N/TC1_4_TR0/TC1_M_0_TR1/EXT_MUX[0]_0/SCB5_RX/SCB5_MISO/LIN1_RX/TRIG_IN[10] P4_1/PWM1_5/PWM1_4_N/TC1_5_TR0/TC1_4_TR1/EXT_MUX[0]_1/\$CB5_TX/\$CB5_SDA/\$CB5_MOSI/LIN1_TX/TRIG_IN[11] P4_2/PWM1_6/PWM1_5_N/TC1_6_TR0/TC1_5_TR1/EXT_MUX[0]_2/SCB5_RTS/SCB5_SCL/SCB5_CLK/LIN1_EN/TRIG_IN[12] P4_3/PWM1_7/PWM1_6_N/TC1_7_TR0/TC1_6_TR1/EXT_MUX[0]_EN/SCB5_CTS/SCB5_SEL0/CAN0_1_TX/TRIG_IN[13] P4_4/PWM1_8/PWM1_7_N/TC1_8_TR0/TC1_7_TR1/LIN15_RX/SCB5_SEL1/CAN0_1_RX USER_LED1 P5 0/PWM1 9/PWM1 8 N/TC1 9 TR0/TC1 8 TR1/PWM0 M 0/PWM1 H 10/LIN15 TX/SCB5 SEL2/LIN7 RX/TRIG IN[38] USER LED2 P5 1/PWM1 10/PWM1 9 N/TC1 10 TR0/TC1_9_TR1/PWM0_M_0_N/PWM1_H_10_N/SCB9_SEL3/LIN7_TX/TRIG_IN[39] USER LED3 P5_2/PWM1_11/PWM1_10_N/TC1_11_TR0/TC1_10_TR1/TC0_M_0_TR0/TC1_H_10_TR0/LIN10_RX/LIN7_EN USER_SW1 P5_3/PWM1_12/PWM1_11_N/TC1_12_TR0/TC1_11_TR1/TC0_M_0_TR1/TC1_H_10_TR1/LIN10_TX/LIN2_RX P5_4/PWM1_13/PWM1_12_N/TC1_13_TR0/TC1_12_TR1/LIN9_RX/PWM1_H_11/LIN2_TX P5_5/PWM1_14/PWM1_13_N/TC1_14_TR0/TC1_13_TR1/LIN9_TX/PWM1_H_11_N/LIN2_EN POT AOUT P6_0/PWM1_M_0/PWM1_14_N/TC1_M_0_TR0/TC1_14_TR1/PWM0_0/LIN9_EN/TC1_H_11_TR0/SCB4_RX/SCB4_MISO/LIN3_RX/ADC[0]_0 I2C_SDA P6 1/PWM1 0/PWM1 M 0 N/TC1 0 TR0/TC1 M 0 TR1/TC1 H 11 TR1/SCB4 TX/SCB4 SDA/SCB4 MOSI/LIN3 TX/ADC[0] 1 12C SCL P6_2/PWM1_M_1/PWM1_0_N/TC1_M_1_TR0/TC1_0_TR1/PWM0_0_N/SDHC_CARD_MECH_WRITE_PROT/PWM1_H_12/SCB4_RTS/SCB4_SCL/SCB4_CLK/LIN3_EN/CA
P6_3/PWM1_1/PWM1_M_1_N/TC1_1_TR0/TC1_M_1_TR1/SPIHB_CLK/SDHC_CARD_CMD/PWM1_H_12_N/SCB4_CTS/SCB4_SEL0/LIN4_RX/CAN0_2_RX/CAL_SUP_N SPIHB CLK P6 4/PWM1 M 2/PWM1 1 N/TC1 M 2 TR0/TC1 1 TR1/TC0 0 TR0/SPIHB RWDS/SDHC CLK CARD/TC1 H 12 TR0/SCB4 SEL1/LIN4 TX/ADCI0I 4 SPIHB_SEL0 () P6_5/PWM1_2/PWM1_M_2_N/TC1_2_TR0/TC1_M_2_TR1/TC0_0_TR1/SPIHB_SEL0/SDHC_CARD_DETECT_N/TC1_H_12_TR1/SCB4_SEL2/LIN4_EN/ADC[0]_5
P6_6/PWM1_M_3/PWM1_2_N/TC1_M_3_TR0/TC1_2_TR1/SCB4_SEL3/TRIG_IN[8]/ADC[0]_6 P6_7/PWM1_3/PWM1_M_3_N/TC1_3_TR0/TC1_M_3_TR1/TRIG_IN[9]/ADC[0]_7 CYT4BF8CDDQ0AESGS Legal Disclaimer : SCH Title: KIT T2G-B-H LITE The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any Page Title: T2G-B-H-8M 176 MCU1 examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Size Document Number Drawn By Approved By Rev Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party. Α4 **VJYM** 01 MERWIN 630-60668-01 Date: Wednesday, July 05, 2023 Sheet 6 of 19

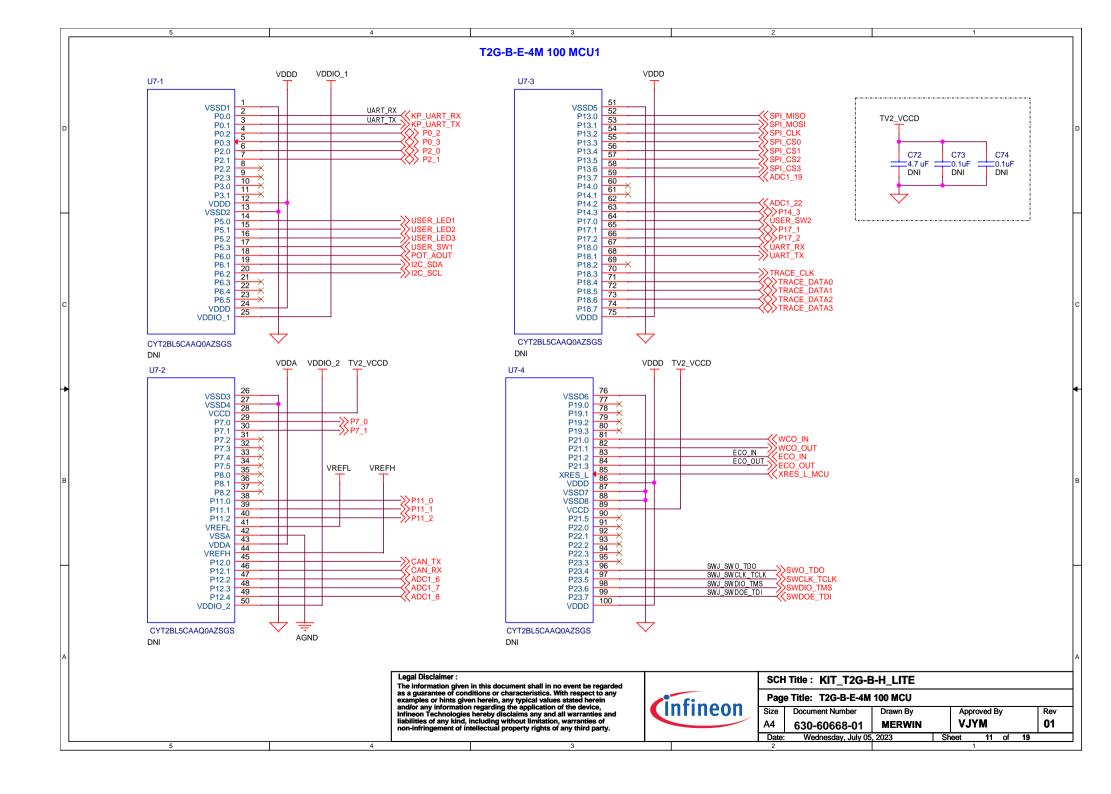
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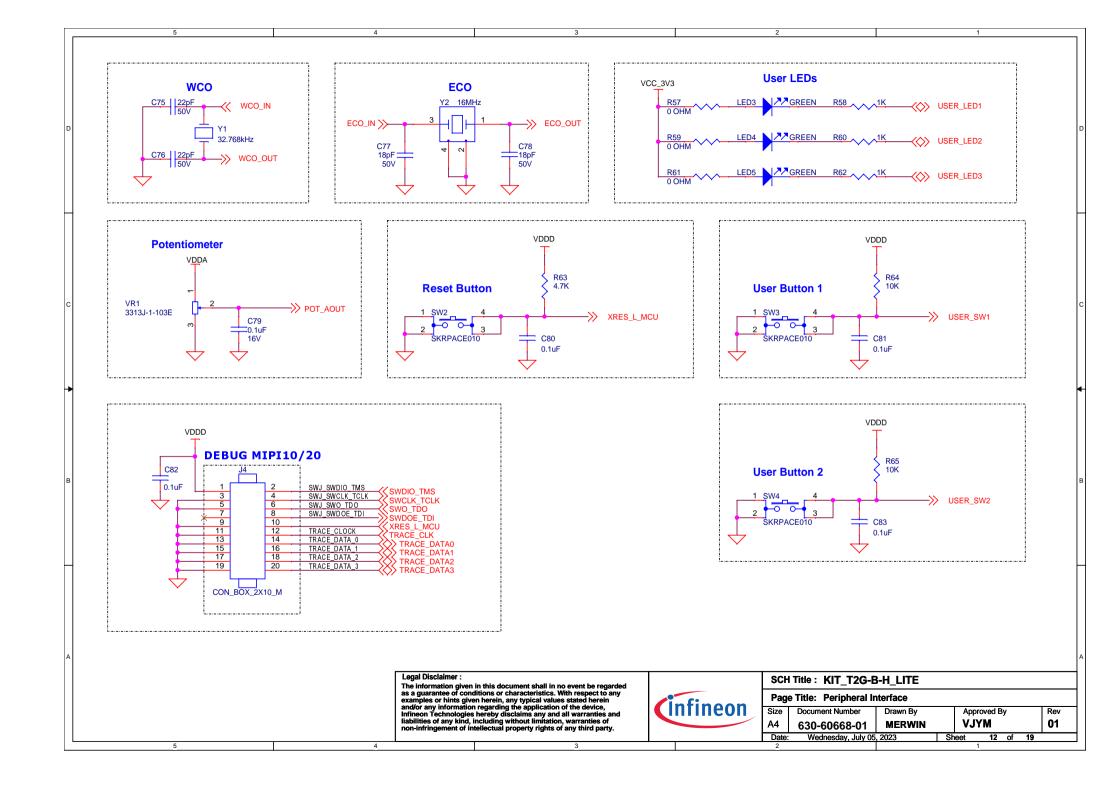
T2G-B-H-8M 176 MCU2 U6B SPIHB SEL1 P7 0/PWM1 M 4/PWM1 3 N/TC1 M 4 TR0/TC1 3 TR1/PWM0 1/SPIHB SEL1/SDHC CARD IF PWR EN/SCB5 RX/SCB5 MISO/LIN4 RX/ADC[0] 16 P7 1/PWM1 15/PWM1 M 4 N/TC1 15 TR0/TC1 M 4 TR1/SPIHB_DATA0/SDHC_CARD_DAT_3TO0_0/SCB5_TX/SCB5_SDA/SCB5_MOSI/LIN4_TX/ADC[0]_17 SPIHB DATAO SPIHB DATA1 P7_2/PWM1_M_5/PWM1_15_N/TC1_M_5_TR0/TC1_15_TR1/PWM0_1_N/SPIHB_DATA1/SDHC_CARD_DAT_3TO0_1/SCB5_RTS/SCB5_SCL/SCB5_CLK/LIN4_EN/ADC SPIHB_DATA2 P7_3/PWM1_16/PWM1_M_5_N/TC1_16_TR0/TC1_M_5_TR1/TC0_1_TR0/SPIHB_DATA2/SDHC_CARD_DAT_3TO0_2/SCB5_CTS/SCB5_SEL0/CAN0_4_TX/ADC[0]_1
P7_4/PWM1_M_6/PWM1_16_N/TC1_M_6_TR0/TC1_16_TR1/TC0_1_TR1/SPIHB_DATA3/SDHC_CARD_DAT_3TO0_3/SCB5_SEL1/CAN0_4_RX/ADC[0]_20 SPIHB DATA3 SPIHB DATA4 P7 5/PWM1 17/PWM1 M 6 N/TC1 17 TR0/TC1 M 6 TR1/PWM0 H 2/SPIHB DATA4/SDHC CARD DAT 7TO4 0/LIN10 RX/SCB5 SEL2/ADC[0] 21 P7_6/PWM1_M_7/PWM1_17_N/TC1_M_7_TR0/TC1_17_TR1/LIN10_TX/TRIG_IN[16]/ADC[0]_22 P7 7/PWM1 18/PWM1 M 7 N/TC1 18 TR0/TC1 M 7 TR1/LIN10 EN/TRIG INI17I/ADCI01 23 P8_0/PWM1_19/PWM1_18_NTC1_19_TR0/TC1_18_TR1/PWM0_H_2_N/SPIHB_DATA5/SDHC_CARD_DAT_7TO4_1/PWM1_H_8/LIN2_RX/CAN0_0_TX SPIHB DATA5 SPIHB DATA6 P8 1/PWM1 20/PWM1 19 N/TC1 20 TR0/TC1 19 TR1/TC0 H 2 TR0/SPIHB DATA6/SDHC CARD DAT 7TO4 2/PWM1 H 8 N/LIN2 TX/CAN0 0 RX/TRIG IN[SPIHB_DATA7 (P8_2/PWM1_21/PWM1_20_N/TC1_21_TR0/TC1_20_TR1/TC0_H_2_TR1/SPIHB_DATA7/SDHC_CARD_DAT_7TO4_3/TC1_H_8_TR0/LIN2_EN/TRIG_IN[15]/ADC[0 P8_3/PWM1_22/PWM1_21_N/TC1_22_TR0/TC1_21_TR1/TC1_H_8_TR1/LIN16_RX/TRIG_DBG[0]/ADC[0]_26 P8_4/PWM1_23/PWM1_22_N/TC1_23_TR0/TC1_22_TR1/LIN16_TX/TRIG_DBG[1]/ADC[0]_27 P9_0/PWM1_24/PWM1_23_N/TC1_24_TR0/TC1_23_TR1/PWM1_H_9/LIN16_EN/ADC[0]_28 P9_1/PWM1_25/PWM1_24_N/TC1_25_TR0/TC1_24_TR1/PWM1_H_9_N/LIN12_RX/ADC[0]_29 P9_2/PWM1_26/PWM1_25_N/TC1_26_TR0/TC1_25_TR1/TC1_H_9_TR0/LIN12_TX/ADC[0]_30 P9 3/PWM1 27/PWM1 26 N/TC1 27 TR0/TC1 26 TR1/TC1 H 9 TR1/LIN12 EN/ADC[0] 31 P10_0/PWM1_28/PWM1_27_NTC1_28_TR0/TC1_27_TR1/PWM1_H_10/SCB4_RX/SCB4_MISO/LIN7_RX/TRIG_IN[18]
P10_1/PWM1_29/PWM1_28_N/TC1_29_TR0/TC1_28_TR1/PWM1_H_10_N/SCB4_TX/SCB4_SDA/SCB4_MOSI/LIN7_TX/TRIG_IN[19]
P10_2/PWM1_30/PWM1_29_N/TC1_30_TR0/TC1_29_TR1/LIN8_RX/TC1_H_10_TR0/SCB4_RTS/SCB4_SCL/SCB4_CLK/FLEXRAY_RXDA P10_3/PWM1_31/PWM1_30_N/TC1_31_TR0/TC1_30_TR1/LIN8_TX/TC1_H_10_TR1/SCB4_CTS/SCB4_SEL0/FLEXRAY_TXDA P10_4/PWM1_32/PWM1_31_N/TC1_32_TR0/TC1_31_TR1/LIN8_EN/PWM1_H_11/SCB4_SEL1/FLEXRAY_TXENA_N/ADC[1]_0 P10_5/PWM1_33/PWM1_32_N/TC1_33_TR0/TC1_32_TR1/PWM1_H_11_N/SCB4_SEL2/LIN13_RX/FLEXRAY_RXDB/ADC[1]_1 P10 6/PWM1 33 N/TC1 33 TR1/PWM1 34/TC1 H 11 TR0/LIN13 TX/FLEXRAY TXDB/ADC[1] 2 P10 7/PWM1 35/PWM1 34 N/TC1 35 TR0/TC1 34 TR1/TC1 H 11 TR1/LIN13 EN/FLEXRAY TXENB N/ADC[1] 3 P11_0/PWM1_61/PWM1_62_N/TC1_61_TR0/TC1_62_TR1/AUDIOSS0_MCLK/ADC[0]_M P11 0 P11 1 P11_1/PWM1_60/PWM1_61_N/TC1_60_TR0/TC1_61_TR1/AUDIOSS0_TX_SCK/ADC[1]_M P11 2 P11 2/PWM1 59/PWM1 60 N/TC1 59 TR0/TC1 60 TR1/AUDIOSS0 TX WS/ADC[2] M CAN TX P12_0/PWM1_36/TC1_36_TR0/PWM0_H_1/PWM1_35_N/AUDIOSS0_TX_SD0/SCB8_RX/SCB8_MISO/CAN0_2_TX/TRIG_IN[20]/ADC[1]_4 CAN_RX P12_1/PWM1_37/PWM1_36_N/TC1_37_TR0/TC1_36_TR1/PWM0_H_1_N/AUDIOSS0_CLK_I2S_IF/SCB8_TX/SCB8_SDA/SCB8_MOSI/LIN6_EN/CAN0_2_RX/TRIG_ ADC₁ 6 P12 2/PWM1 38/PWM1 37 N/TC1 38 TR0/TC1 37 TR1/TC0 H 1 TR0/AUDIOSSO RX SCK/EXT MUX[1] EN/SCB8 RTS/SCB8 SCL/SCB8 CLK/LIN6 RX/ADCI ADC1 7 P12_3/PWM1_39/PWM1_38_N/TC1_39_TR0/TC1_38_TR1/TC0_H_1_TR1/AUDIOSSO_RX_WS/EXT_MUX[1]_0/SCB8_CTS/SCB8_SEL0/LIN6_TX/ADC[1]_7 P12_4/PWM1_40/PWM1_39_N/TC1_40_TR0/TC1_39_TR1/TC0_2_TR1/AUDIOSSO_RX_SDI/EXT_MUX[1]_1/SCB8_SEL1/CAN1_1_TX/ADC[1]_8 ADC1_8 P12_5/PWM1_41/PWM1_40_N/TC1_41_TR0/TC1_40_TR1/EXT_MUX[1]_2/CAN1_1_RX/ADC[1]_9 P12 6/PWM1 42/PWM1 41 N/TC1 42 TR0/TC1 41 TR1/ADC[1] 10 P12 7/PWM1 43/PWM1 42 N/TC1 43 TR0/TC1 42 TR1/ADC[1] 11 CYT4BF8CDDQ0AESGS Legal Disclaimer : SCH Title: KIT T2G-B-H LITE The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Page Title: T2G-B-H-8M 176 MCU2 Infineon Size Document Number Drawn By Approved By Rev Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party. Α4 **VJYM** 01 MERWIN 630-60668-01 Date: Wednesday, July 05, 2023 Sheet of 19 4

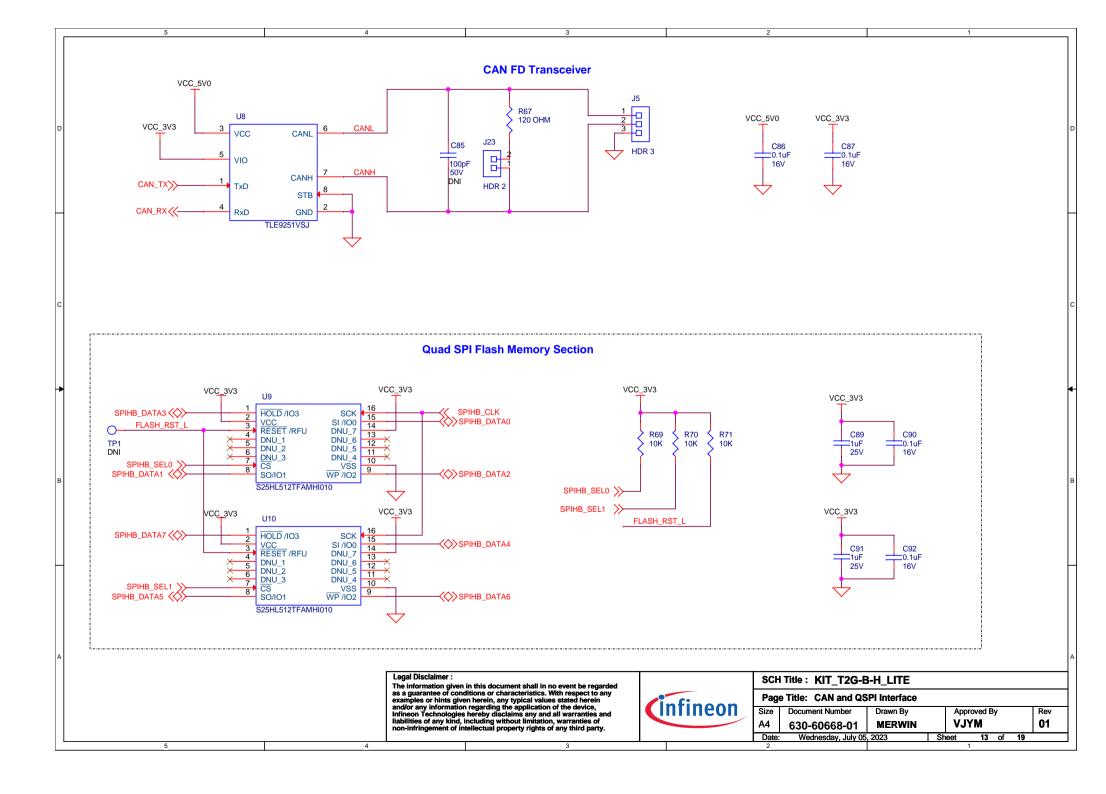
T2G-B-H-8M 176 MCU3 U6C SPL MISO P13 0/PWM1 M 8/PWM1 43 N/TC1 M 8 TR0/TC1 43 TR1/TC0 2 TR0/AUDIOSS1 MCLK/EXT MUX[2] 0/SCB3 RX/LIN3 RX/SCB3 MISO/ADC[1] 12 SPI MOSI P13 1/PWM1 44/PWM1 M 8 N/TC1 44 TR0/TC1 M 8 TR1/PWM0 2 N/AUDIOSS1 TX SCK/EXT MUX[2] 1/SCB3 TX/SCB3 SDA/LIN3 TX/SCB3 MOSI/ADC[1] SPI CLK P13_2/PWM1_M_9/PWM1_44_N/TC1_M_9_TR0/TC1_44_TR1/PWM0_2/AUDIOSS1_TX_WS/EXT_MUX/2]_2/SCB3_RTS/SCB3_SCL/LIN3_EN/SCB3_CLK/ADC[1]_14 SPI CS0 P13 3/PWM1 45/PWM1 M 9 N/TC1 45 TR0/TC1 M 9 TR1/AUDIOSS1 TX SD0/EXT MUXI2I EN/SCB3 CTS/LIN2 RX/SCB3 SEL0/ADC[1] 15 P13 4/PWM1 M 10/PWM1 45 N/TC1 M 10 TR0/TC1 45 TR1/LIN8 RX/AUDIOSS1 CLK I2S IF/PWM1 H 4/LIN2 TX/SCB3 SEL1/ADC[1] 16 SPI_CS2 P13_5/PWM1_46/PWM1_M_10_N/TC1_46_TR0/TC1_M_10_TR1/LINS_TX/AUDIOSS1_RX_SCK/PWM1_H_4_N/SCB3_SEL2/ADC[1]_17 SPI CS3 P13_6/PWM1_M_11/PWM1_46_N/TC1_M_11_TR0/TC1_46_TR1/LIN8_EN/AUDIOSS1_RX_WS/PWM1_H_5/SCB3_SEL3/TRIG_IN[/22]/ADC[1]_18 ADC1_19 P13 7/PWM1 47/PWM1 M 11 N/TC1 47 TR0/TC1 M 11 TR1/AUDIOSS1 RX SDI/PWM1 H 5 N/TRIG INI23I/ADCI11 19 P14_0/PWM1_48/PWM1_47_N/TC1_48_TR0/TC1_47_TR1/PWM0_M_1/AUDIOSS2_MCLK/PWM1_H_6/SCB2_M/SO/SCB2_RX/CAN1_0_TX/ADC[1]_20 UART RX UART TX P14 1/PWM1 49/PWM1 48 N/TC1 49 TR0/TC1 48 TR1/PWM0 M 1 N/AUDIOSS2 TX SCK/PWM1 H 6 N/SCB2 MOSI/SCB2 SDA/SCB2 TX/CAN1 0 RX/ADC[1] ADC1 22 P14_2/PWM1_50/PWM1_49_N/TC1_50_TR0/TC1_49_TR1/TC0_M_1_TR0/PWM1_H_7/SCB2_CLK/SCB2_SCL/SCB2_RTS/LIN6_RX/ADC[1]_22_ P14_3 P14_3/PWM1_51/PWM1_50_N/TC1_51_TR0/TC1_50_TR1/TC0_M_1_TR1/PWM1_H_7_N/SCB2_SEL0/SCB2_CT5/LIN6_TX/ADC[1]_23 P14_4/PWM1_52/PWM1_51_N/TC1_52_TR0/TC1_51_TR1/AUDIOSS2_TX_WS/TC1_H_4_TR0/SCB2_SEL1/LIN6_EN/ADC[1]_24 P14_5/PWM1_53/PWM1_52_N/TC1_53_TR0/TC1_52_TR1/AUDIOSS2_TX_SD0/TC1_H_4_TR1/SCB2_SEL2/LIN14_RX/ADC[1]_25 P14_6/PWM1_54/PWM1_53_N/TC1_54_TR0/TC1_53_TR1/TC1_H_5_TR0/LIN14_TX/TRIG_IN[24]/ADC[1]_26 P14_7/PWM1_55/PWM1_54_N/TC1_55_TR0/TC1_54_TR1/TC1_H_5_TR1/LIN14_EN/TRIG_IN[25]/ADC[1]_27 P15 0/PWM1 56/PWM1 55 N/TC1 56 TR0/TC1 55 TR1/AUDIOSS2 CLK I2S IF/TC1 H 6 TR0/SCB9 RX/SCB9 MISO/CAN1 3 TX/ADC[1] 28 P15_1/PWM1_57/PWM1_56_N/TC1_57_TR0/TC1_56_TR1/AUDIOSS2_RX_SCK/TC1_H_6_TR1/SCB9_TX/SCB9_SDA/SCB9_MOSI/CAN1_3_RX/ADC[1]_29
P15_1/PWM1_58/PWM1_57_N/TC1_58_TR0/TC1_57_TR1/AUDIOSS2_RX_WS/TC1_H_7_TR0/SCB9_RTS/SCB9_SCL/SCB9_CLK/ADC[1]_30
P15_3/PWM1_58/PWM1_58_N/TC1_59_TR0/TC1_58_TR1/AUDIOSS2_RX_SDI/TC1_H_7_TR1/SCB9_CTS/SCB9_SEL0/ADC[1]_31 P16_3/PWM1_62/PWM1_62_N/TC1_62_TR0/TC1_62_TR1/PWM1_H_1_N/ADC[2]_3
P17_0/PWM1_61/PWM1_62_N/TC1_61_TR0/TC1_62_TR1/LIN11_RX/CAN1_1_TX/ADC[2]_8
P17_1/PWM1_60/PWM1_61_N/TC1_60_TR0/TC1_61_TR1/SCB3_RX/LIN11_TX/CAN1_1_RX/ADC[2]_9 USER SW2 P17 1 P17 2 P17 2/PWM1 59/PWM1 60 N/TC1 59 TR0/TC1 60 TR1/SCB3 TX/SCB3 SDA/LIN11 EN/ADC[2] 10 P17_3/PWM1_58/PWM1_59_N/TC1_58_TR0/TC1_59_TR1/PWM1_H_3/SCB3_RTS/SCB3_SCL//SCB3_CLK/TRIG_IN[26]/ADC[2]_11 P17_4/PWM1_57/PWM1_58_N/TC1_57_TR0/TC1_58_TR1/PWM1_H_3_N/SCB3_CTS/SCB3_SEL0/TRIG_IN[27]/ADC[2]_12 P17_5/PWM1_56/PWM1_57_N/TC1_56_TR0/TC1_57_TR1/PWM1_H_2/LIN15_RX/SCB3_SEL1/ADC[2]_13 P17 6/PWM1 M 4/PWM1 56 N/TC1 M 4 TR0/TC1 56 TR1/PWM1 H 2 N/LIN15 TX/SCB3 SEL2/ADC[2] 14 P17_7/PWM1_M_5/PWM1_M_4_N/TC1_M_5_TR0/TC1_M_4_TR1/LIN15_EN/LIN12_RX/ADC[2]_15 ETH_REFCLK P18_0/PWM1_M_6/PWM1_M_5_N/TC1_M_6_TR0/TC1_M_5_TR1/ETHO_REF_CLK/PWM1_H_0/SCB1_RX/SCB1_MISO/LIN12_TX/FAULT_OUT_0/ADC[2]_16 ETH_TXEN P18 1/PWM1 M 7/PWM1 M 6 N/TC1 M 7 TR0/TC1 M 6 TR1/ETH0 TX CTL/PWM1 H 0 N/SCB1 TX/SCB1 SDA/SCB1 MOSI/SCB3 MISO/FAULT OUT 1/ADC[2 P18_2/PWM1_55/PWM1_M_7_NTC1_55_TR0/TC1_M_7_TR1/ETH0_TX_ER/PWM1_H_1/SCB1_RTS/SCB1_SCL/SCB1_CLK/SCB3_MOSI/ADC[2]_18 P18_3/PWM1_54/PWM1_55_N/TC1_54_TR0/TC1_55_TR1/ETH0_TX_CLK/PWM1_H_1_N/SCB1_CTS/SCB1_SEL0/SCB3_CLK/TRACE_CLOCK/ADC[2]_19 ETH_TXD0 P18 4/PWM1 53/PWM1 54 N/TC1 53 TR0/TC1 54 TR1/PWM0 M 2/ETH0 TXD 0/PWM1 H 2/SCB1 SEL1/SCB3 SEL0/TRACE DATA 0/ADCI2I 20 ETH TXD1 P18_5/PWM1_52/PWM1_53_N/TC1_52_TR0/TC1_53_TR1/PWM0_M_2_N/ETH0_TXD_1/PWM1_H_2_N/SCB1_SEL2/TRACE_DATA_1/ADC[2]_21
P18_6/PWM1_51/PWM1_52_N/TC1_51_TR0/TC1_52_TR1/TC0_M_2_TR0/ETH0_TXD_2/PWM1_H_3/SCB1_SEL3/CAN1_2_TX/TRACE_DATA_2/ADC[2]_22 P18_7/PWM1_50/PWM1_51_N/TC1_50_TR0/TC1_51_TR1/TC0_M_2_TR1/ETH0_TXD_3/PWM1_H_3_N/CAN1_2_RX/TRACE_DATA_3/ADC[2]_23 CYT4BF8CDDQ0AESGS Legal Disclaimer : SCH Title: KIT T2G-B-H LITE The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party. Page Title: T2G-B-H-8M 176 MCU3 infineon Size Document Number Drawn By Approved By Rev Α4 **VJYM** 01 MERWIN 630-60668-01 Date: Wednesday, July 05, 2023 Sheet 8 of 19 4

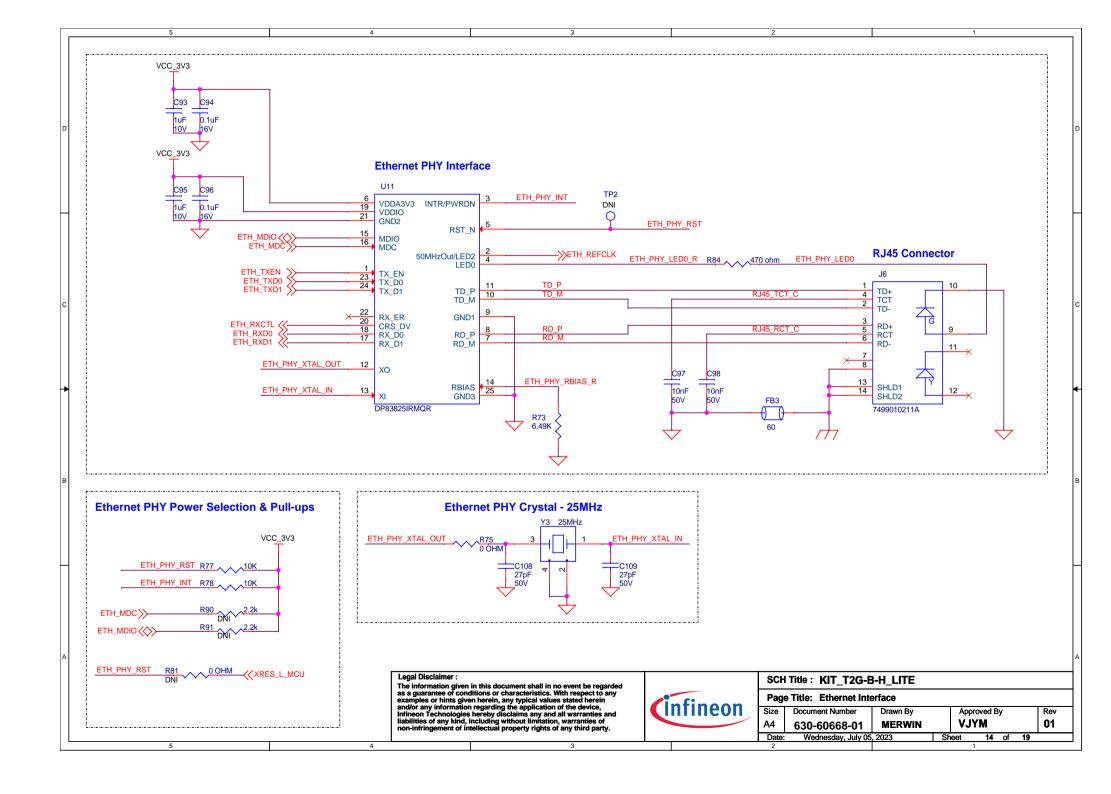
T2G-B-H-8M 176 MCU4 U6D ETH RXD0 P19 0/PWM1 M 3/PWM1 50 N/TC1 M 3 TR0/TC1 50 TR1/ETH0 RXD 0/TC1 H 0 TR0/SCB2 MISO/SCB2 RX/CAN1 3 TX/FAULT OUT 2/ADC[2] 24 135 ETH RXD1 P19_1/PWM1_26/PWM1_M_3_N/TC1_26_TR0/TC1_M_3_TR1/ETH0_RXD_1/TC1_H__TR1/SCB2_MOSI/SCB2_SDA/SCB2_TX/CAN1_3_RX/FAULT_OUT_3/ADC[2]_ P19_2/PWM1_27/PWM1_26_N/TC1_27_TR0/TC1_26_TR1/ETH0_RXD_2/TC1_H_1_TR0/SCB2_CL//SCB2_SCL/SCB2_RTS/TRIG_IN[28]/ADC[2]_26 P19_3/PWM1_28/PWM1_27_N/TC1_28_TR0/TC1_27_TR1/ETH0_RXD_3/TC1_H_1_TR1/SCB2_SEL0/SCB2_CTS/TRIG_IN[29]/ADC[2]_27 P19_4/PWM1_29/PWM1_28_N/TC1_29_TR0/TC1_28_TR1/TC1_H_2_TR0/SCB2_SEL1/ADC[2]_28 P20_0/PWM1_30/PWM1_29_N/TC1_30_TR0/TC1_29_TR1/TC1_H_2_TR1/SCB2_SEL2/LIN5_RX/ADC[2]_29 P20 1/PWM1 49/PWM1 30 N/TC1 49 TR0/TC1 30 TR1/TC1 H 3 TR0/LIN5 TX/ADC[2] 30 P20_2/PWM1_48/PWM1_49_N/TC1_48_TR0/TC1_49_TR1/TC1_H_3_TR1/LIN5_EN/ADC[2]_31 P20 3/PWM1 47/PWM1 48 N/TC1 47 TR0/TC1 48 TR1/SCB1 RX/SCB1 MISO/CAN1 2 TX P20 4/PWM1 46/PWM1 47 N/TC1 46 TR0/TC1 47 TR1/SCB1 TX/SCB1 SDA/SCB1 MOSI/CAN1 2 RX P20_5/PWM1_45/PWM1_46_N/TC1_45_TR0/TC1_46_TR1/SCB1_RTS/SCB1_SCL/SCB1_CLK P20 6/PWM1 44/PWM1 45 N/TC1 44 TR0/TC1 45 TR1/SCB1 CTS/SCB1 SEL0/CAN1 4 TX P20_7/PWM1_43/PWM1_44_N/TC1_43_TR0/TC1_44_TR1/SCB1_SEL1/CAN1_4_RX WCO_IN P21_0/PWM1_42/PWM1_43_N/TC1_42_TR0/TC1_43_TR1/SCB1_SEL2/WCO_IN P21_1/PWM1_41/PWM1_42_N/TC1_41_TR0/TC1_42_TR1/WCO_OUT WCO_OUT ECO IN P21 2/PWM1 40/PWM1 41 N/TC1 40 TR0/TC1 41 TR1/EXT CLK/TRIG DBG[1]/ECO IN ECO OUT P21 3/PWM1 39/PWM1_40_N/TC1_39_TR0/TC1_40_TR1/ECO_OUT 151 P21_4/PWM1_38/PWM1_39_N/TC1_38_TR0/TC1_39_TR1/HIBERNATE_WAKEUP[0] XRES_L_MCU XRES 157 ETH RXCTL P21 5/PWM1 37/PWM1 38 N/TC1 37 TR0/TC1 38 TR1/PWM1 34/PWM1 35 N/ETH0 RX CTL/LIN0 RX/CAN1 1 TX/TRACE DATA 0 158 P21 6/PWM1 36/PWM1 37 N/TC1 36 TR0/TC1 37 TR1/LIN0 TX/LIN13 RX P21 7/PWM1 35/PWM1 36 N/TC1 35 TR0/TC1 36 TR1/SCB6 RX/SCB6 MISO/LIN0 EN/LIN13 TX/CAL SUP NZ/RTC CAL DRV VOUT DRV VOUT 161 EXT PS CTL0 P22 1/PWM1 33/PWM1 34 N/TC1 33 TR0/TC1 34 TR1/SCB6 TX/SCB6 SDA/SCB6 MOSI/CAN1 1 RX/TRACE DATA 1/EXT PS CTL0 162 EXT_PS_CTL1 〈 P22_2/PWM1_32/PWM1_33_N/TC1_32_TR0/TC1_33_TR1/SCB6_RTS/SCB6_SCL/SCB6_CLK/TRACE_DATA_2/EXT_PS_CTL1 P22_3/PWM1_31/PWM1_32_N/TC1_31_TR0/TC1_32_TR1/SCB6_CTS/SCB6_SEL0/TRACE_DATA_3/EXT_PS_CTL2
P22_4/PWM1_30/PWM1_31_N/TC1_30_TR0/TC1_31_TR1/SCB6_SEL1/TRACE_CLOCK P22_5/PWM1_29/PWM1_30_N/TC1_29_TR0/TC1_30_TR1/PWM1_H_8/SCB6_SEL2/LIN7_RX P22_6/PWM1_28/PWM1_29_N/TC1_28_TR0/TC1_29_TR1/PWM1_H_8_N/LIN7_TX P22 7/PWM1 27/PWM1 28 N/TC1 27 TR0/TC1 28 TR1/TC1 H 8 TR0/LIN14 RX/LIN7 EN P23_0/PWM1_M_8/PWM1_27_N/TC1_M_8_TR0/TC1_27_TR1/TC1_H_8_TR1/SCB7_RX/LIN14_TX/SCB7_MISO/CAN1_0_TX/FAULT_OUT_0 P23 1/PWM1 M 9/PWM1 M 8 N/TC1 M 9 TR0/TC1 M 8 TR1/SCB7 TX/SCB7 SDA/SCB7 MOSI/CAN1 0 RX/FAULT OUT 1 P23 2/PWM1 M 10/PWM1 M 9 N/TC1 M 10 TR0/TC1 M 9 TR1/SCB7 RTS/SCB7 SCL/SCB7 CLK/LIN6 RX/FAULT OUT 2 P23_3/PWM1_M_11/PWM1_M_10_N/TC1_M_11_TR0/TC1_M_10_TR1/ETH0_RX_CLK/SCB7_CTS/SCB7_SEL0/LIN6_TX/FAULT_OUT_3/TRIG_IN[30] P23_4/PWM1_25/PWM1_M_11_N/TC1_25_TR0/TC1_M_11_TR1/PWM1_H_9/SCB2_MISO/SCB7_SEL1/TRIG_DBG[0]/SWJ_SWO_TD0/TRIG_IN[31] SWO TDO SWCLK TCLK P23_5/PWM1_24/PWM1_25_N/TC1_24_TR0/TC1_25_TR1/LIN9_RX/PWM1_H_9_N/SCB2_MOSI/SCB7_SEL2/SWJ_SWCLK_TCLK P23_6/PWM1_23/PWM1_24_NTC1_23_TR0/TC1_24_TR1/LIN9_TX/TC1_H_9_TR0/SCB2_CLK/SWJ_SWDIO_TMS
P23_7/PWM1_22/PWM1_23_N/TC1_22_TR0/TC1_23_TR1/EXT_CLK/LIN9_EN/TC1_H_9_TR1/SCB2_SEL0/CAL_SUP_NZ/SWJ_SWDOE_TDI/HIBERNATE_WAKEUP[1 SWDIO TMS SWDOE TDI CYT4BF8CDDQ0AESGS Legal Disclaimer : SCH Title: KIT T2G-B-H LITE The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information reparating the application of the development infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties and infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of properties of any kind, including without limitation, warranties of properties. Page Title: T2G-B-H-8M 176 MCU4 infineon Size Document Number Drawn By Approved By Rev Α4 **VJYM** 01 MERWIN 630-60668-01 Date: Wednesday, July 05, 2023 Sheet of 19 4

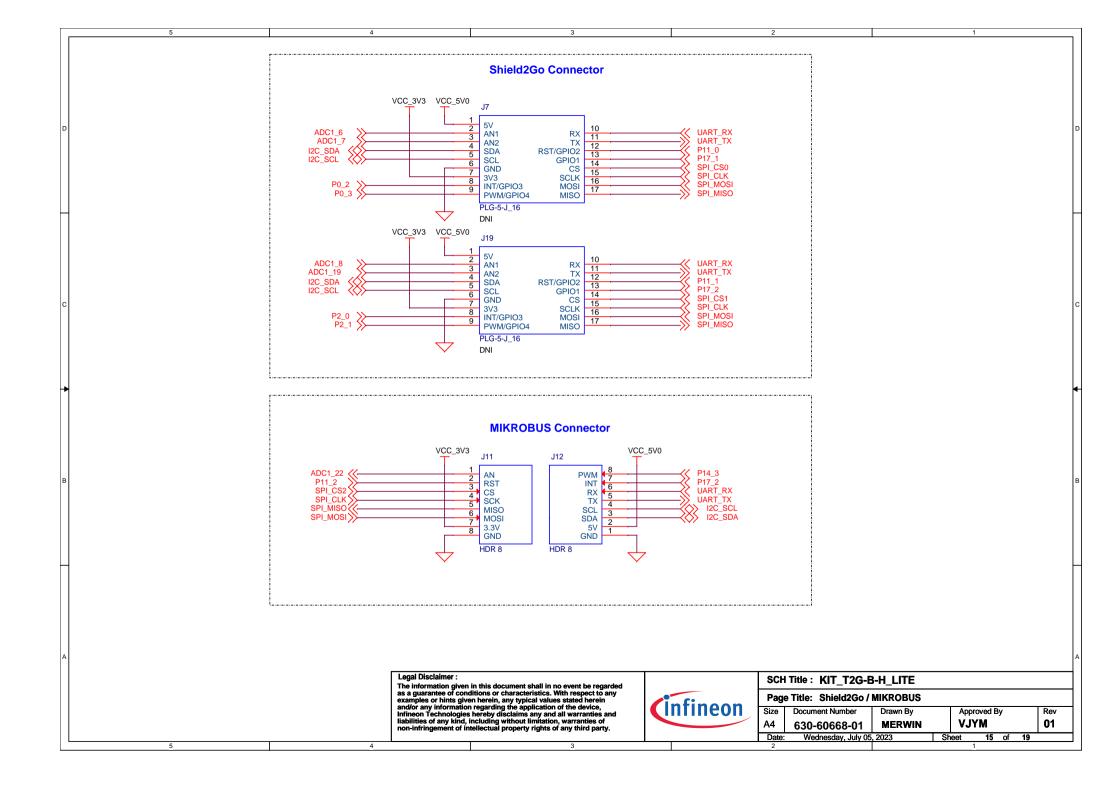


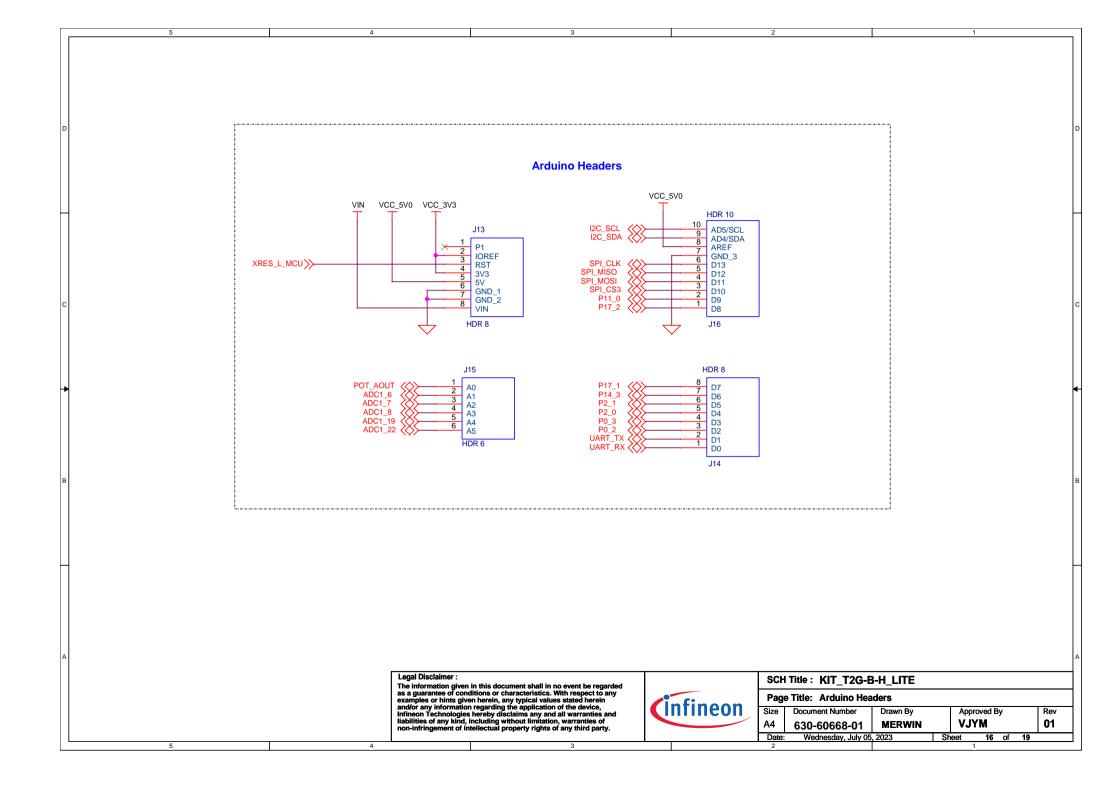


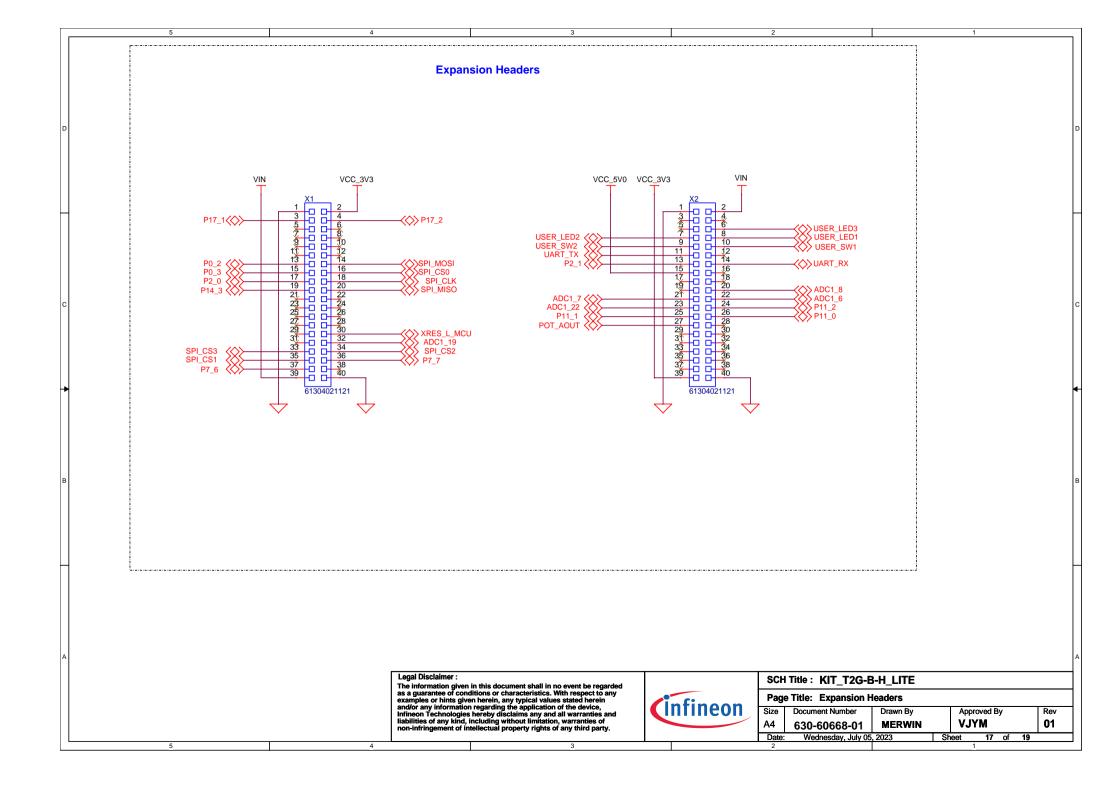


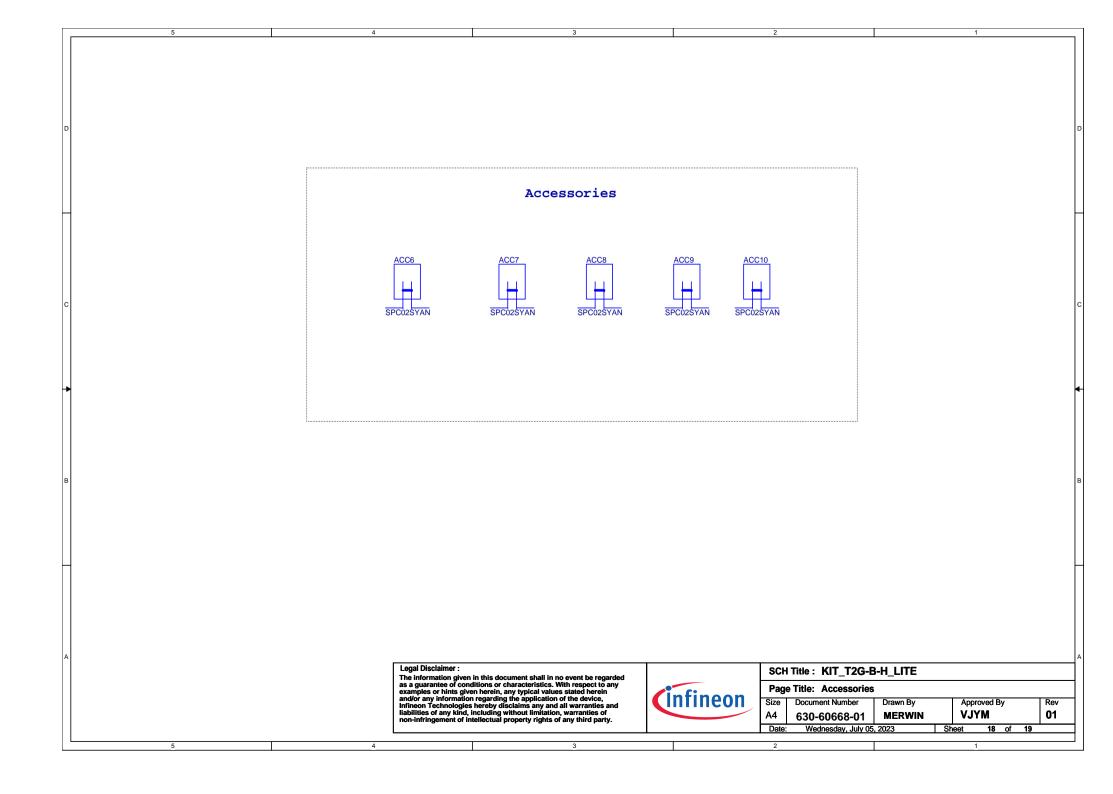












REVISION HISTORY

REV	DESCRIPTION OF CHANGE	Orig. of Change	DATE
Α	Initial Release	TAKEBAYASHI	2022/7/29
В	J11, J12, J14 change to 61300811821	TAKEBAYASHI	2023/1/12
С	U9 and U10 change to S25HL512TFAMHI010 Ether PHY(U11) and RJ45(J6) change parts	TAKEBAYASHI	2023/2/21
D	no change. just revision up	TAKEBAYASHI	2023/3/30
01	J4 is loaded TP1, TP2 added for QSPI and Ethernet Reset J22 is added between VCC and KP_VBUS J25 is added for Regulator VIN selection R90, R91 has been changed to 2.2k and not loaded. VREFH, VREFL has been changed according to design guidelines R66,R68,C84 has been removed. R67 is loaded. J23 has been added. FB2 has been removed and FB4 added. T2G_VDD rail is removed and VIN_REG is added. X2.35,X2.36 is routed to P7.0 and P7.1 in B-E. P14.0,P14.1 short to P18.0 and P18.1 is removed. User Button, Reset and Debug has been pulled to VDDD.	MERWIN	2023/07/05

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Pag	Page Title: Revision History						
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