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01_Revision_History.SchDoc

Revision History

Rev.	Rel.	Date	Author	Description	Page(s)
V2.0		06/2020	H.D.	First new design for A2G	

AURIX™ Lite Kit V2

[No Variations]

V2.0

Infineon Technologies AG
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07/2020

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Title	AURIX™ Lite Kit V2		
Variant	[No Variations]	Approved	<Appr.>
Size: A3	Document Name: 01_Revision_History.SchDoc	Rel.: H.D.	Rev.: V2.0
Author: H.D.			
Date: 16.10.2020 Time: 13:19:43	SVN Revision: Not in version control		
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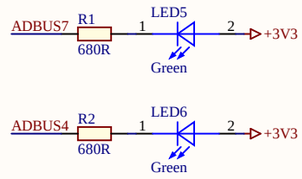
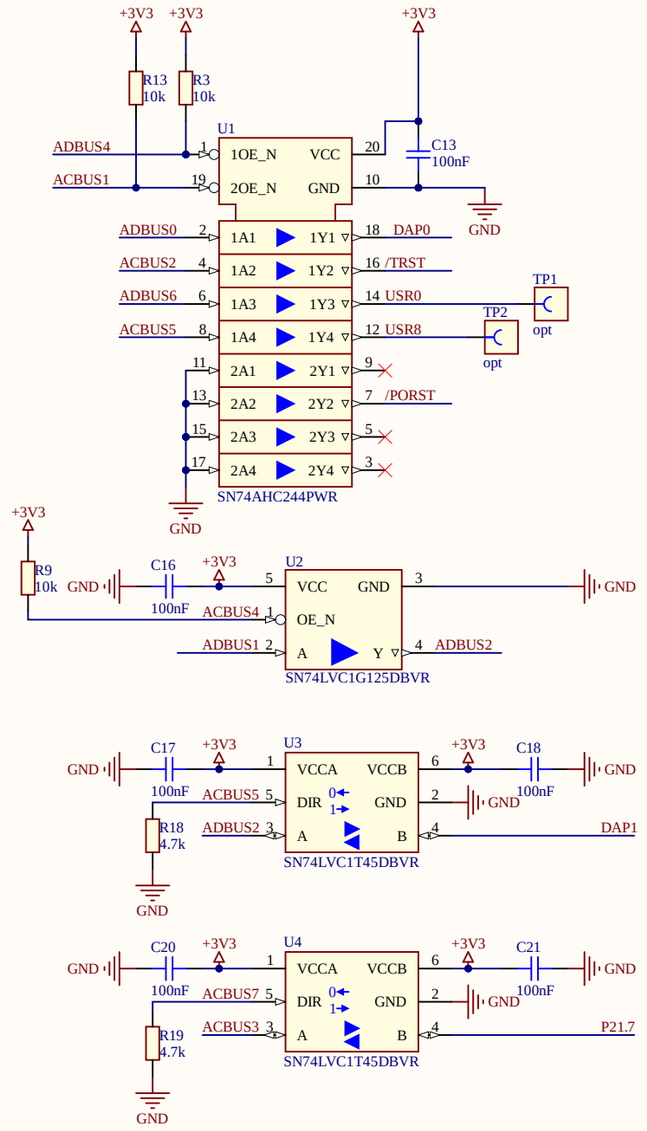
OCDS

02_OCDS.SchDoc

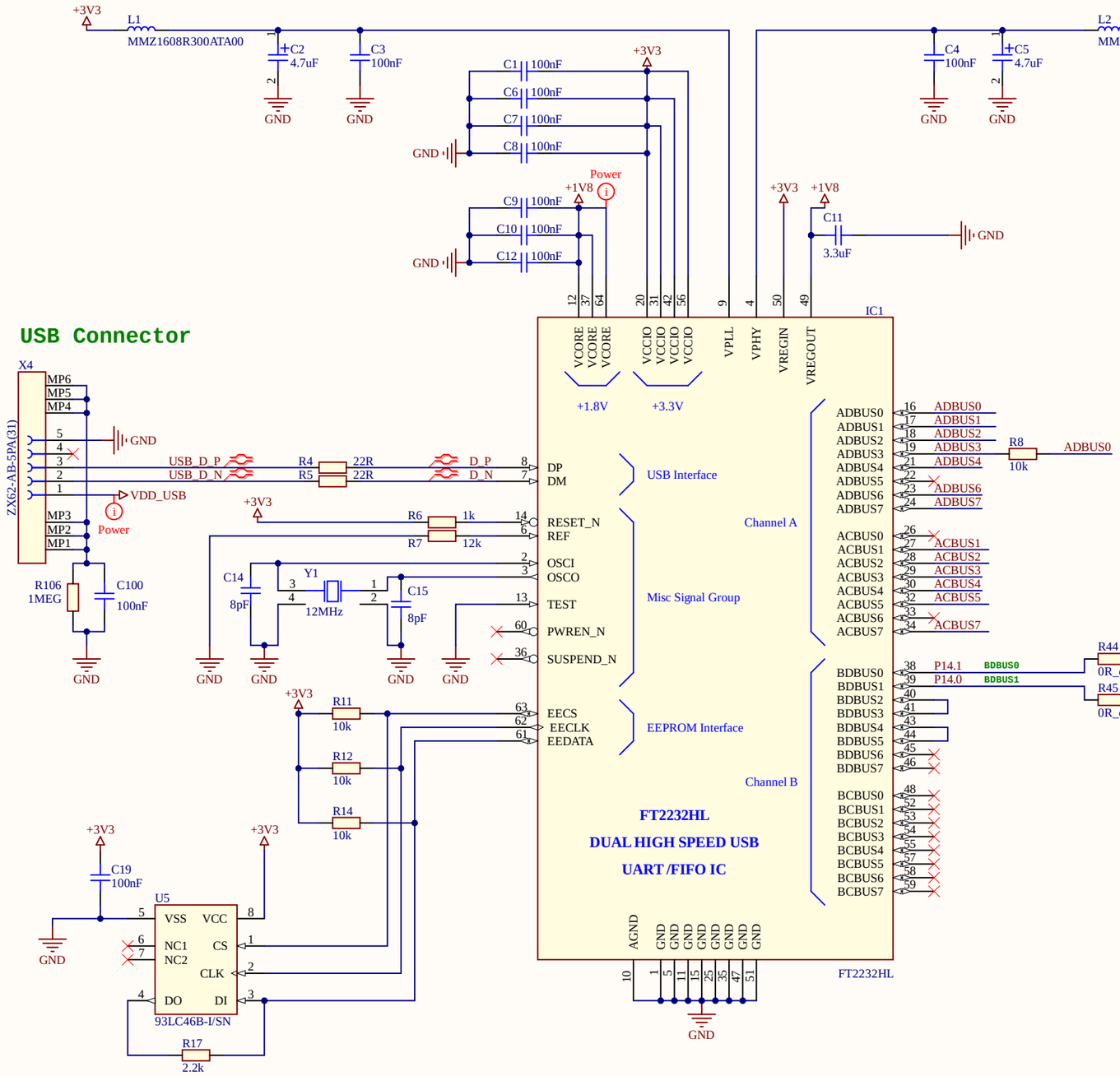
LEDs of OCDS



Signal Network Switches



USB Connector



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Size: A3	Document Name: 02_OCDS.SchDoc	Rel.: H.D.	Rev.: V2.0

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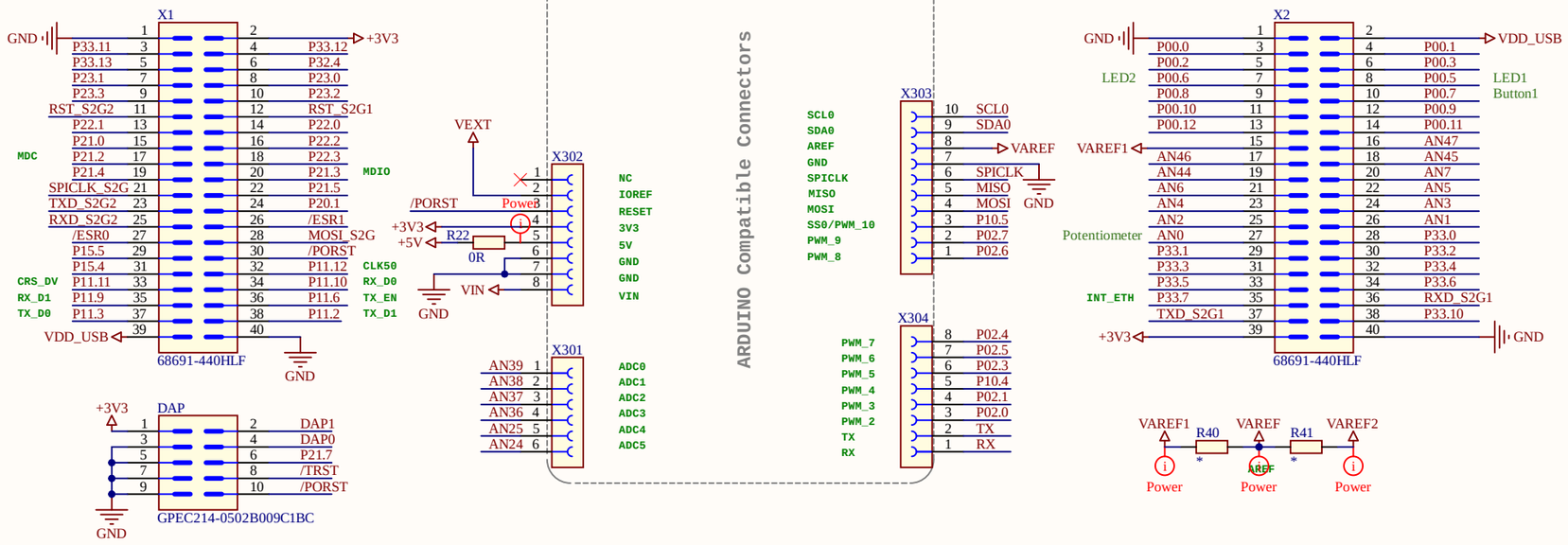
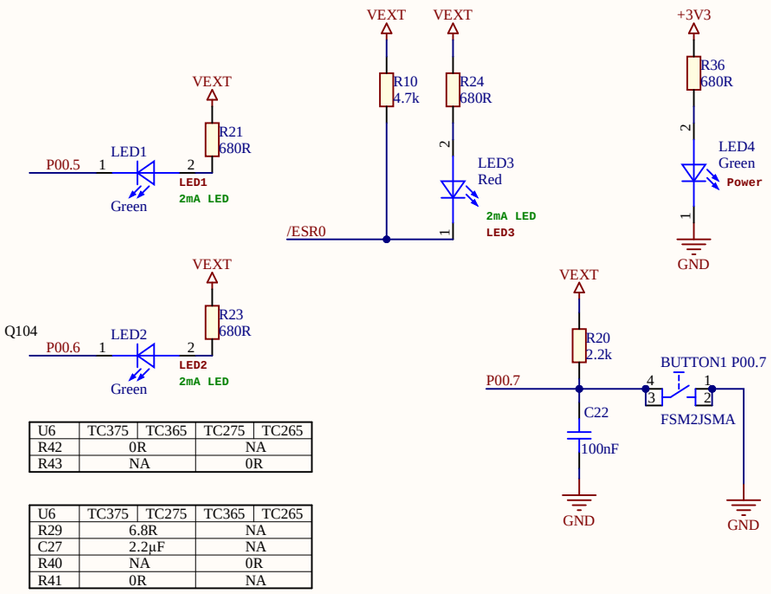
 Sheet 2 of 5

03_Power_a_Connector.SchDoc

Arduino Connector & X1/X2



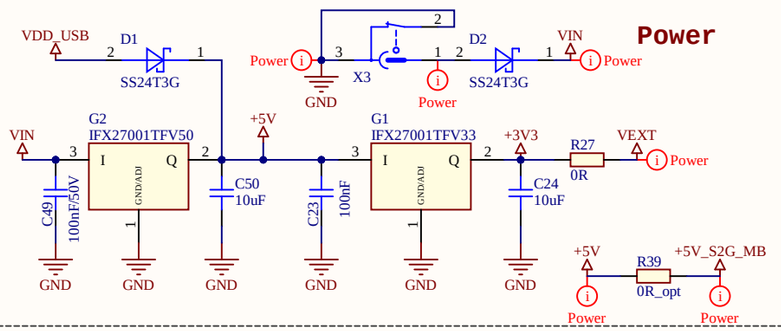
Buttons & LEDs



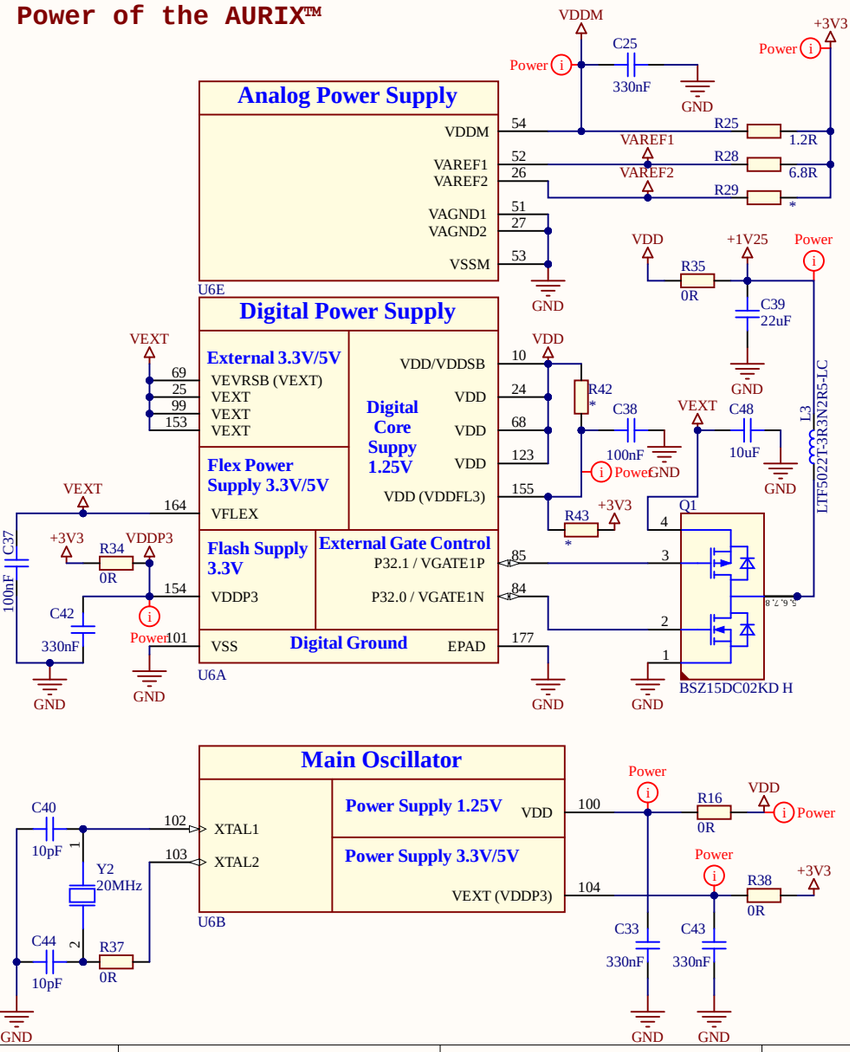
U6	TC375	TC365	TC275	TC265
R42	0R		NA	
R43	NA		0R	

U6	TC375	TC275	TC365	TC265
R29	6.8R		NA	
C27	2.2µF		NA	
R40	NA		0R	
R41	0R		NA	

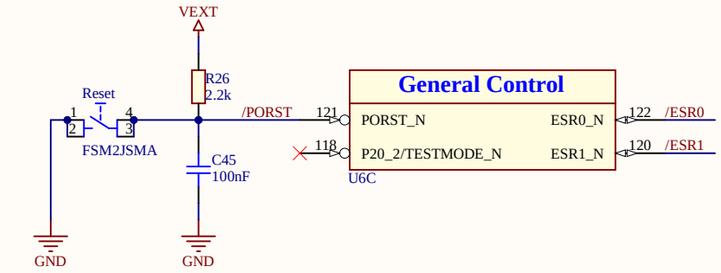
Power



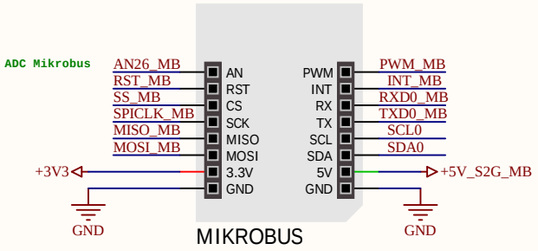
Power of the AURIX™



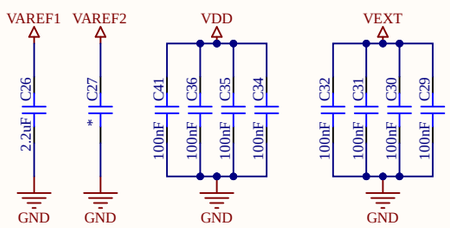
Reset Circuit of AURIX™



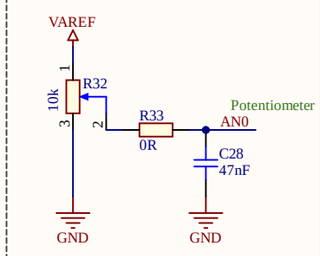
Mikrobus



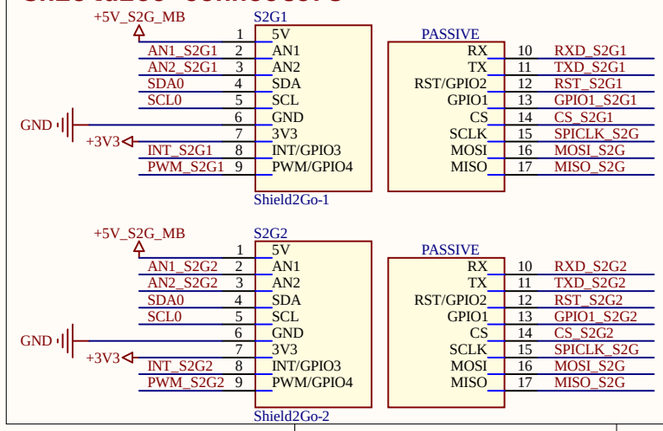
AURIX™ Capacitors



Potentiometer



Shield2GO Connectors



Mikrobus

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Author: H.D.			
Date: 16.10.2020 Time: 13:19:43	SVN Revision: Not in version control		

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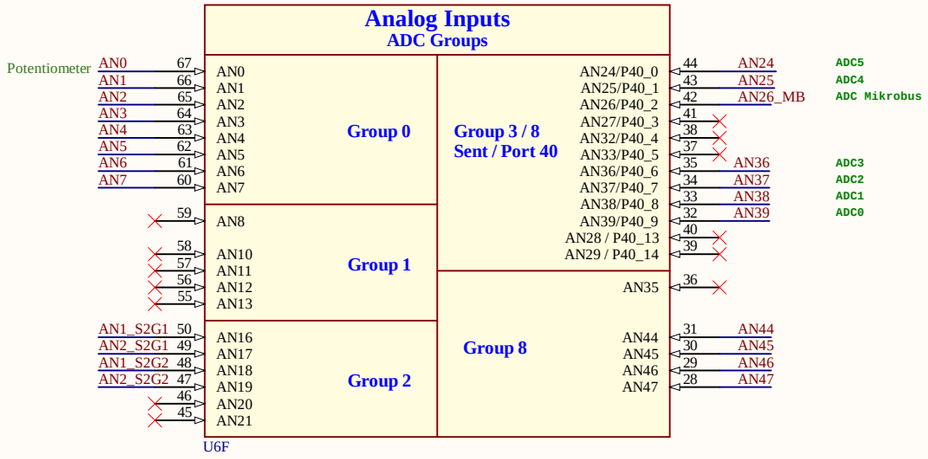
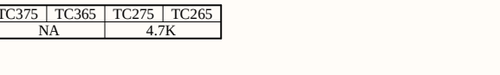
04_CPU.SchDoc

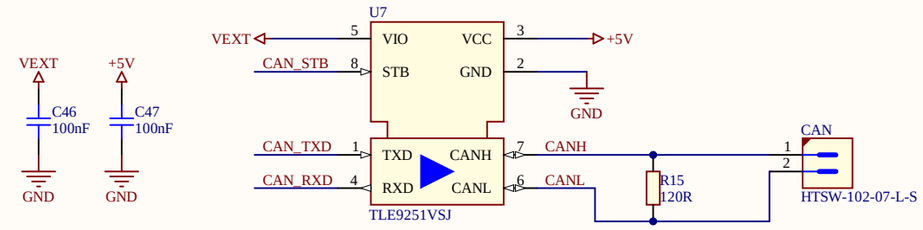
Analog Circuit of AURIX™



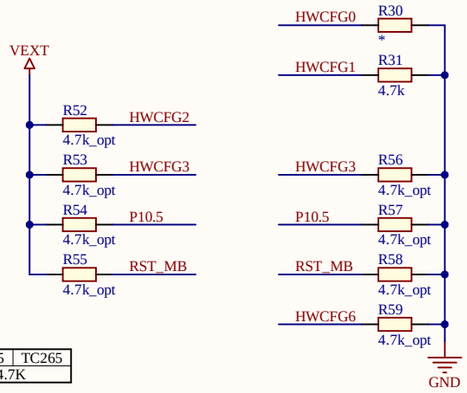
DAP Control of AURIX™



CAN Tranceiver

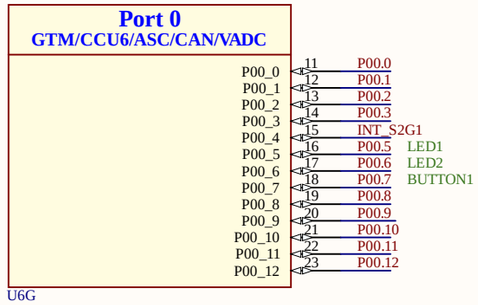


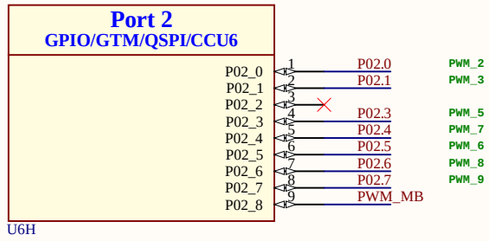
HW Config

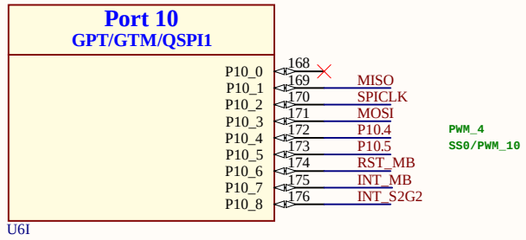


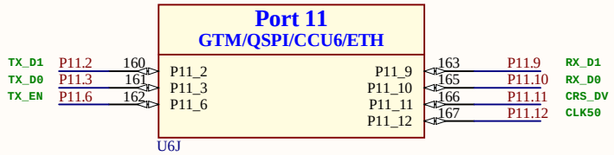
U6	TC375	TC365	TC275	TC265
R30	NA		4.7K	

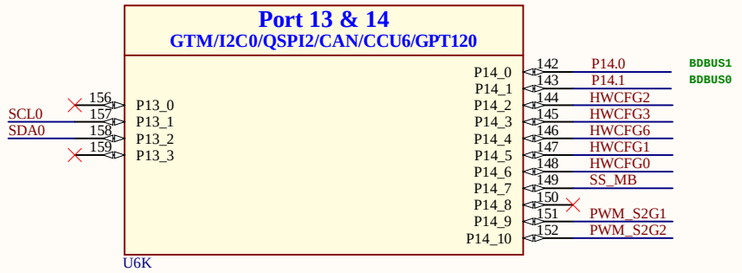
Ports of AURIX™

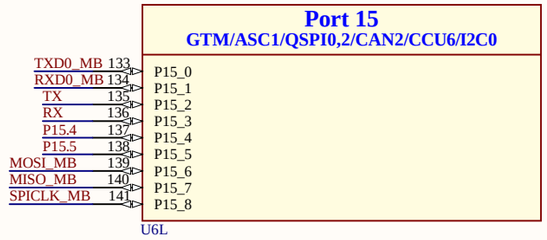


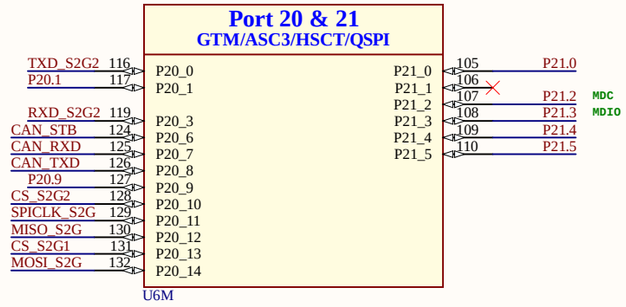


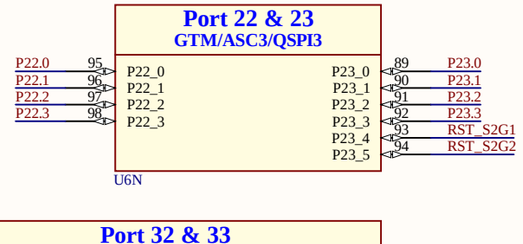


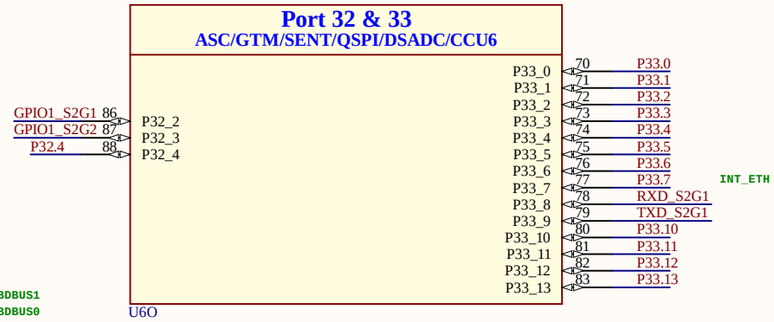












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Author: H.D.
Date: 16.10.2020 Time: 13:19:44 SVN Revision: Not in version control

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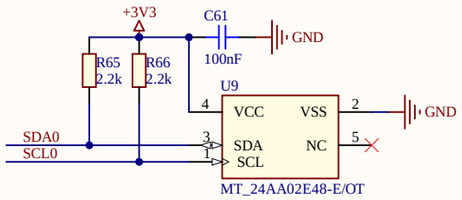
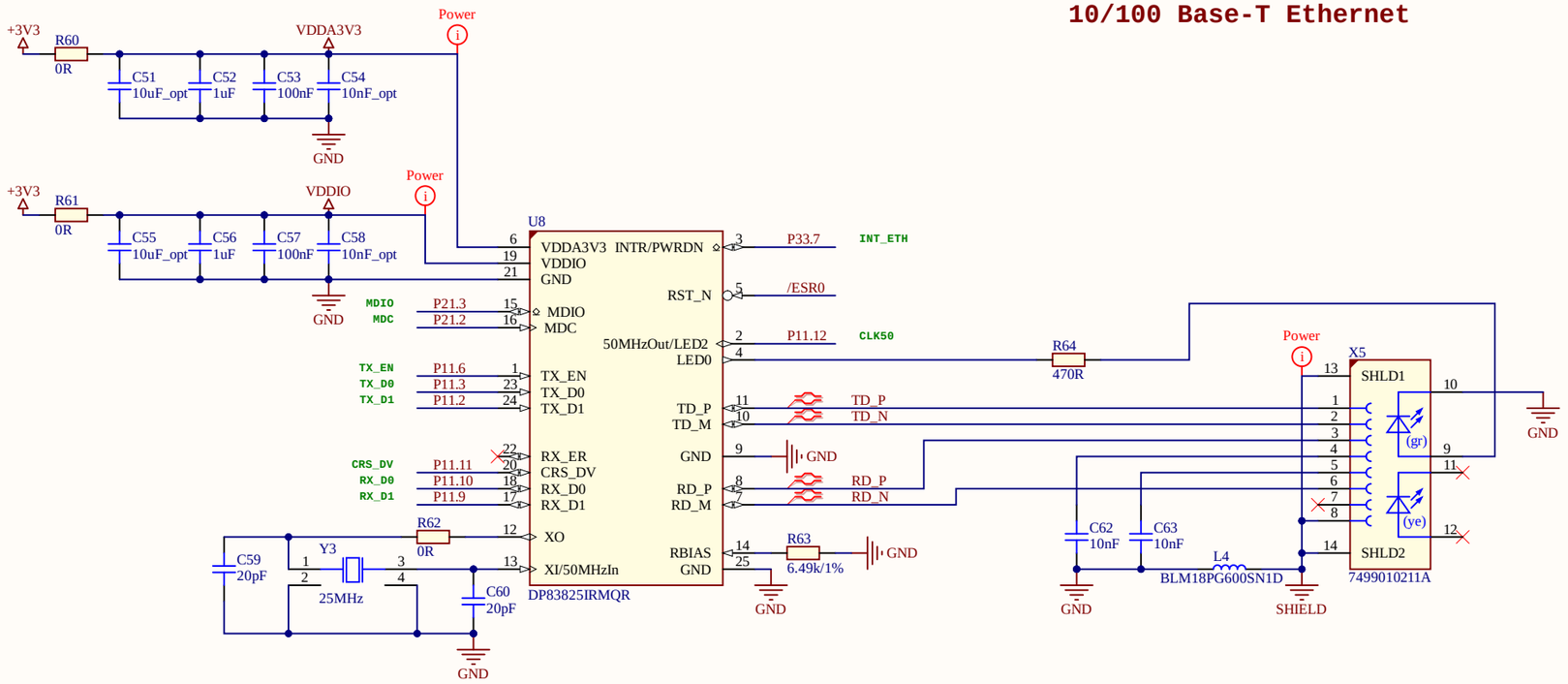
05_Ethernet_Memory_Expansion.SchDoc

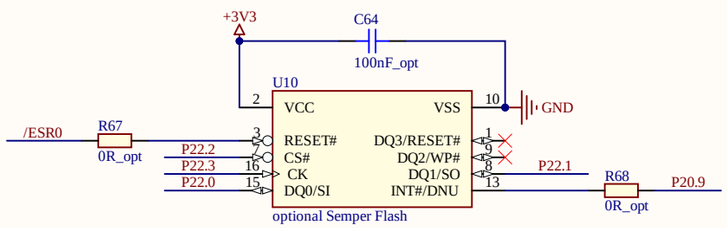
I2C Eeprom with unique MAC ID



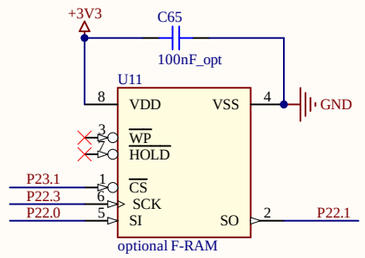
10/100 Base-T Ethernet



External serial flash



External serial ram



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Author: H.D.			
Date: 16.10.2020 Time: 13:19:44	SVN Revision: Not in version control		
	Sheet 5 of 5		