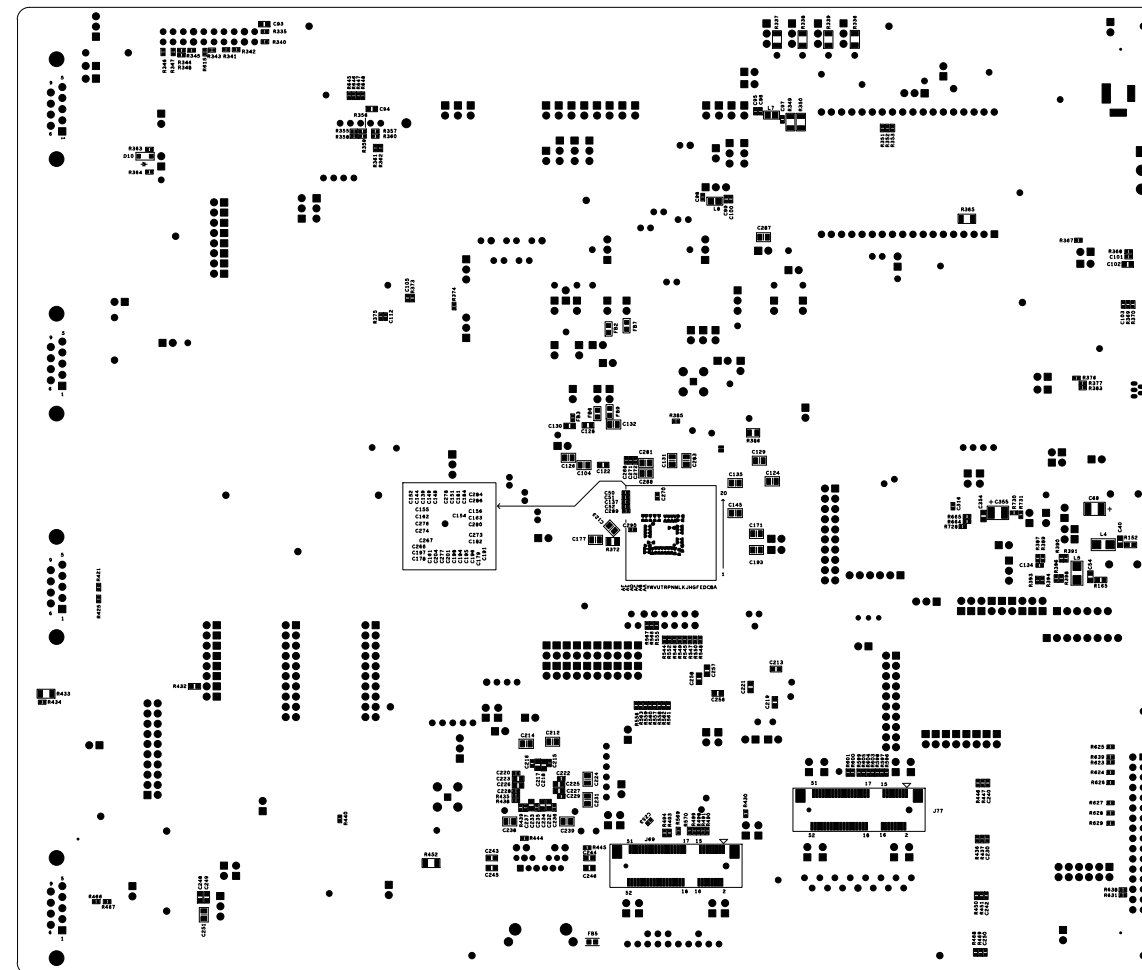




REVISIONS			
S.No	DESCRIPTION	DATE	APPROVED
01	INITIAL RELEASE	14/01/20	SPPD
01	REV-A	05/04/20	SPPD
02	REV-B	12/06/21	AVEN
03	REV-01	01/09/21	AVEN
04	REV-02	18/08/23	MERWIN



600-60614-01-TVII-C-2D-6M-327-BGA-CPU-REV-02

600-60614-01-TVII-C-2D-6M-327-BGA-SET-REV-02

SECONDARY ASSEMBLY

UNLESS OTHERWISE SPECIFIED		SIGNATURES		DATE			
DIMENSIONS ARE IN INCHES TOLERANCES ON: ANGLES +/- .2 2 PL DECIMALS +/- .010 3 PL DECIMALS +/- .005 THIRD ANGLE PROJECTIONS  CYPRESS PROPRIETARY THIS DOCUMENT CONTAINS CONFIDENTIAL, PROPRIETARY INFORMATION THAT IS CYPRESS SEMICONDUCTOR PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY CYPRESS.		DRAWN RASB		18/08/23			
		CHECKED MERWIN		18/08/23			
		ENGRG					
		ISSUED				TVII - C - 2D - 6M - 327 - BGA CPU-BOARD_REV-02	
				SIZE	FSCM NO	DWG NO	REV
				C		620-60614-01	02
				SCALE: 1/1			SHEET: