

ASYNCHRONOUS MOD 5 COUNTER

CET1044B

S.Y.B.Tech

A.Y. : 2022_23

Semester : III

DECA CCA COMPONENT II
GROUP PRESENTATION ON DESIGN

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INTRODUCTION & INSIGHTS

AVIPSA GHORAI

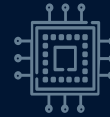
PROBLEM STATEMENT



A **4D movie Theater** has a capacity of **5 seats**.



Design a **digital system** to be **installed at entry** of the theater for **counting the entrants**.



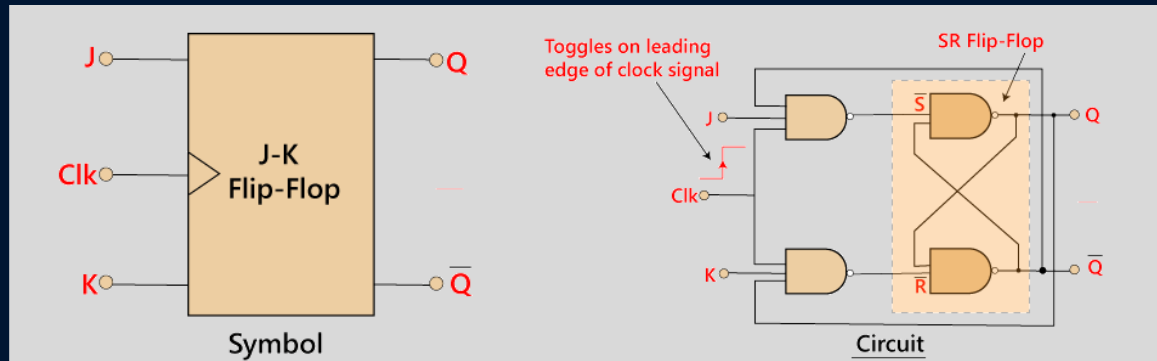
Give the indication when the theater is full.
Comment on the **advantages** and **limitations** of the circuit designed.

Mention **any other solution** available for the same problem.



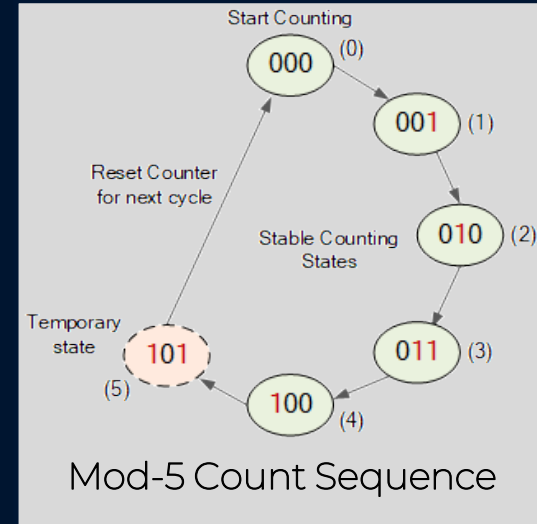
FLIP FLOPS

- A flip-flop is a binary storage device having two stable states —HIGH or 1, and LOW or 0.
- A flip-flop has the property of maintaining the same state until and unless an input signal tells it to switch its state.
- Flip-flops are the fundamental building blocks of the digital system. In the sequential logical circuit, the flip-flop is the basic storage element.



COUNTERS

- A special type of sequential circuit used to count the pulse is known as a counter.
- It is a collection of flip flops where the clock signal is applied.
- The counter is one of the widest applications of the flip flop.
- Based on the clock pulse, the output of the counter contains a predefined state.



ASYNCHRONOUS COUNTERS

- The number of flip flops used in an asynchronous counter depends upon the number of states of the counter (ex: Mod 5, Mod 2 etc.). The number of output states of a counter is called “Modulus” or “MOD” of the counter.
- The maximum number of states a counter can have is 2^n , where n represents the number of flip flops used in the counter.
- For example, if we have 2 flip flops, the maximum number of outputs of the counter is 4 i.e. 2×2 . So it is called a “MOD-4 counter” or “Modulus 4 counter”.

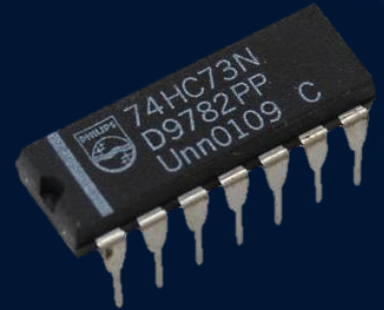


CIRCUIT DESIGNS AND IMPLEMENTATION

KRISHNARAJ THADESAR

DESIGN BRIEF

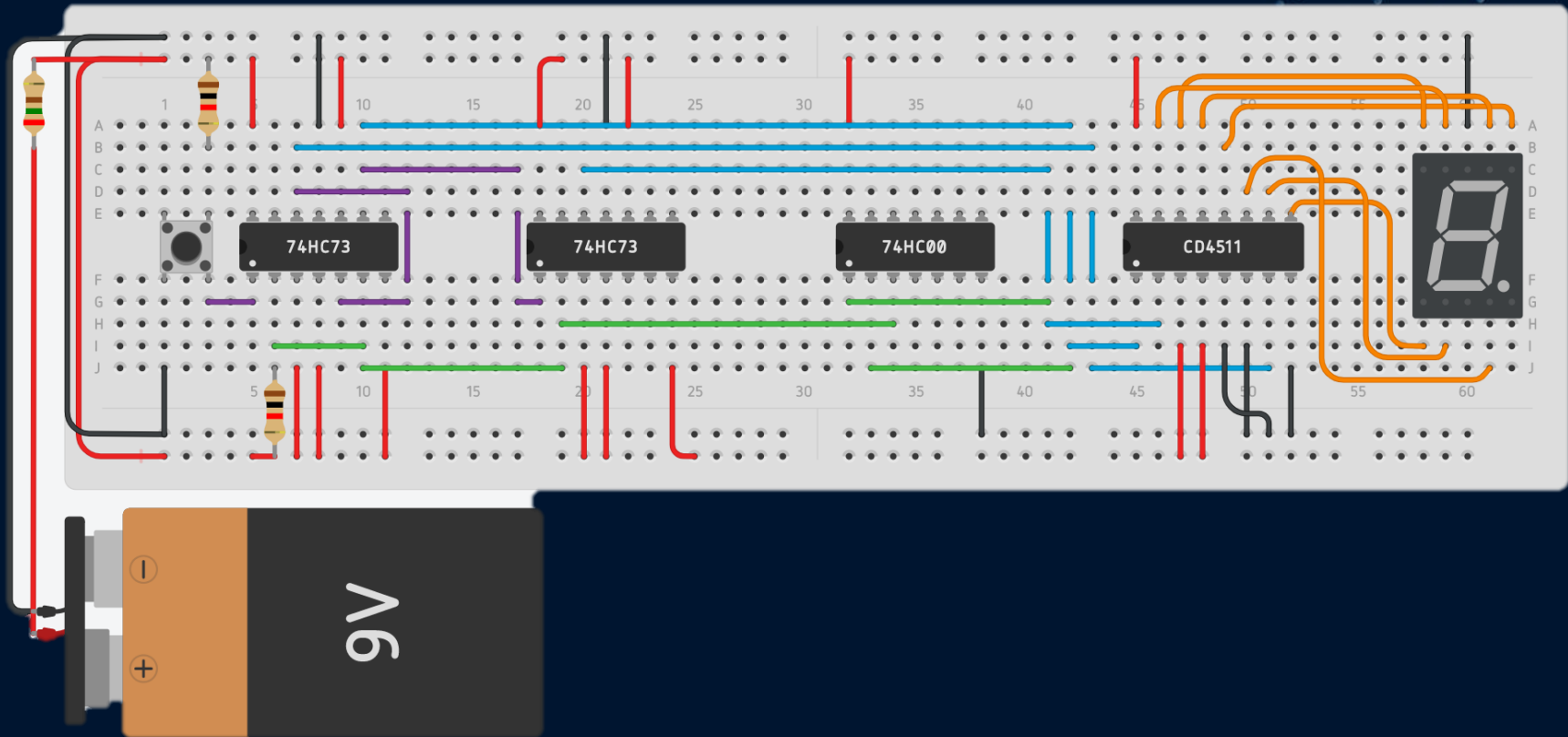
We have implemented a design which includes **Asynchronous Mod 5 Up Counter** using **IC7473** and a **BCD to 7 segment decoder and display**, to provide a solution to the given problem statement of **keeping count of the entrants** entering the 4D theatre.



IC 7473



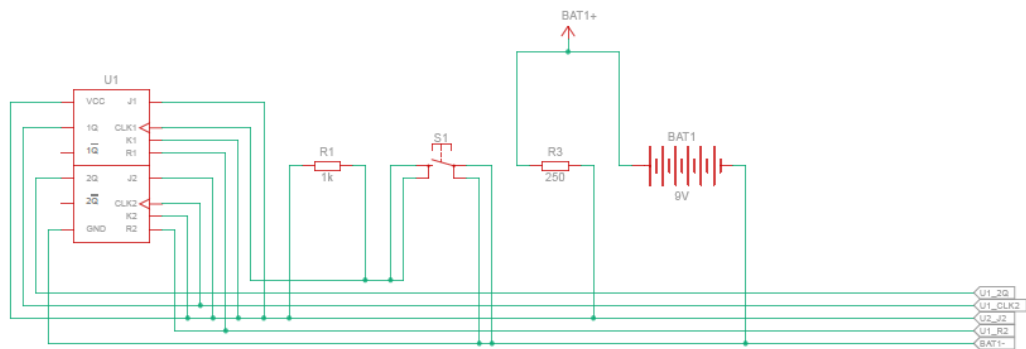
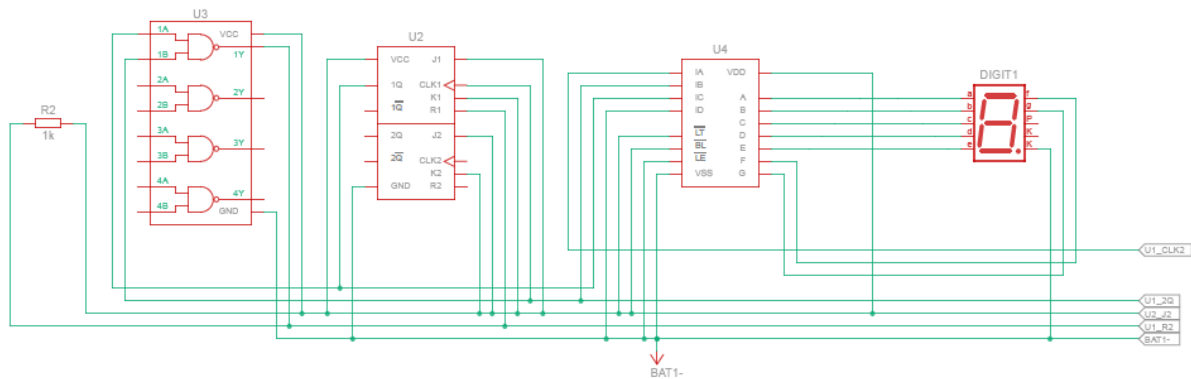
CIRCUIT IMPLEMENTATION ON TINKERCAD



COMPONENT LIST OF CIRCUIT

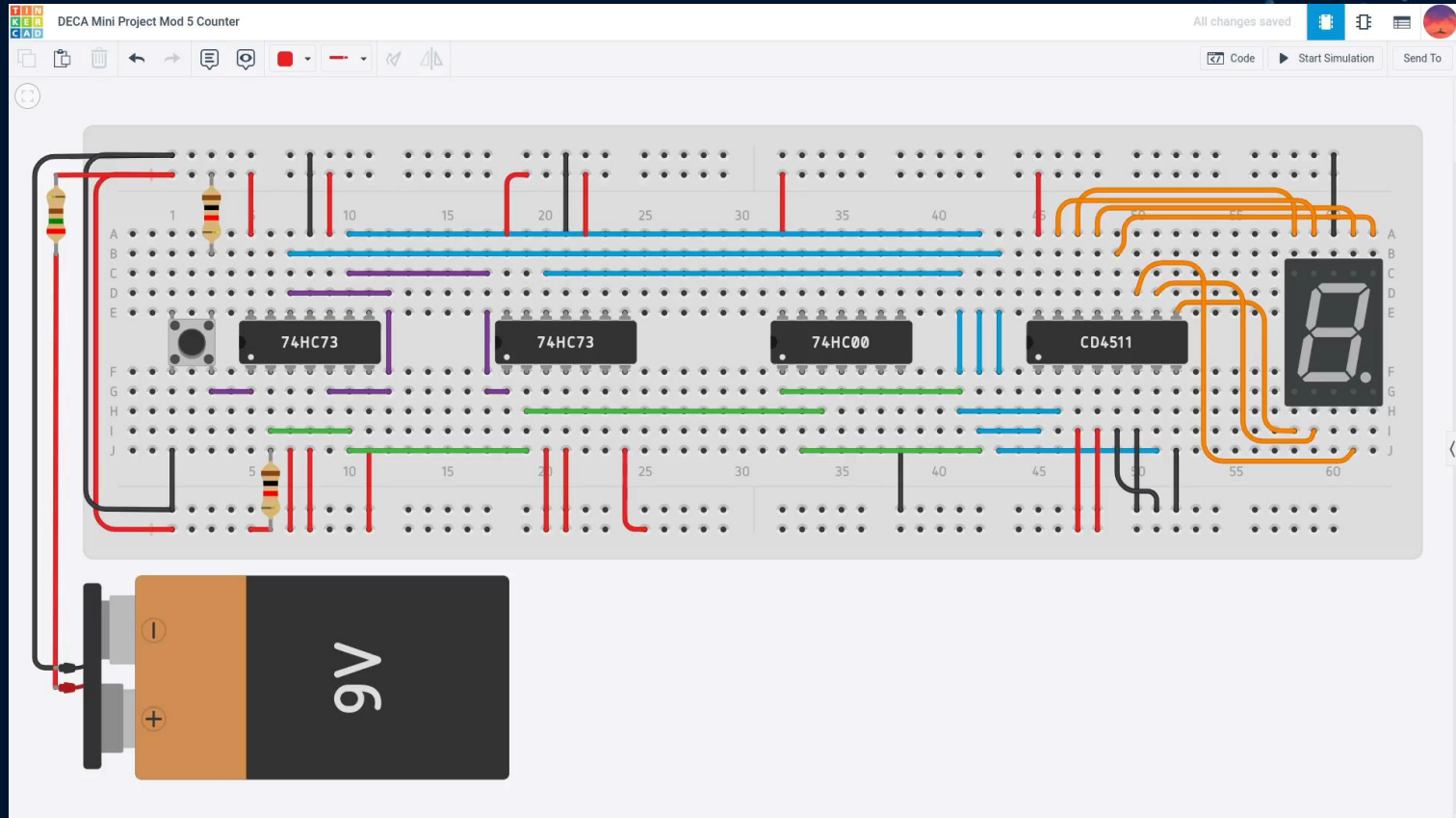
Name	Quantity	Component
BAT 1	1	9V Battery
Digit 1	1	Cathode 7 Segment Display
U1, U2	2	Dual J-K Flip-Flop
U3	1	Quad NAND gate
U4	1	7-Segment Decoder
S1	1	Pushbutton
R1, R2	2	1 k Ω Resistor
R3	1	250 Ω Resistor






SCHEMATIC VIEW

DEMONSTRATION OF THE CIRCUIT



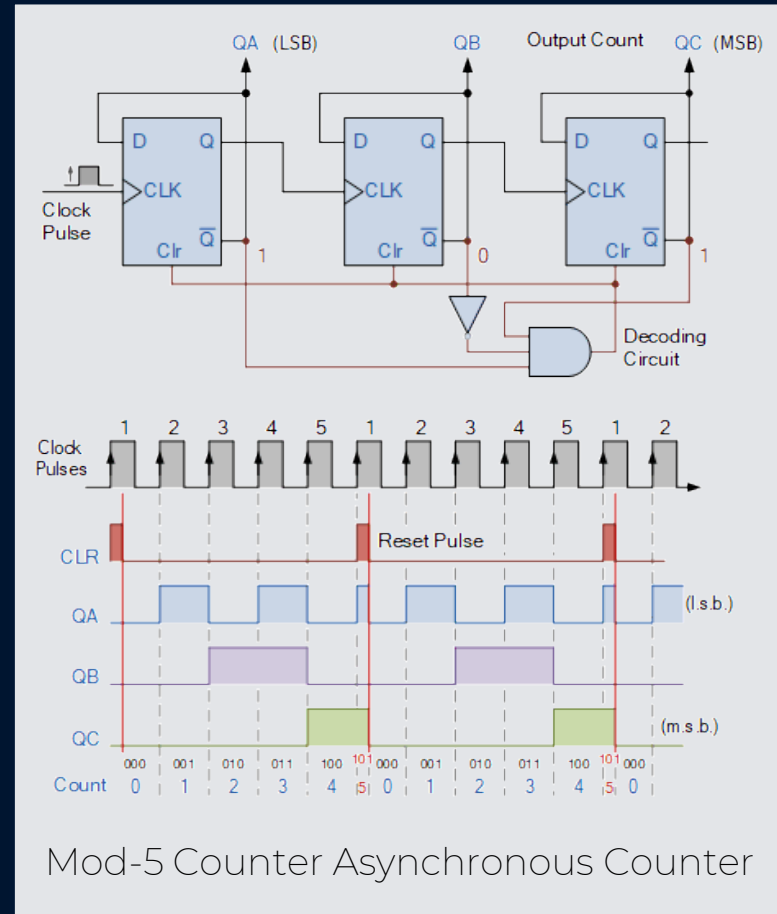


ADVANTAGES OF COMPONENTS AND CIRCUIT

SHIVRANJAN PATHAK

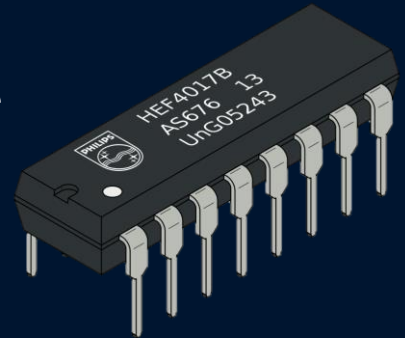
ASYNCHRONOUS COUNTERS

- If the flip-flops do not receive the same clock signal, then that counter is called an Asynchronous counter.
- The clock signal is applied only to the first flip-flop. The remaining flip-flops receive the clock signal from the output of its previous stage flip-flop.
- As the flip flops are supplied with different clock signals, the output may be delayed.
- Another name for Asynchronous counters is “Ripple counters”.



ADVANTAGES OF USING FLIP FLOPS

- Flip-Flops have a **clock** and a toggle input. When a clock is activated, the value of Flip-Flops is inverted.
- The main advantage of flip flop is that it has a circuit inside it which **contains gates** and can generate specific output, it makes a complex circuit much simpler and easier to make and to understand.
- It can perform the functions of the **set/reset** flip flop and has the advantage that there are no ambiguous **states**.



ADVANTAGES OF USING AN ASYNCHRONOUS COUNTER

- Asynchronous counters can be easily designed by T flip flop or D flip flop.
- These are also called as **Ripple counters**, and are used in **low speed circuits**.
- They are used as **Divide by- n counters**, which divide the input by n, where n is an integer.
- Asynchronous counters are also used as **Truncated counters**. These can be used to design any mod number counters, i.e. **even Mod** (ex: mod 4) or like in our case an **odd Mod** (ex: mod 5)

LIMITATIONS OF COMPONENTS AND CIRCUIT

VAISHNAVI POWER

LIMITATIONS

- If there are **multiple doors** for the same room, the project becomes quite complex.
- If one component in a **series circuit** fails, then all the components in the circuit fail because the circuit has been broken.
- The **propagation delay** of asynchronous counters is very large, while counting large number of bits. Due to this delay, counting errors may occur for high clock frequencies.
- We cannot increment multiple counts of people entering at the same time.



ALTERNATIVE SOLUTIONS TO OUR PROBLEM

PRATYUSH CHOWDHURY

ALTERNATIVE SOLUTIONS

- Cameras on the cutting edge have impressive on-board computer vision. They can generate a ton of information about the areas they're deployed in.
- So by installing a smart infrared camera or a thermal camera, we can keep track of people currently inside the theatre. This information can be relayed through a network to a display outside theatre showing the currently occupied seats.
- Another Solution is to put sensors below or inside the seat which detect motion and transmit count data to display outside the theatre.



Optical Vision Sensor



Seat Occupancy Sensor

CONTRIBUTION DETAILS

- PA-18 Avipsa Ghorai – Introduction to Flip Flops, Counters & Asynchronous Counters
- PA-16 Shivranjan Pathak - Advantages of FFs, Asynchronous Counters
- PA-19 Vaishnavi Powar - Limitations of the implemented design
- PA-20 Krishnaraj Prashant Thadesar – Tinkercad Circuits, Demonstration of circuit.
- PA-17 Pratyush Chowdhury - Other Solutions, conclusion & PowerPoint Presentation

BIBLIOGRAPHY

- <https://www.tinkercad.com/>
- <https://www.electronicshub.org/asynchronous-counter/>
- https://www.tutorialspoint.com/digital_circuits/digital_circuits_counters.htm#:~:text=Synchronous%20counters-.Asynchronous%20Counters,its%20previous%20stage%20flip%20flop
- <https://www.daenotes.com/electronics/digital-electronics/flip-flops-types-applications-working>
- <https://www.electronics-tutorials.ws/counter/mod-counters.html>

THANKS!

Do you have any
questions?

