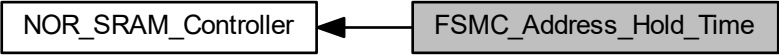


NOR_SRAM_Controller

FSMC_Address_Hold_Time



```
graph LR; A[FSMC_Address_Hold_Time] --> B[NOR_SRAM_Controller]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The box on the left is white with a black border and contains the text 'NOR_SRAM_Controller'. The box on the right is light gray with a black border and contains the text 'FSMC_Address_Hold_Time'. A black arrow points from the right side of the gray box to the left side of the white box, indicating a dependency or data flow from the parameter to the controller.