

NAND_PCCARD_Controller

FSMC_TAR_Setup_Time



```
graph RL; A[FSMC_TAR_Setup_Time] --> B[NAND_PCCARD_Controller];
```

The diagram consists of two rectangular boxes connected by a horizontal arrow pointing from right to left. The box on the right is shaded gray and contains the text 'FSMC_TAR_Setup_Time'. The box on the left is white with a black border and contains the text 'NAND_PCCARD_Controller'. A black arrow points from the right box to the left box, indicating a dependency or relationship between the two components.