

TIM_Exported_Types



```
graph LR; A[TIM_Output_Compare_and_PWM_modes] --> B[TIM_Exported_Types]
```

A diagram showing a dependency. On the right is a gray rectangular box containing the text "TIM_Output_Compare_and_PWM_modes". A horizontal arrow points from the left side of this box to the right side of a white rectangular box on the left, which contains the text "TIM_Exported_Types".

TIM_Output_Compare
_and_PWM_modes