Computer Architecture
HW#2 Single-cycle MIPS

Dept. of Computer Engineering

Jaehyun Nam

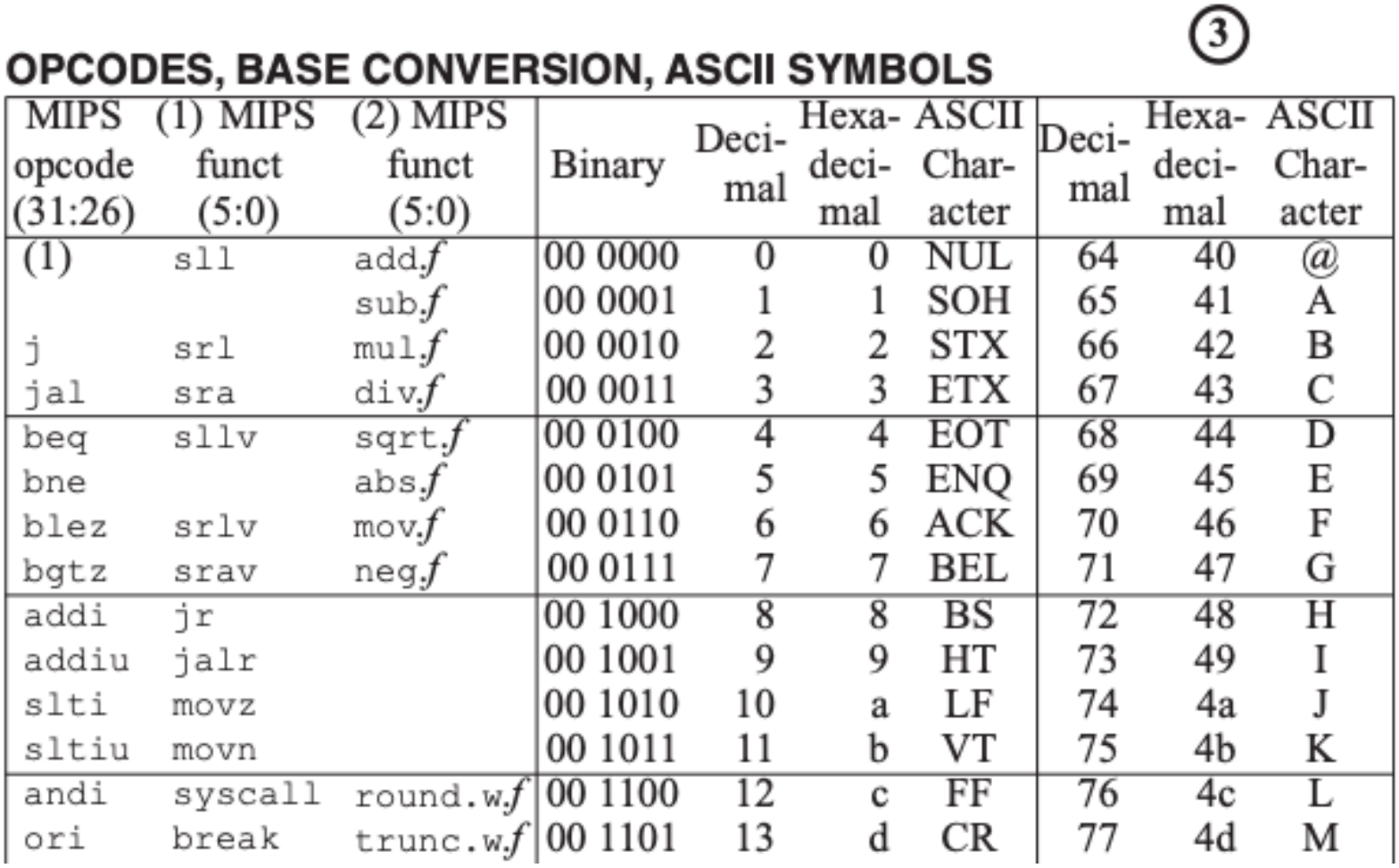
jaehyun.nam@dankook.ac.kr

Single-cycle MIPS Emulator

- Make a MIPS CPU emulator
- Execute MIPS Instructions (except for floating point operations)
- Assume that you have memory and a program, which is loaded in memory before execution
- Take MIPS a binary program
- Display the state changes at every clock cycle
- Handle exception gracefully

Green Sheet

- https://inst.eecs.berkeley.edu/~cs61c/resources/MIPS_Green_Sheet.pdf



③

OPCODES, BASE CONVERSION, ASCII SYMBOLS

MIPS opcode (31:26)	(1) MIPS funct (5:0)	(2) MIPS funct (5:0)	Binary	Deci-mal	Hexa-deci-mal	ASCII Char-acter	Deci-mal	Hexa-deci-mal	ASCII Char-acter
(1)	sll	add.*f*	00 0000	0	0	NUL	64	40	@
		sub.*f*	00 0001	1	1	SOH	65	41	A
j	srl	mul.*f*	00 0010	2	2	STX	66	42	B
jal	sra	div.*f*	00 0011	3	3	ETX	67	43	C
beq	sllv	sqrt.*f*	00 0100	4	4	EOT	68	44	D
bne		abs.*f*	00 0101	5	5	ENQ	69	45	E
blez	srlv	mov.*f*	00 0110	6	6	ACK	70	46	F
bgtz	srav	neg.*f*	00 0111	7	7	BEL	71	47	G
addi	jr		00 1000	8	8	BS	72	48	H
addiu	jalr		00 1001	9	9	HT	73	49	I
slti	movz		00 1010	10	a	LF	74	4a	J
sltiu	movn		00 1011	11	b	VT	75	4b	K
andi	syscall	round.w.*f*	00 1100	12	c	FF	76	4c	L
ori	break	trunc.w.*f*	00 1101	13	d	CR	77	4d	M

Requirements

- Before the execution, the binary file is loaded into the memory
  - Note that the memory can be a data structure defined with a large array

- Read all the file content into your memory (data structure)
- Assume that all register values are all zero, except for
  - RA(r31) and SP(r29), of which value is 0xFFFFFFFF and 0x1000000

- When your PC becomes 0xFFFFFFFF, your machine completes execution
- Your application is loaded to 0x0, and stack pointer is 0x1000000

- Need to fix the jump address of each function call in manual

Requirements

- MIPS executes instructions in the following stages:
  - Instruction fetch
    - Instruction is moved from memory to CPU
  - Instruction decode
    - Instruction is decoded
  - Execution
    - ALU operates, and the calculation result is out
  - Load/store result to memory
    - load/store memory operation is completed
  - Write back to reg. file
    - Update register values (including PC)

Requirements

- Single-cycle MPS processor performs all five stages in the same cycle
- The emulator must implement functions that correspond to each stage
- To begin the machine, it moves PC value to the very beginning point of the program, which is the address zero
- At the end of each cycle, the emulator prints out all of the changed states from the previous ones
  - A set of general registers, PC, and memory
  - Print out only changed states

Programming Language

- **C** is highly recommended since we need **C** for the following assignments
- But **Java** and **Python** are also okay in this assignment

Document

- Introduction
  - Brief description of the assignment
- Background
  - Important concept, specific considerations for implementation
- Implementation
  - How you organized your program (design)
  - What parts you implemented and what parts you didn't (couldn't)
    - Including the implementation for extra points
  - Explain why
- Environment
  - How to build the development environment for testing (+ screenshots)
    - Should be specific
    - If I can't create an environment by doing what you've described, I won't grade your code
  - How to compile and run your program
    - Should be specific
  - Screenshots working proofs with explanation
- Lesson
  - What was hard, What you thought while doing the assignment, etc.

E-mail Submission

- Send your assignment to jaehyun.nam@dankook.ac.kr

- E-mail title
  - [2023-1 컴퓨터구조] HW2 [StudentID] [Name]
  - Ex) [2023-1 컴퓨터구조] HW2 12345678 남재현

- E-mail body
  - **YOU MUST CHANGE THE NAMES OF YOUR CODE AND DOCUMENT AS FOLLOWS**
  - **IF NOT, YOU WILL GET A PENALTY**
  - Source code
    - **HW2_학번_이름_code.zip**
    - Ex) HW2_12345678_남재현_code.zip
  - Document
    - **HW2_학번_이름_document.pdf**
    - Ex) HW2_12345678_남재현_document.pdf

Due Date

- 2:30PM on June 2nd, 2023
  - Right before class

Appendix

Dept. of Computer Engineering

Jaehyun Nam

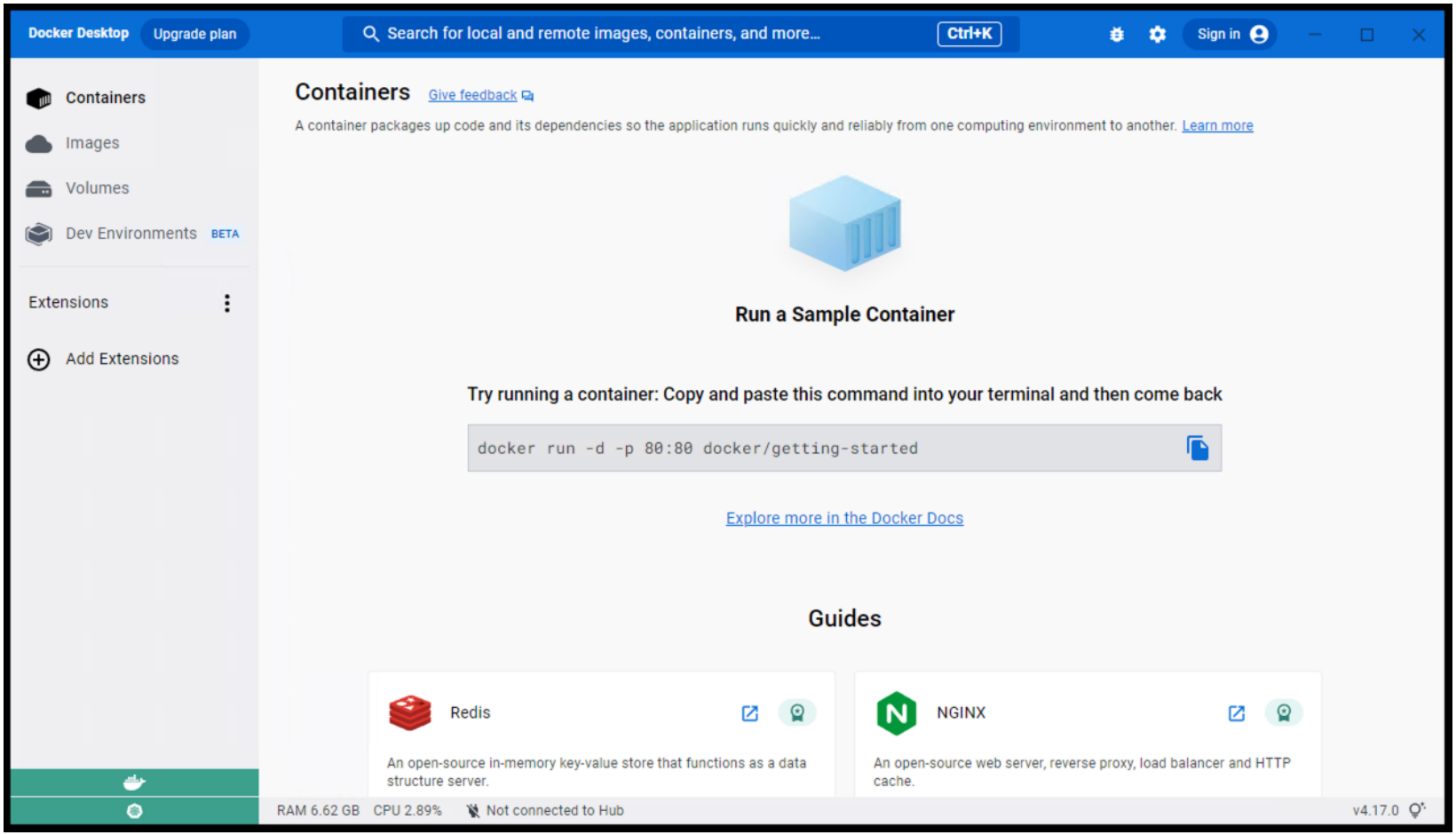
jaehyun.nam@dankook.ac.kr

MIPS Binary

- To make MIPS binary, you need to use MIPS cross-compiler toolchain
  - sudo apt-get update
  - sudo apt-get -y install gcc-mips-linux-gnu
- First, write C code (input.c)
- Compile the code with mips-gcc (compile only)
  - mips-linux-gnu-gcc -c input.c -mips1 -mfp32
- Translate the object binary, stripping ELF headers
  - mips-linux-gnu-objcopy -O binary -j .text input.o input.bin
  - mips-linux-gnu-objdump -d input.o
  - hexdump input.bin

Docker Environment for MIPS Compilation

- Docker Desktop
  - https://www.docker.com/products/docker-desktop/



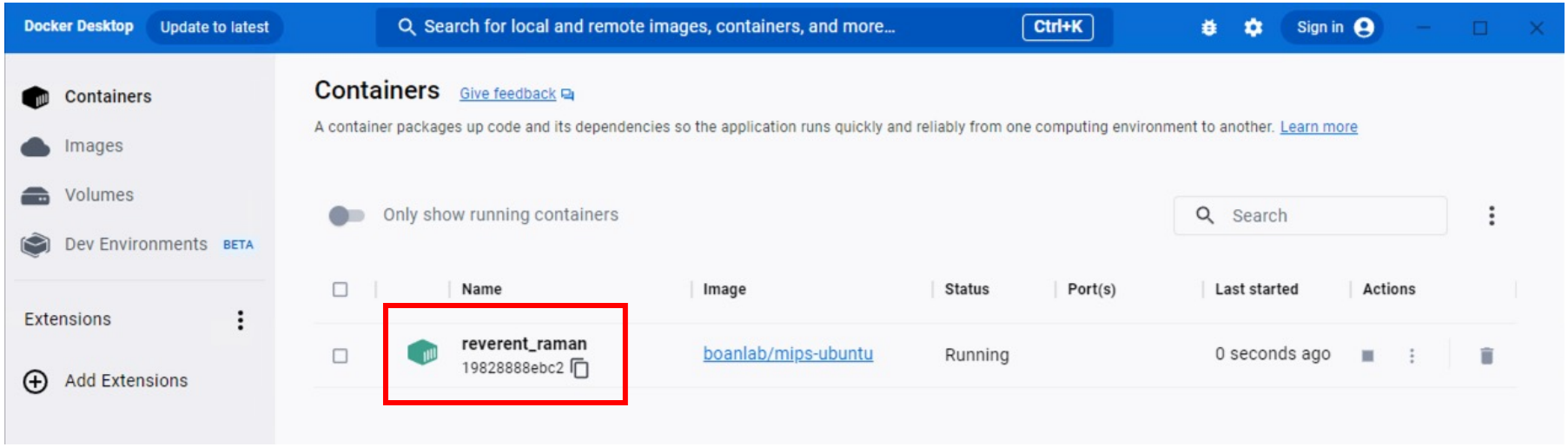
Docker Environment for MIPS Compilation

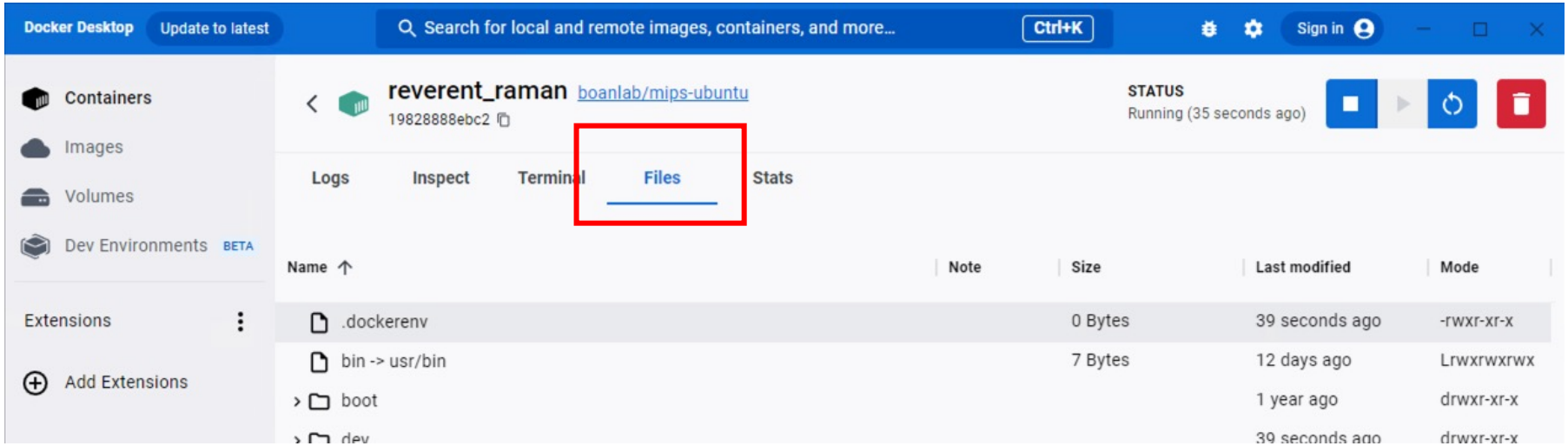
- PowerShell (or some other terminal)
  - docker run -it --rm boanlab/mips-ubuntu

```
PS C:₩Users₩jn> docker run -it --rm boanlab/mips-ubuntu
Unable to find image 'boanlab/mips-ubuntu:latest' locally
latest: Pulling from boanlab/mips-ubuntu
dbf6a9befcde: Pull complete
2f8028e30bc0: Pull complete
e7577e005956: Pull complete
Digest: sha256:9e781aad0a0a9695aadcb28817aab5fcae5b8e0f57d150e89f42e8deaa958a0d
Status: Downloaded newer image for boanlab/mips-ubuntu:latest
root@fb24150baa75:/#
```

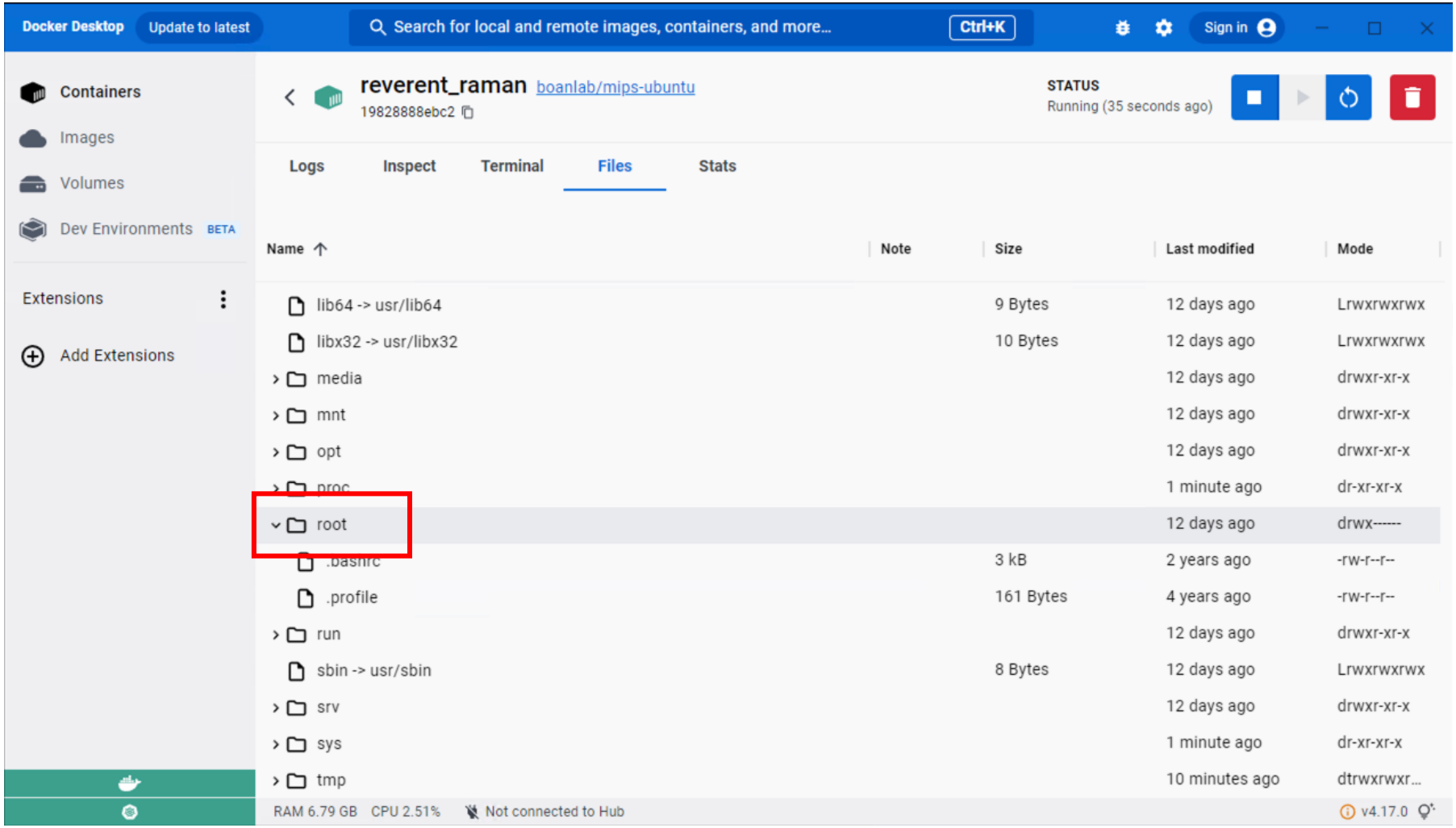
Docker Environment for MIPS Compilation

- 생성된 컨테이너 선택

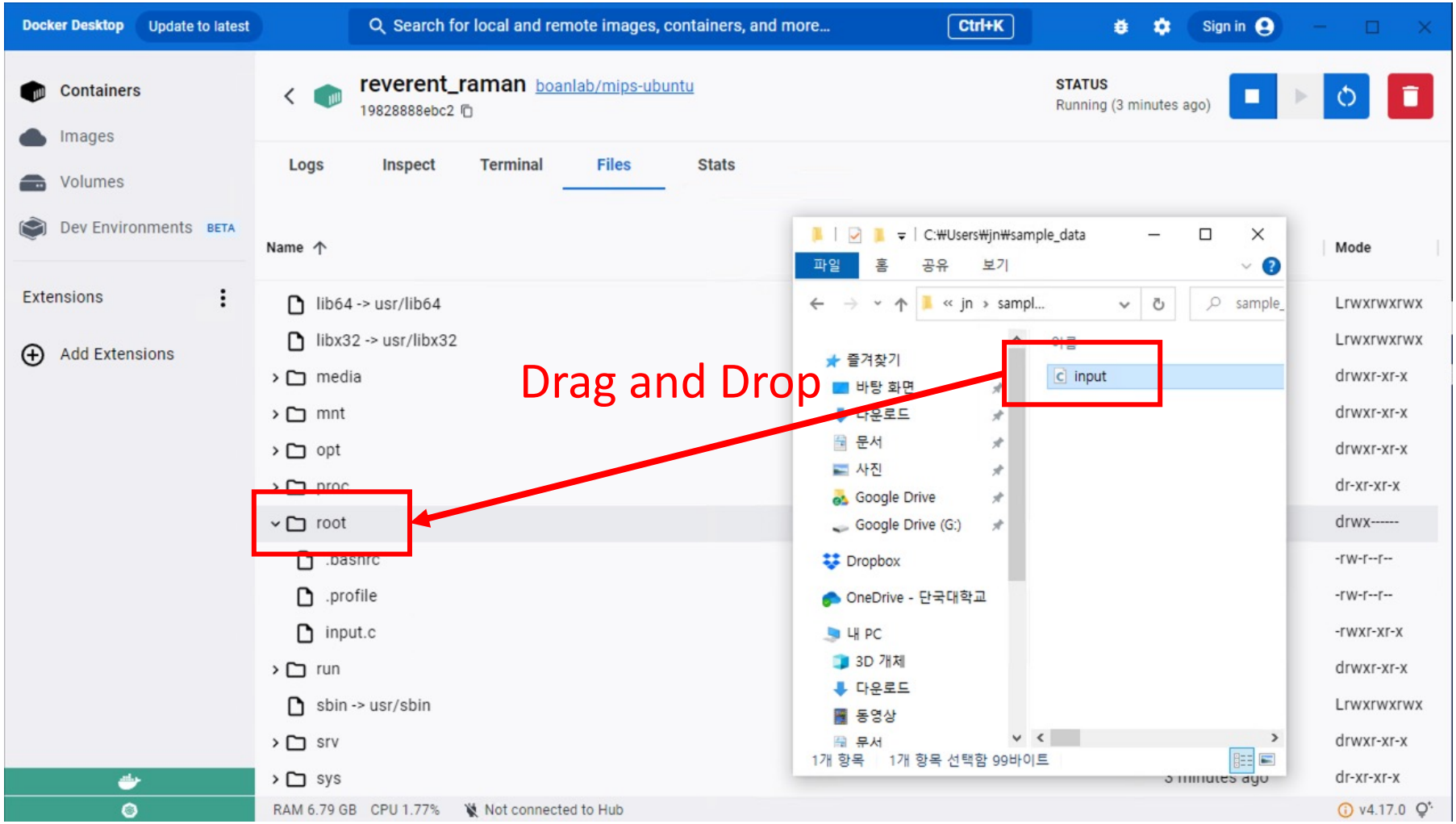




Docker Environment for MIPS Compilation



Docker Environment for MIPS Compilation

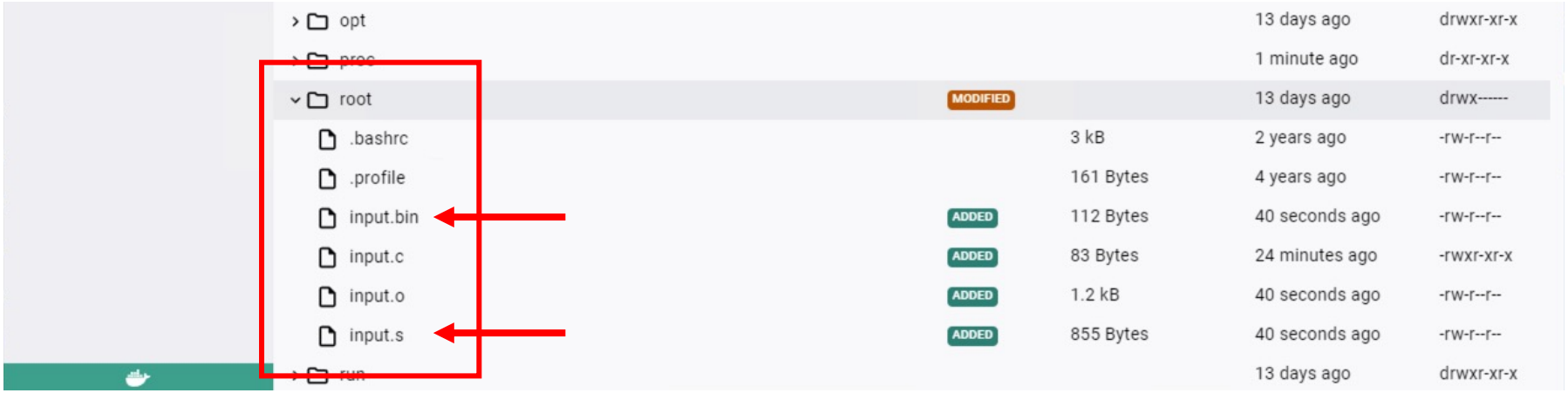


Docker Environment for MIPS Compilation

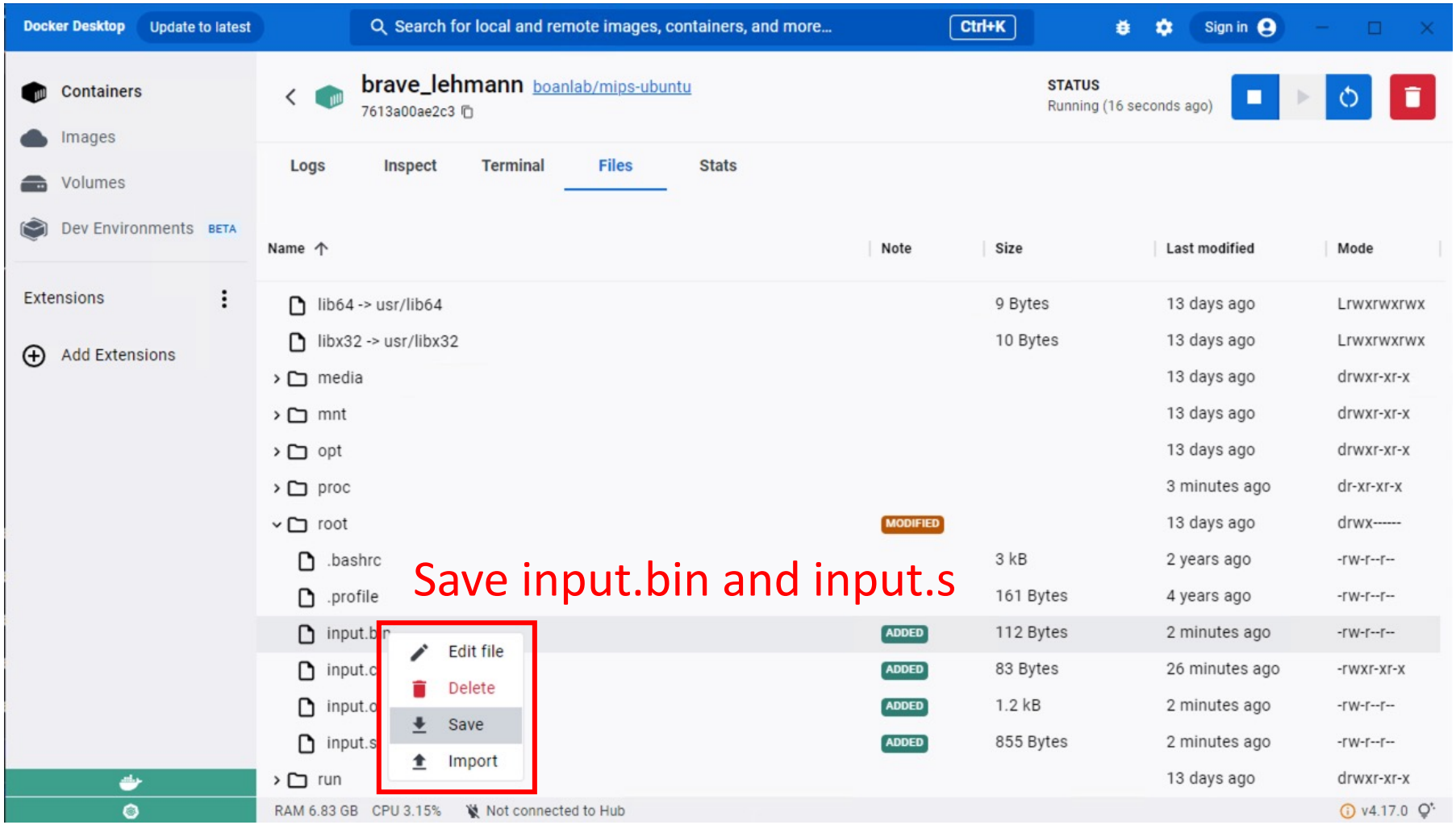
- mips-compile /root/input.c

```
PS C:¥Users¥jn> docker run -it --rm boanlab/mips-ubuntu
root@7613a00ae2c3:/#
root@7613a00ae2c3:/# mips-compile /root/input.c
root@7613a00ae2c3:/#
```

- input.bin / input.s

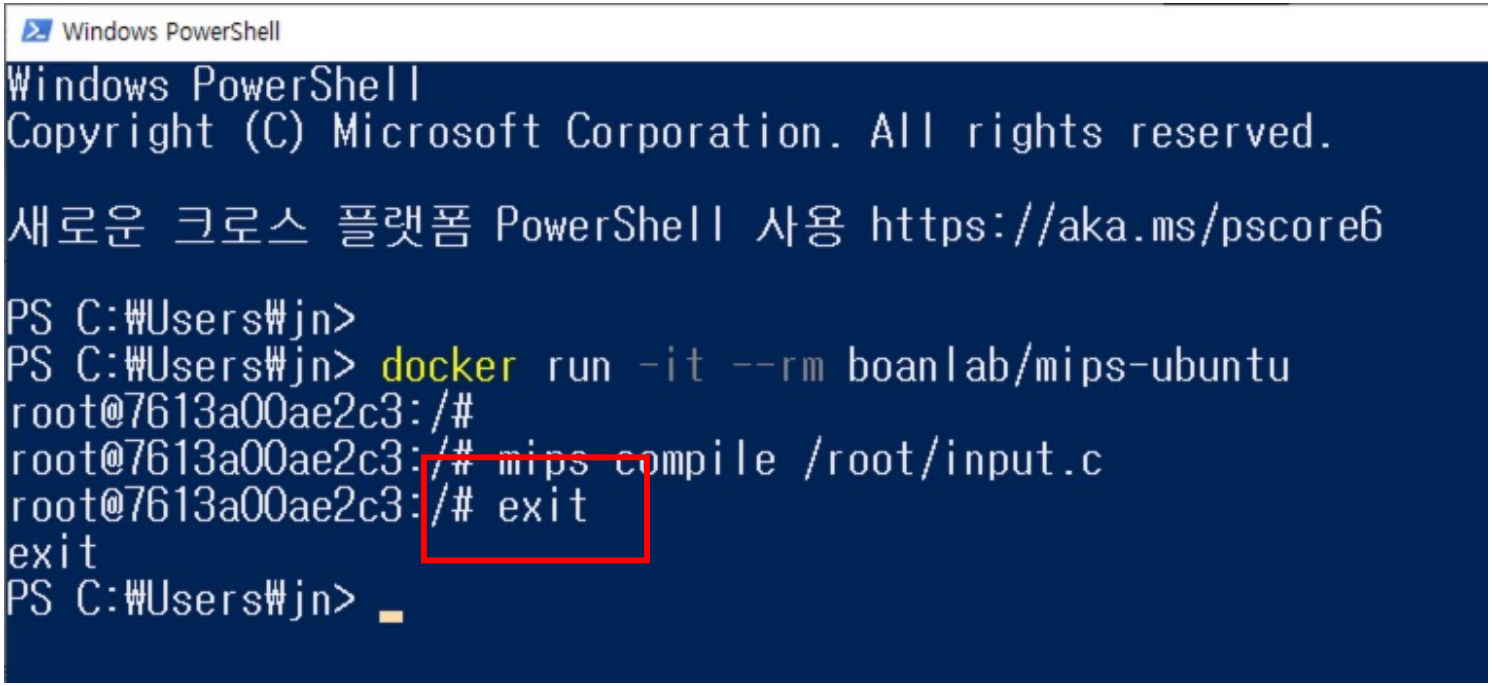


Docker Environment for MIPS Compilation



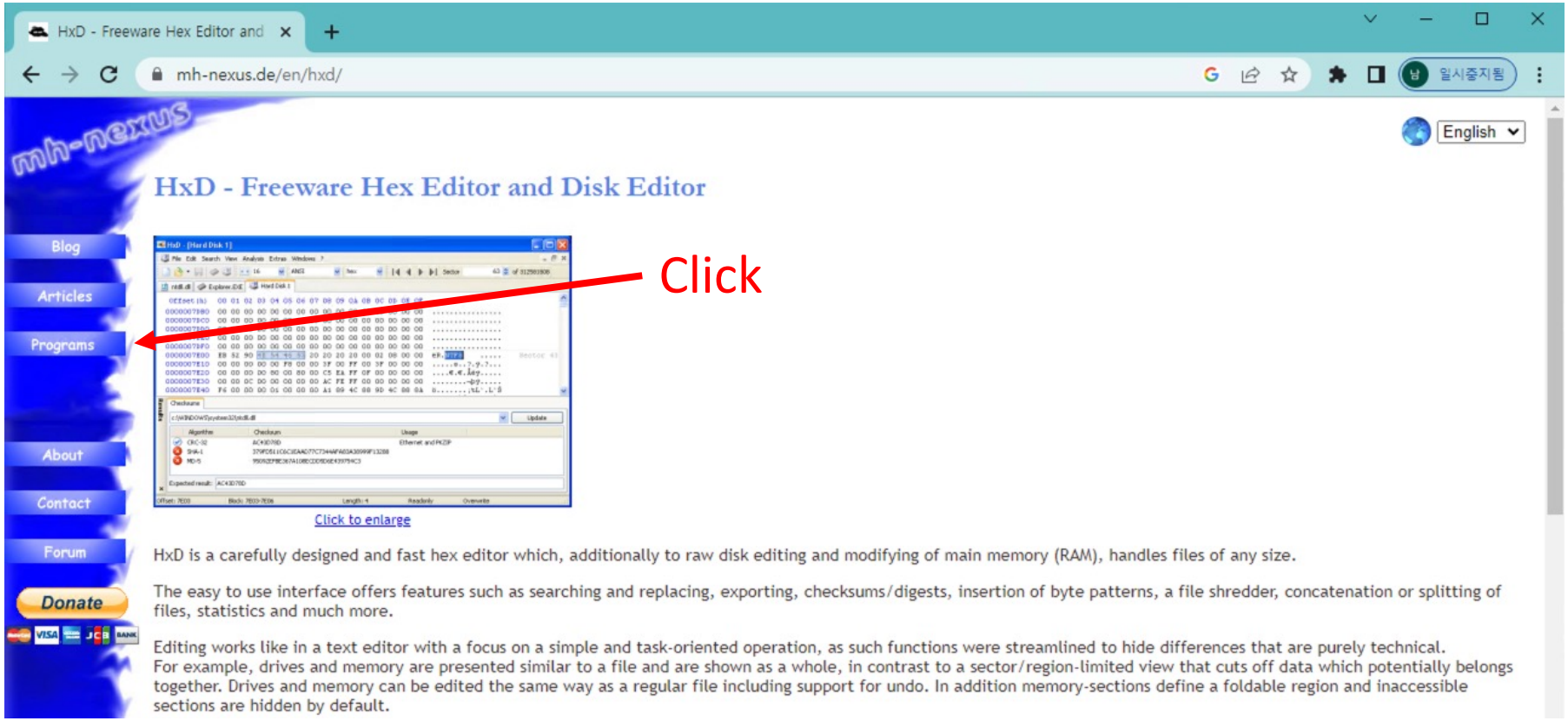
Docker Environment for MIPS Compilation

- exit



Fix Jump Address

- Install HxD



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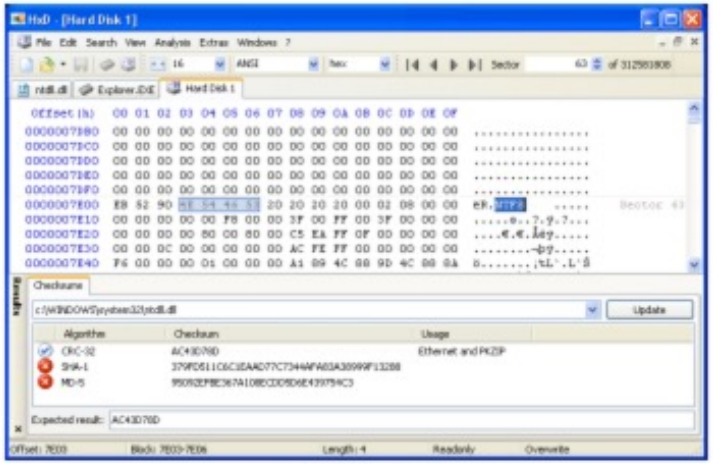
HxD

HxD is a carefully designed and fast hex editor which, additionally to raw disk editing and modifying of main memory (RAM), handles files of any size.

The easy to use interface offers features such as searching and replacing, exporting, checksums/digests, insertion of byte patterns, a file shredder, concatenation or splitting of files, statistics and much more.

Read more...



Click to enlarge

Click

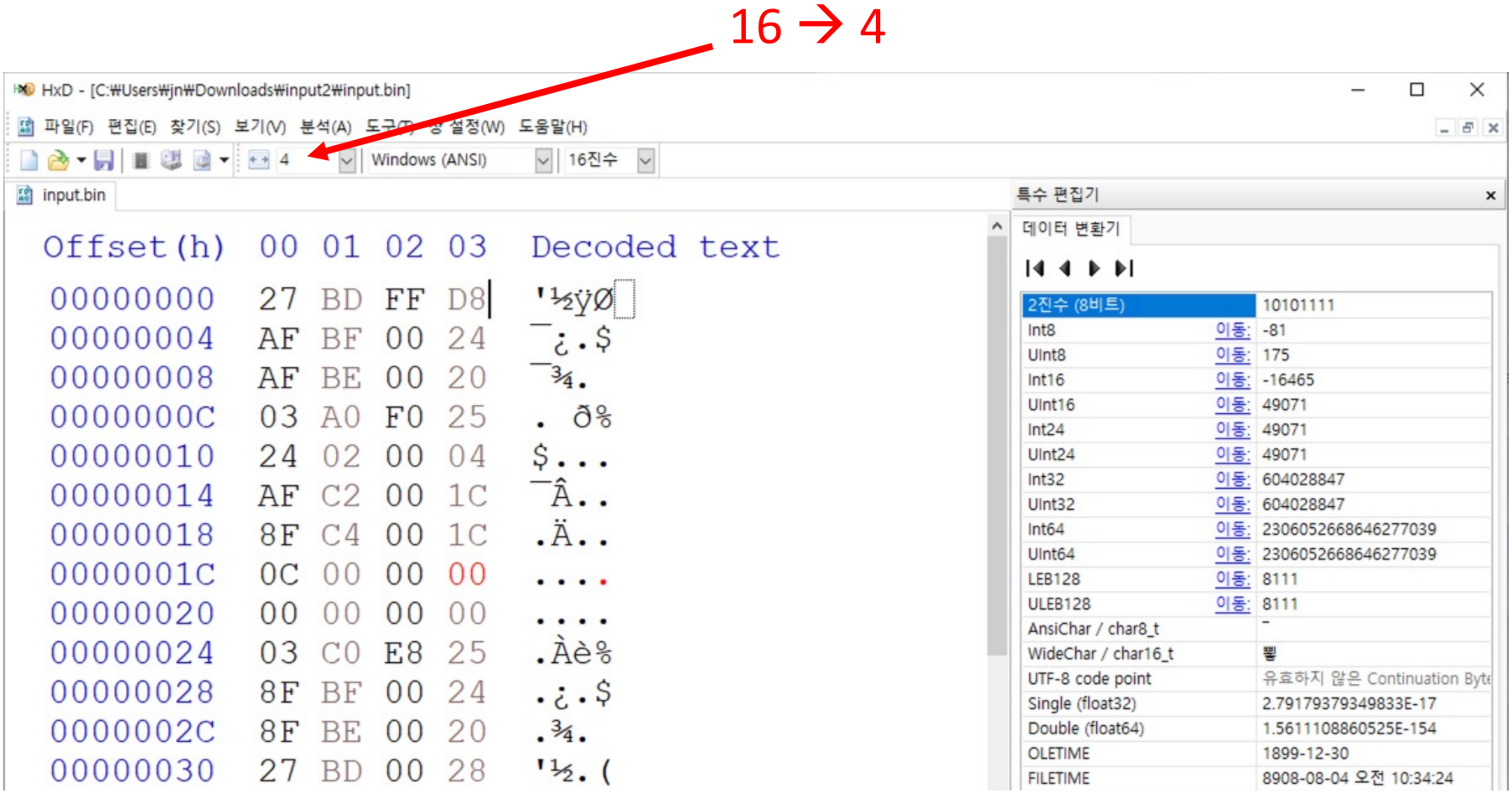
Version	2.5.0.0 (February 11, 2021)	What's new?
OS	Windows XP, 2003, Vista, 7, 8 or 10	
	Download page	

Fix Jump Address

- Download and extract a Zip file → Run the HxD installer

Fix Jump Address

- Run HxD and open input.bin



Fix Jump Address

- Open input.s

input - Windows 메모장

파일(F) 편집(E) 서식(O) 보기(V) 도움말(H)

```
00000000 <main>:
   0:   27bdffd8 addiu   sp,sp,-40
   4:   afbf0024 sw      ra,36(sp)
   8:   afbe0020         sw      s8,32(sp)
   c:   03a0f025         move    s8,sp
  10:   24020004         li      v0,4
  14:   afc2001c sw      v0,28(s8)
  18:   8fc4001c lw      a0,28(s8)
  1c:   0c000000         jal     0 <main>
  20:   00000000         nop
  24:   03c0e825         move    sp,s8
  28:   8fbf0024 lw      ra,36(sp)

0000003c <foo>:
  3c:   27bdffe0 addiu   sp,sp,-32
  40:   afbf001c sw      ra,28(sp)
```

input - Windows 메모장

파일(F) 편집(E) 서식(O) 보기(V) 도움말(H)

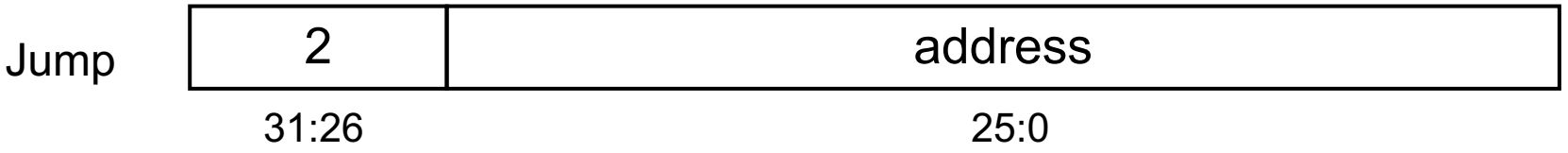
```
int foo(int index);

int main() {
    int index = 4;
    return foo(index);
}

int foo(int index) {
    if (index == 1)
        return 1;
    else
        return index + foo(index-1);
}
```

Should point 0x3c instead of 0x0

(Review) Implementing Jumps



- Jump uses word address
- Update PC with concatenation of
  - Top 4 bits of old PC
  - 26-bit jump address
  - 00

- Need an extra control signal decoded from opcode

Fix Jump Address

- Address for Jump = (0x3c >> 2) = 0xF