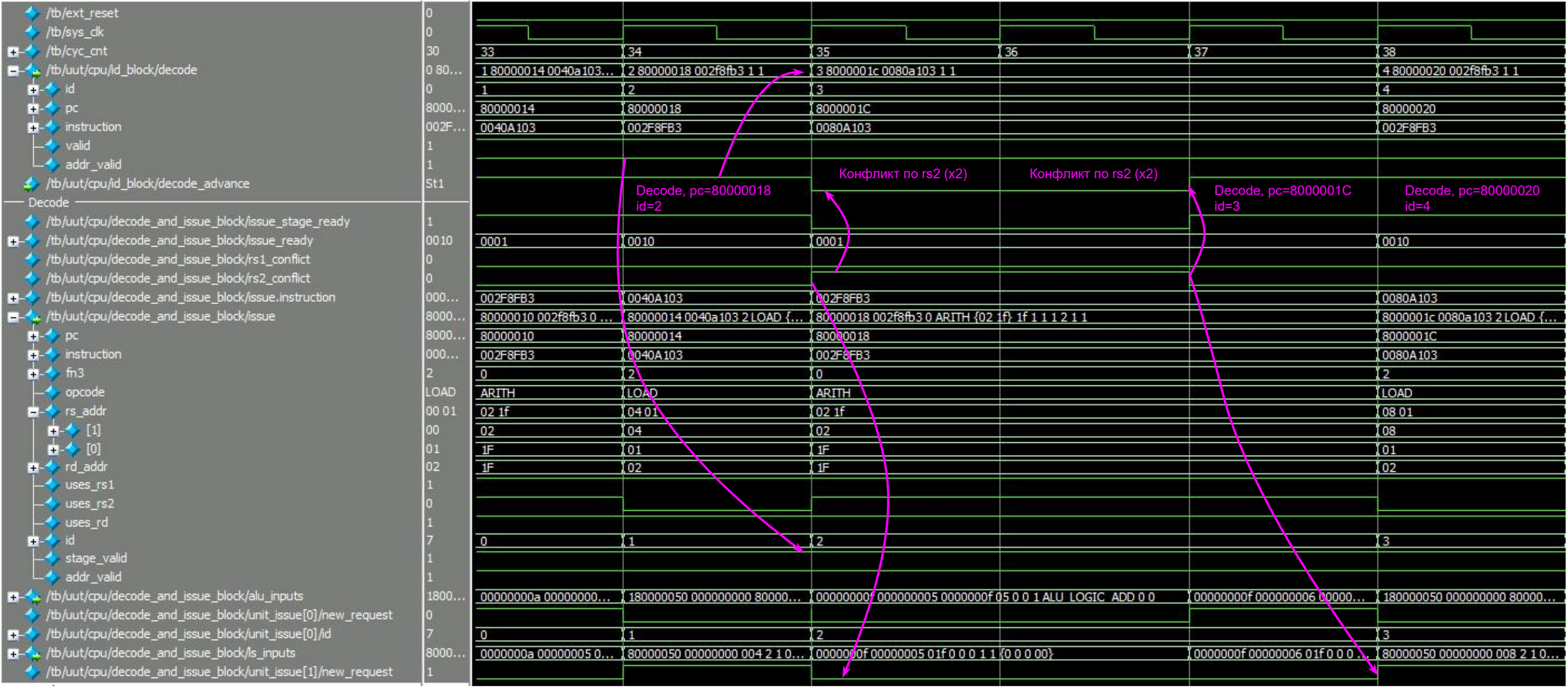
/tb/ext_reset
/tb/sys_clk
/tb/cyc_cnt
/tb/uut/cpu/id_block/decode
id
pc
instruction
valid
addr_valid
/tb/uut/cpu/id_block/decode_advance
Decode
/tb/uut/cpu/decode_and_issue_block/issue_stage_ready
/tb/uut/cpu/decode_and_issue_block/issue_ready
/tb/uut/cpu/decode_and_issue_block/rs1_conflict
/tb/uut/cpu/decode_and_issue_block/rs2_conflict
/tb/uut/cpu/decode_and_issue_block/issue.instruction
/tb/uut/cpu/decode_and_issue_block/issue
pc
instruction
fn3
opcode
rs_addr
[1]
[0]
rd_addr
uses_rs1
uses_rs2
uses_rd
id
stage_valid
addr_valid
/tb/uut/cpu/decode_and_issue_block/alu_inputs
/tb/uut/cpu/decode_and_issue_block/unit_issue[0]/new_request
/tb/uut/cpu/decode_and_issue_block/unit_issue[0]/id
/tb/uut/cpu/decode_and_issue_block/ls_inputs
/tb/uut/cpu/decode_and_issue_block/unit_issue[1]/new_request
Decode, pc=80000018 id=2
Конфликт по rs2 (x2)
Конфликт по rs2 (x2)
Decode, pc=8000001C id=3
Decode, pc=80000020 id=4